

250mA Low Dropout Voltage Regulators

ON/OFF & BYPASS

B4252

Description

The B4252 series are a group of positive output voltage, five-pin regulators, which provide a high current even when the input/output voltage differential is small. Low power consumption and high accuracy is achieved through CMOS and programmable fuse technologies. Output voltage: 2.0V to 6.0V in 0.1V increments.

The B4252 consists of a high-precision voltage reference, an error correction circuit, and a current limited output driver. Key features include a Shutdown Pin that reduces supply current to 0.5 μ A max and a 470pF Bypass input to ground reduces noise present on the internal reference, which in turn significantly reduce output noise. If output noise is not concern, this input maybe left unconnected. Larger capacitor values cap be used, but results in longer time period to rated output voltage when power initially applied.

The ON/OFF pin enables the output to be turned off, resulting in reduced power consumption. Provided in SOT-25 (150mW) packages minimizes the usage of board real state.

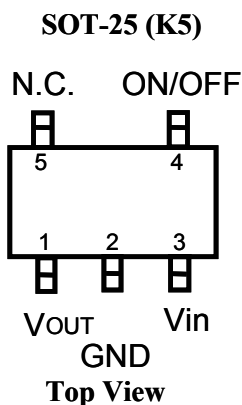
Features

- **Maximum Output Current: 250 mA.**
- **Highly Accurate Output Voltage +/- 1.4%**
- **Only 19 μ A Power Consumption MAX**
- **Shutdown Supply Current 1 μ A**
- **Ground Current of Less than 5 μ A**
- **Very Low Dropout**
0.12V at 100mA
0.23V at 250mA
- **Shutdown Mode for Power Savings**
- **With ON/OFF & BYPASS Features**
- **Offered in SOT-25 Packages**
- **PIN-to-PIN RICOH R1121N-E**

Applications

- **Battery Powered Equipment**
- **Notebook PC, Palmtops, PDA**
- **Portable Cameras and Video Recorders**
- **Reference Voltage Sources**
- **Cellular / GSM / PCS Phones**

Pin Connection



Ordering Information

Package	Part No.
SOT-25	B4252CK5-X.X
X.X = Fixed Output Voltages from 2.0V to 6.0V	

ON/OFF (Enable) Function

Part No	ON/Off	V _{OUT}
B4252	High	ON
	Low	OFF

Absolute Maximum Ratings

Parameter		Symbol	Ratings	Units
Input Voltage		V_{IN}	12	V
Output Current		I_{OUT}	500	mA
Output Voltage		V_{OUT}	$V_{SS} - 0.3$ to $V_{IN} 0.3$	V
On/Off Voltage		On/Off	$V_{SS} - 0.3$ to $V_{IN+} 0.3$	V
Continual Total Power Dissipation	SOT-25	Pd	150	mW
	SOT-89		300	
Operating Ambient Temperature		T_{OPR}	-30 to 80	°C
Storage Temperature		T_{STG}	-40 to 125	°C

Electrical Characteristics ($T_a = 25^\circ\text{C}$, $V_{IN} = V_{OUT} + 0.5\text{V}$; unless otherwise noted)

Parameter	Conditions	MIN	TYP	MAX	UNIT
Output Voltage Accuracy	$I_O = 1\text{mA}$	-1.4%	-	+1.4%	V
	$I_O = 0$ to 150mA	-2%	-	+2%	
Line Regulation $\Delta V_{OUT}/\Delta V_{IN} - V_{OUT}$	$I_O = 100\text{mA}$, $(V_{OUT} + 0.1\text{V}) < V_{IN} < 6\text{V}$		0.2	0.3	%/V
Load Regulation	$V_{IN} 6\text{V}$, $0\text{mA} < I_O < 120\text{mA}$ $C_{OUT} = 1\mu\text{F}$		0.01	0.04	%/mV
Maximum Output Current		250			mA
Current Limit		160	500		
Shutdown Supply Current	$T_A = 25^\circ\text{C}$		0.01	1	μA
Shutdown Input Bias Current	$V_{SHDN} = V_{IN}$, $T_A = 25^\circ\text{C}$		0.01	100	nA
Shutdown Exit Delay	$CBP = 0\mu\text{F}$ $C_{OUT} = 1\mu\text{F}$, $I_O = 1\text{mA}$		30	150	μsec
Shutdown Input Threshold Low	$V_{in} = 2.5$ to 5.5V		0.4		V
Shutdown Input Threshold High	$V_{in} = 2.5$ to 5.5V	2			V
Ground Pin Current	$I_{OUT} = 0\text{mA} \dots 150\text{mA}$		2	5	μA
Dropout Voltage	$I_O = 150\text{mA}$		160	250	mV
	$I_O = 250\text{mA}$		200	280	
Thermal Shutdown Temperature			155		°C

Marking Format**SOT-25**

FXWW

F = B4252

Variables**X = Voltage Codes WW = Work Week Code**

B = 2.0 F = 3.3 SOT-25 = 01 to 99

C = 2.5 G = 3.5

D = 2.8 H = 5.0

E = 3.0

PRODUCT DESCRIPTION

The B4252 is precision fixed output voltage regulator. Unlike bipolar regulators, the B4252 supply current does not increase with load current. In addition, V_{out} remains stable and within regulator at very low load currents (an important consideration in RTC and CMOS RAM battery back-up application).

Figure 1 shows a typical application circuit. The regulator is enabled any time the shutdown input is at or above V_{IH} . And shutdown (disabled) when SHDN is at or below V_{IL} . SHDN may be controlled by a CMOS logic gate, or I/O port of a micro controller. If the SHDN input is not. Required, it should be connected directly to the supply. While in shutdown, supply current decreases to 0.05 μ A (typical) and V_{out} falls to zero volts.

Bypass Input

A 470pF capacitor connected from the bypass input to ground reduces noise present on the internal reference, which in turn significantly reduces output noise. If output noise is not a concern, this input may be left unconnected. Larger capacitor values may be used, but it results in a longer time period to rated output voltage power is initially applied.

Output Capacitor

A 1 μ F (min) capacitor from V_{out} to ground is required. Then output capacitor should have an effective series resistance of 5 Ω or less. A 1 μ A capacitor should be connected from V_{in} to GND if there is more than 10 inches of wire between the regulator and the AC filter capacitor, or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used. (since many aluminum electrolytic capacitors freeze at approximately -30°C, solid tantalums are recommended for applications operating below -25°C.) When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

Thermal Considerations

Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds 160°C. The regulator remaining off until the die temperature drops to approximately 150 °C.

Power Dissipation

The amount of power the regulator dissipates is primarily a function of input and output voltage, and output current. The following equation is used to calculate worst case power dissipation:

$$P_D = (V_{INMAX} - V_{OUTMIN}) I_{LOADMAX}$$

Where:

P_D = worst case actual power dissipation

V_{INMAX} = Maximum voltage on V_{IN}

V_{OUTMIN} = Minimum regulator output voltage

$I_{LOADMAX}$ = maximum output (LOAD) Current

The maximum allowable power dissipation is function of the maximum ambient temperature (T_{AMAX}). The maximum allowable die temperature (125°C) and the thermal resistance from junction-to-air (θ_{JA}). The 5-pin SOT-23A package has a θ_{JA} of approximately 220°C/Watt when mounted on a single layer FR4 dielectric copper clad PC board.

$$P_{DMAX} = (T_{JMAX} - T_{AMAX}) / \theta_{JA}$$

Where all terms are previously defined

This can be caused in conjunction with other equation to ensure regulator thermal operation is within limit. For example:

Given:

$$V_{INMAX} = 3.0V + 10\%$$

$$V_{OUTMIN} = 2.7V - 2.5\%$$

$$I_{LOADMAX} = 40 \text{ mA}$$

$$T_{JMAX} = 125^\circ\text{C}$$

$$T_{AMAX} = 55^\circ\text{C}$$

Find:

1. Actual power dissipation
2. Maximum allowable dissipation

Actual power dissipation:

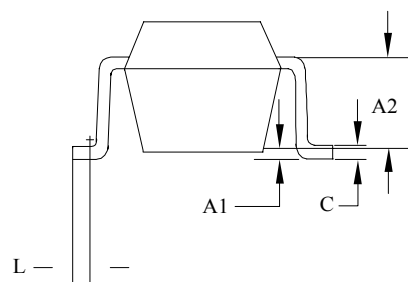
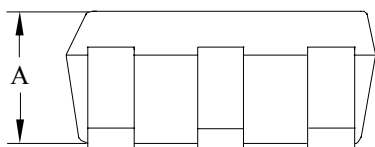
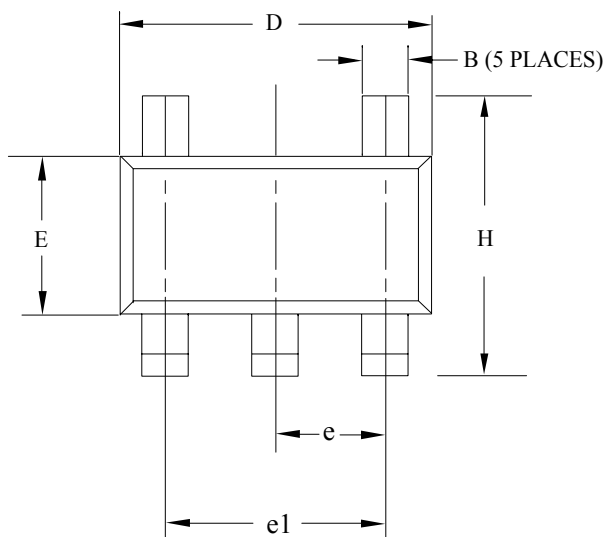
$$\begin{aligned} P_D &= (V_{INMAX} - V_{OUTMIN}) I_{LOADMAX} \\ &= [(3.0 \times 1.1) - (2.7 \times 0.975)] 40 \times 10^{-3} \\ &= 26.7 \text{ mW} \end{aligned}$$

Maximum allowable power dissipation:

$$\begin{aligned} P_{DMAX} &= (T_{JMAX} - T_{AMAX}) \\ &= (125 - 55) / 220 \\ &= 318 \text{ mW} \end{aligned}$$

These equations can be used to calculate maximum current and/or input voltage limits.

SOT-25 Packaging Outline



SYMBOL	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.30	.035	.051
A1	0	0.10	0	.004
A2	0.80 REF		.0315 REF	
B	0.30	0.50	.012	.019
C	0.10	0.35	.004	.0137
D	2.70	3.10	.106	.122
E	1.40	1.80	.055	.071
e	0.95 BSC.		.037 BSC.	
e1	1.70	2.10	.066	.082
H	2.50	3.00	.098	.118
L	MIN 0.2		MIN .0078	

NOTE:

1. REFER TO APPLICABLE
2. CONTROLLING DIMENTION : MILLIMETER
3. PACKAGE SURFACE FINISHING TO BE SMOOTH FINISH.

Advance Information- These data sheets contain descriptions of products that are in development. The specifications are based on the engineering calculations, computer simulations and/ or initial prototype evaluation.

Preliminary Information- These data sheets contain minimum and maximum specifications that are based on the initial device characterizations. These limits are subject to change upon the completion of the full characterization over the specified temperature and supply voltage ranges.

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