

Midium Power Transistors (120V / 700mA)

Structure

NPN Silicon epitaxial planar transistor

Features

Low saturation voltage $V_{CE (sat)} = 0.3V (Max.) (I_C / I_B = 500mA / 50mA)$

Applications

Driver

Packaging specifications

	Package	Taping
Type	Code	T100
	Basic ordering unit (pieces)	1000
2SCR372P		0

hFE values are classified follows:

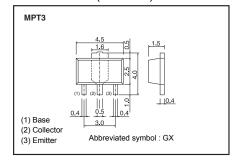
Item	Q	R
hFE	120 to 270	180 to 390

● Absolute maximum ratings (Ta = 25°C)

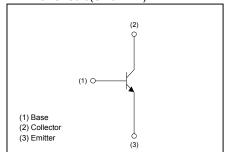
Para	Symbol	Limits	Unit	
Collector-base voltage		V_{CBO}	120	V
Collector-emitter voltage		V_{CEO}	120	V
Emitter-base voltage		V_{EBO}	6	V
Collector current	DC	I _C	0.7	Α
Collector current	Pulsed	I _{CP} *1	1.4	Α
Power dissipation	P _D *2	0.5	W	
Power dissipation		P _D *3	2	W
Junction temperature		T _j 150		°C
Range of storage temperature		T _{stg}	-55 to +150	°C

^{*1} Pw=10ms, Single Pulse

• Dimensions (Unit: mm)



• Inner circuit (Unit : mm)



^{*2} Each terminal mounted on a recommended land.

^{*3} Mounted on a ceramic board. (40x40x0.7mm)

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV_{CEO}	120	-	-	V	I _C = 1mA
Collector-base breakdown voltage	BV_{CBO}	120	-	-	V	I _C = 100μA
Emitter-base breakdown voltage	BV_{EBO}	6	-	-	V	I _E = 100μA
Collector cut-off current	I _{CBO}	-	-	1	μ A	V _{CB} = 100V
Emitter cut-off current	I _{EBO}	-	-	1	μ A	V _{EB} = 4V
Collector-emitter staturation voltage	$V_{CE(sat)}$	-	100	300	mV	I_C = -500mA, I_B = 50mA
DC current gain	h_{FE}	120	-	390	-	V _{CE} = 5V, I _C = 100mA
Transition frequency	f⊤	-	220	-	MHz	V _{CE} = 5V I _E =-300mA, f=100MHz
Collector output capacitance	C _{ob}	-	8	-	pF	V _{CB} = 10V, I _E =0A f=1MHz

●Electrical characteristic curves (Ta=25°C)

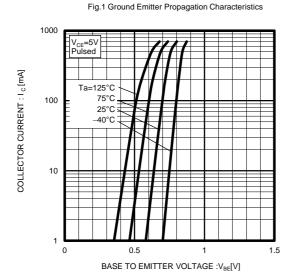


Fig.3 DC Current Gain vs. Collector Current(I)

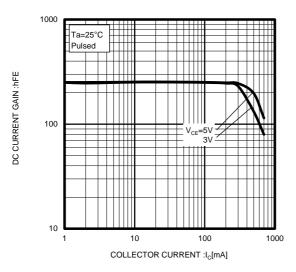
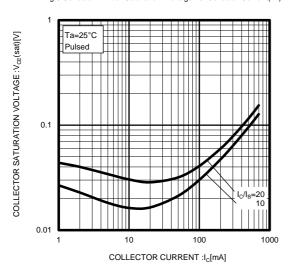


Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current(I)



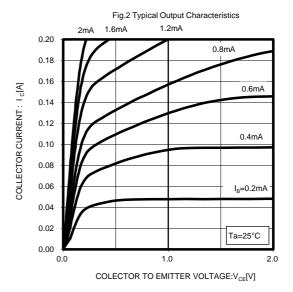


Fig.4 DC Current Gain vs. Collector Current(${\rm I\hspace{-.1em}I}$)

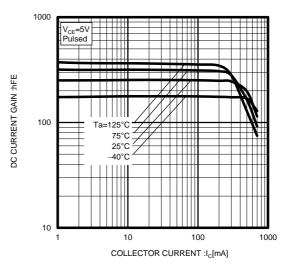


Fig.6 Collector-Emitter Saturation Voltage vs. Collector Current(II)

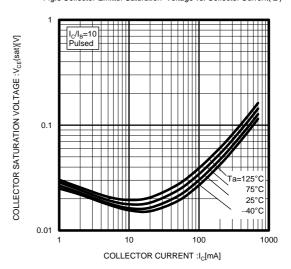


Fig.7 Emitter input capacitance vs. Emitter-Base Voltage Collector output capacitance vs.Collector-Base Voltage

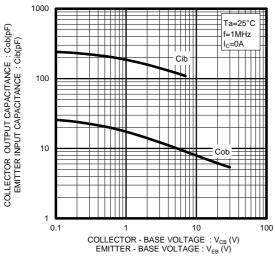


Fig9. Safe Operating Area

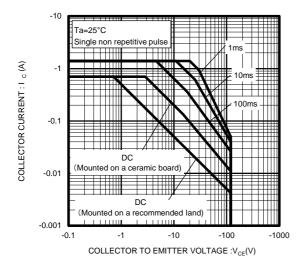
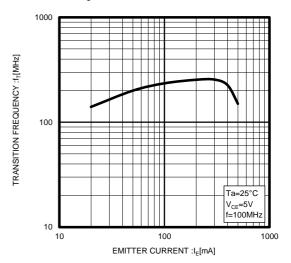


Fig8. Gain Bandwidth Product vs. Emitter Current



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