

# 2SA1980EF

**PNP Silicon Transistor** 

#### **Description**

• General small signal amplifier

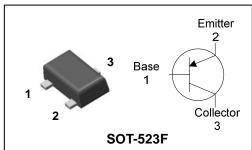
#### **Features**

- Low collector saturation voltage :  $V_{CE(sat)} = -0.3V(Max.)$
- Low output capacitance : Cob=4pF(Typ.)
- Complementary pair with 2SC5343EF

### **Ordering Information**

Type NO. Marking

## **PIN Connection**



Package Code

2SA1980EF	<u>A</u> <u> </u>	SOT-523F	

①Device Code ②hFE Rank ③Year&Week Code

#### **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	-50	V
Collector-Emitter voltage	$V_{\sf CEO}$	-50	V
Emitter-Base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-150	mA
Collector dissipation	P <sub>C</sub>	150	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	$T_{stg}$	-55~150	°C

### **Electrical Characteristics**

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = -100 \mu A, I_E = 0$	-50	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=-1$ mA, $I_B=0$	-50	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-10μA, I <sub>C</sub> =0	-5	-	-	٧
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50V$ , $I_{E} = 0$	-	-	-0.1	μА
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -5V, I_{C} = 0$	-	-	-0.1	μА
DC current gain	h <sub>FE</sub> *	$V_{CE}$ =-6V, $I_{C}$ =-2mA	70	-	700	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-100mA, I <sub>B</sub> =-10mA	-	-	-0.3	V
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> =-10V, I <sub>C</sub> =-1mA	80	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz	-	4	7	pF
Noise figure	NF	$V_{CE}$ =-6V, $I_{C}$ =-0.1mA f=1KHz, $Rg$ =10K $\Omega$	-	_	10	dB

<sup>\*:</sup>  $h_{FE}$  rank / O : 70~140, Y : 120~240, G : 200~400, L : 300~700

KSD-T5E008-000

### **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub>-T<sub>a</sub>

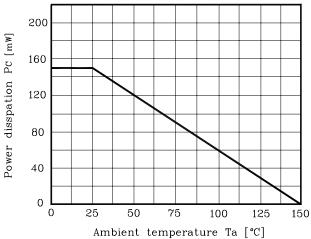


Fig. 3  $I_{\text{C}}$ . $V_{\text{CE}}$ 

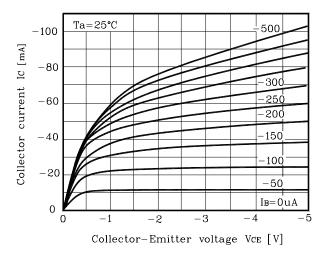


Fig. 5  $V_{CE(sat)}$ - $I_C$ 

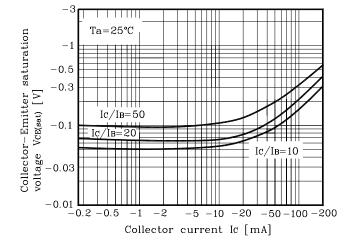


Fig. 2  $I_{\text{C-}}V_{\text{BE}}$ 

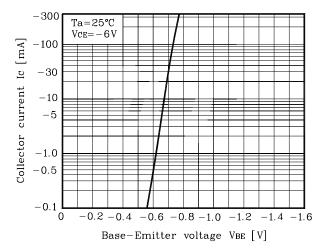
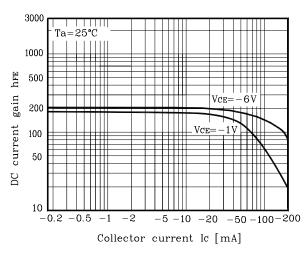
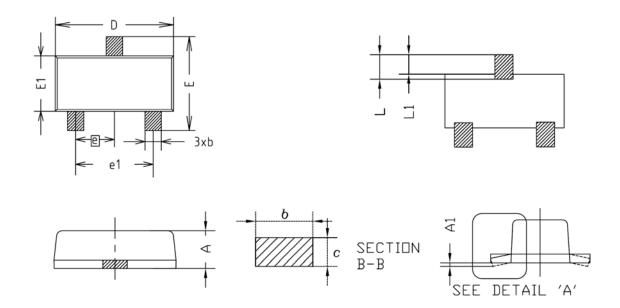


Fig. 4 h<sub>FE</sub>-I<sub>C</sub>



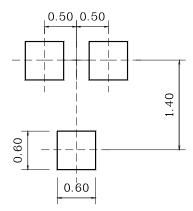
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## **Outline Dimension**



SYMBOL	MILLIMETERS			NOTE
STINDEL	MINIMUM	NOMINAL	MAXIMUM	NUIL
Α	0.63	0.68	0.73	
A1	0.00	_	0.10	
A2	_	_	_	
b	0.25	0.30	0.35	
U	0.04	0.11	0.20	
D	1.50	1.60	1.70	
Ε	1.50	1.60	1.70	
E1	0.78	0.88	0.98	
е	0.50BSC			
e1	0.90	_	1.10	
L	0.34	0.44	0.54	
L1	0.28	0.34	0.43	

#### **\*Recommend PCB solder land [Unit: mm]**



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