

$PIC16C55 \rightarrow PIC16C55A \ Migration$

DEVICE MIGRATIONS

This document is intended to describe the functional differences and the electrical specification differences that are present when migrating from one device to the next.

Note: This device has been designed to perform to the parameters of its data sheet. It has been tested to an electrical specification designed to determine its conformance with these parameters. Due to process differences in the manufacture of this device, this device may have different performance characteristics than its earlier version. These differences may cause this device to perform differently in your application than the earlier version of this device.

Table 1 shows the considerations that must be taken into account when migrating from the PIC16C55 to the PIC16C55A.

TABLE 1: PIC16C55 \rightarrow PIC16C55A DIFFERENCES

Functional Differences								
No.	Difference		S/W	Prog.				
1	Master Clear Filter added, PIC16C55A. See Electrical Specification #30	>	—					
2	Programming algorithm change, PIC16C55A uses a new programming algorithm		—	~				
4	Oscillator configuration bits are user selectable on the PIC16C55A		~	—				

Parm. No.	Sym.	Characteristic	PIC16C55 Data Sheet		PIC16C55A Data Sheet			11	Conditions	
			Min	Тур	Max	Min	Тур	Max	Units	Conditions
	Vdd	Supply Voltage								
		XT, RC Options	3.0	_	6.25	3.0	_	5.5	V	
		LP Option	2.5	_	6.25	2.5	_	5.5	V	Note 4
		HS option	4.5	_	5.5	4.5	_	5.5	V	
		XT, RC Opt. Extended	3.25	_	6.0	3.0	_	5.5	V	
		LP Option Extended	2.5	—	6.0	3.0	—	5.5	V	Note 4
	IDD	Supply Current								
		XT and RC options	_	1.8	3.3	_	1.8	2.4	mA	Note 1
		HS option	_	4.8	10	_	4.5	16	mA	Note 2
		LP Option, Commercial	_	15	32	—	14	32	μΑ	Note 3
		LP Option, Industrial	—	15	40	—	17	40	μA	Note 3
	IPD	Power Down Current								VDD=3.0V
		Industrial	_	4.0	14	_	4.0	14	μA	WDT Enabled
			_	0.6	12.0	_	0.25	5.0	μA	WDT Disabled
		Extended	—	5.0	22	—	4.5	22	μA	WDT Enabled
			—	0.8	18	—	0.3	18	μΑ	WDT Disabled
	VIL	Input Low Voltage								
		I/O Ports	Vss	_	0.2 Vdd					4.0V <vdd≤5.5< td=""></vdd≤5.5<>
										For all VDD
						Vss	—	0.8	V	4.5V <vdd≤5.5< td=""></vdd≤5.5<>
						Vss		0.15 Vdd	V	Otherwise
	VIH	Input High Voltage								
		I/O Ports	2.0	—	Vdd					4.0V <vdd≤5.5< td=""></vdd≤5.5<>
			0.45Vdd	—	Vdd					For all VDD
						2.0	—	Vdd	V	4.5V <vdd≤5.5< td=""></vdd≤5.5<>
						0.25 VDD+.8V	—	Vdd	V	Otherwise

Note 1: Fosc=4.0MHz, VDD=5.5V

2: Fosc=20MHz, VDD=5.5V

3: Fosc=32kHz, VDD=3.0V, WDT disabled

4: The LP oscillator option is specified for the PIC16C55 up to 40kHz.

Note: The user should verify that the device oscillator starts and performs as expected. Adjusting the loading capacitor values and /or the oscillator mode may be required.

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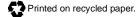
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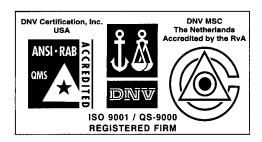
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