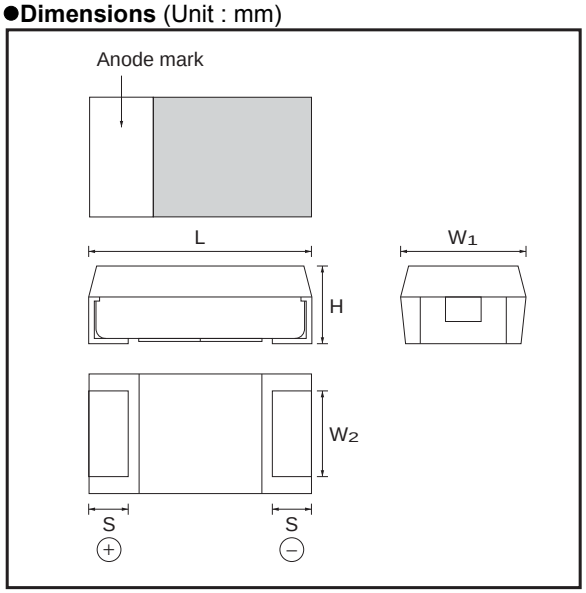
Chip tantalum capacitors with (Fail-safe open structure type)

TCFG series C Case

●Features

1) Safety design by open function built - in.
2) Wide capacitance range
3) Screening by thermal shock.

●Dimensions (Unit : mm)



Case code	L	W1	W2	H	S
C 6032-27(2412)	6.0±0.2	3.2±0.2	2.2±0.1	2.5±0.2	1.3±0.2

●Part No. Explanation

T	C	F	G	C	1	A	1	0	7	M	C	R
	①			②	③		④			⑤	⑥	

① Series name
TCFG

② Case code
TCFG ····· C

③ Rated Voltage

Rated voltage (V)	4	6.3	10	16
CODE	0G	0J	1A	1C

④ Capacitance
Nominal capacitance in pF in 3 digits : 2significant figure representing the number of 0's.

⑤ Capacitance tolerance
M : ±20%

⑥ Taping
C : Reel width (12mm)
R : Positive electrode on the side opposite to sprocket hole

●Capacitance range

TCFG series

(μF)	Rated voltage (V)				
	4 0G	6.3 0J	10 1A	16 1C	20 1D
22 (226)					
33 (336)					
47 (476)				C	
68 (686)			C ＊		
100 (107)			C		
150 (157)		C			
220 (227)	C ＊				

Remark) Case size codes (C) in the above show each size products line-up.
＊ : Under development

●Marking

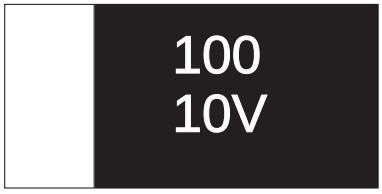
The indications listed below should be given on the surface of a capacitor.

① Polarity : The polarity should be shown by □ bar. (on the anode side)
② Rated DC voltage
③ Nominal capacitance

[C Case] note 1) Visual typical example (1) capacitance code (2) voltage code

(1) 100μF

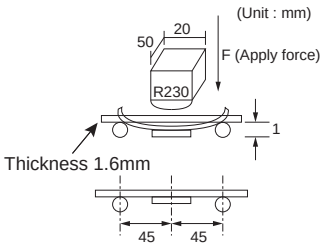
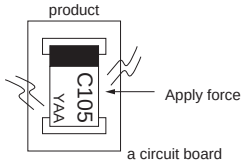
(2) 10V



note 2) voltage code and capacitance code are variable with parts number

●Characteristics

Item		Performance	Test conditions (based on JIS C5101-1 and JIS C5101-3)
Operating Temperature		–55 °C to +125 °C	Voltage reduction when temperature exceeds +85°C
Maximum operating temperature with no voltage derating		+85 °C	
Rated Voltage (V.DC)		4 | 6.3 | 10 | 16	at 85°C
Category Voltage (V.DC)		2.5 | 4 | 6.3 | 10	at 125°C
Surge Voltage		5.0 | 8 | 13 | 20	at 85°C
DC leakage current		0.5μA or 0.01CV whichever is greater (Shown in "Standard list")	As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage : Rated voltage for 1 min
Capacitance tolerance		Shall be satisfied allowance range. ±20%	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms, +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit
Tangent of loss angle (Df, tanδ)		Shall be satisfied the voltage on "Standard list"	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms, +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit
Impedance		Shall be satisfied the voltage on "Standard list"	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency : 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit
Resistance to soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3 Dip in the solder bath Solder temp : 260±10°C Duration : 5±0.5s Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample.
	L.C	Less than initial limit	
	ΔC / C	Within ±10% of initial value	
	tanδ	Less than 150% of initial limit	
Fail-Safe open unit actuation		Within 320°C – 20s	Dip in the solder bath Solder temp : 330±5°C
Temperature cycle	Appearance	There should be no significant abnormality.	As per 4.16 JIS C 5101-1 As per 4.10 JIS C 5101-3 Repetition : 5 cycles (1 cycle : steps 1 to 4) without discontinuation. Step | Temp. | Time 1 | –55±3°C | 30±3min 2 | Room temp. | 3min. or less 3 | 125±2°C | 30±3min 4 | Room temp. | 3min. or less After the specimens, leave it at room temperature for over 24h and then measure the sample.
	L.C	Less than initial limit	
	ΔC / C	Within ±20% of initial value	
	tanδ	Less than 150% of initial limit	
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3 After leaving the sample under such atmospheric condition that the temperature and humidity are 60±2°C and 90 to 95%RH, respectively, for 500±24h level it at room temperature for over 24h and then measure the sample.
	L.C	Less than initial limit	
	ΔC / C	Within ±20% of initial value	
	tanδ	Less than 150% of initial limit	

Item		Performance	Test conditions (based on JIS C5101-1 and JIS C5101-3)
Temperature Stability	Temp.	–55°C	As per 4.29 JIS C 5101-1 As per 4.13 JIS C 5101-3
	ΔC / C	Within 0/–12%of initial value	
	tanδ	Shall be satisfied the voltage on "Standard list"	
	L.C	–	
	Temp.	+85°C	
	ΔC / C	Within +12/0%of initial value	
	tanδ	Shall be satisfied the voltage on "Standard list"	
	L.C	Less than 1000% of initial limit	
	Temp.	+125°C	
	ΔC / C	Within +15/0%of initial value	
	tanδ	Shall be satisfied the voltage on "Standard list"	
	L.C	Less than 1250% of initial limit	
Surge Voltage	Appearance	There should be no significant abnormality.	As per 4.26 JIS C 5101-1 As per 4.14 JIS C 5101-3 Apply the specified surge voltage every 5±0.5min. for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for over 24h and then measure the sample.
	L.C	Less than initial value	
	ΔC / C	Within ±12%of initial value	
	tanδ	Less than 150% of initial limit	
Loading at High temperature	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3 After applying the rated voltage for 2000+72/0h without discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave the sample at room temperature/humidity for over 24h and measure the value.
	L.C	Less than 125% of initial limit	
	ΔC / C	Within ±20%of initial value	
	tanδ	Less than 150% of initial limit	
Terminal Strength	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1 As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below.) 
	Appearance	There should be no significant abnormality.	
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board. 

Item		Performance	Test conditions (based on JIS C5101-1 and JIS C5101-3)
Dimensions		Be based on "External dimensions"	Measure using a caliper of JIS B 7505 Class 2 or higher grade.
Resistance to solvents		The indication should be clear.	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed = 25±2.5mm/s Pre-treatment (accelerated aging) : Leave the sample on the boiling distilled water for 1h. Solder temp. : 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25%, IPA 75%
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm Time : 2h each in X, Y and Z directions Mounting : The terminal is soldered on a print circuit board.
	Appearance	There should be no significant abnormality.	

●Table 1 standard list, TCFG series C Case

(C : 6032)

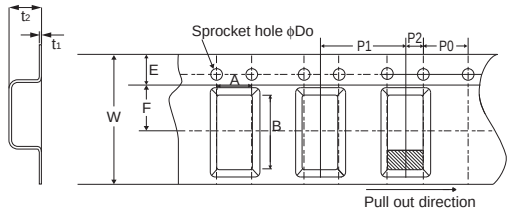
Part No.	Rated Voltage @85°C (V)	Derated Voltage @125°C (V)	Surge Voltage @85°C (V)	Capacitance 120Hz (µF)	Tolerance (%)	Leakage current 25°C 1WV.60s (µA)	DF 120Hz (%)			Impedance 100kHz	Case code
							–55°C	25°C 85°C	125°C	(Ω)	
TCFG C 0J 157 M8R	6.3	4	8	150	±20	9.5	30	12	16	1.3	C
TCFG C 1A 107 M8R	10	6.3	13	100	±20	10.0	14	10	12	1.3	C
TCFG C 1C 476 M8R	16	10	20	47	±20	7.5	12	8	10	1.6	C

●Packaging specifications

Case code C (6032)	A±0.2	B±0.2	W±0.3	E±0.1	F±0.1	P_1±0.1
	3.7	6.4	12	1.75	5.5	8.0
	P_2±0.1	P_0±0.1	D_0	t_1±0.1	t_2±0.2	
	2.0	4.0	ϕ1.5	0.3	3.0	

Taping

C case

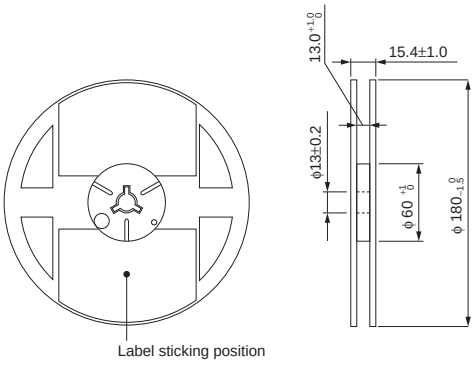


●Packaging style

Case size	Packaging	Packaging style		Symbol	Basic ordering unit
C Case	Taping	Plastic taping	ϕ180mm reel	R	500

Reel

Plastic reel



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