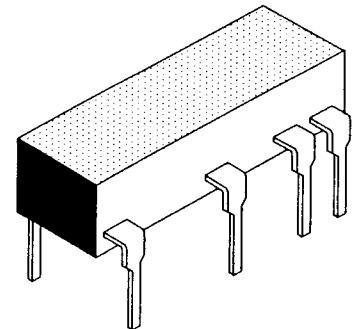


14 PIN, 5 TAP TTL, DIP STYLE ACTIVES

DC Electrical Characteristics		Test Conditions	Min	Max	Unit
Parameter					
VOH	High-Level Output Voltage	VCC=min. VIL=max. IOH=max	2.7		V
VOL	Low Level Output Voltage	VCC=min. VIH=min. IOL=max		0.5	V
VIK	Input Clamp Voltage	VCC=min. II=-18mA		-1.2	V
IIH	High-Level Input Current	VCC=max. VIN=2.7V		50	uA
		VCC=max. VIN=5.25V		1.0	mA
II L	Low-Level Input Current	VCC=max. VIN=0.5V		-2	mA
IOS	Short Circuit Output Current	VCC=max. VOOUT=0 (One output at a time)	-40	-100	mA
ICCH	High-Level Supply Current	VCC=max. VIN=OPEN		75	mA
ICCL	Low-Level Supply Current	VCC=max. VIN=0		75	mA
TRO	Output Rise Time	Td ≤ 500nS(0.75 to 2.4 Volts) Td > 500nS		4	nS
NH	Fanout High-Level Output	VCC=max. VOH=2.7V		5	nS
NL	Fanout Low-Level Output	VCC=max. VOH=0.5V		20 TTL LOAD	
				10 TTL LOAD	



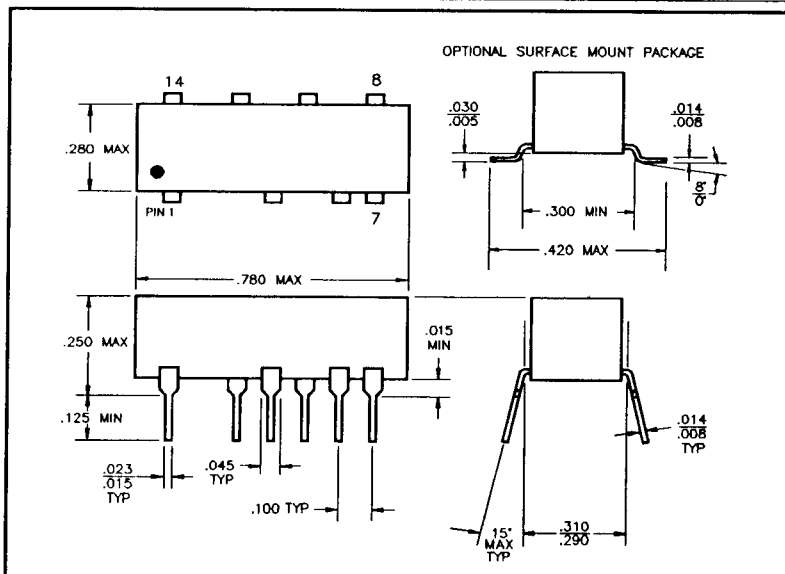
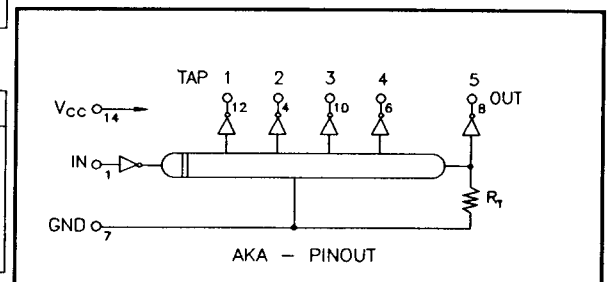
Recommended Operating Conditions		Min	Max	Unit
Parameter				
VCC	Supply Voltage	4.75	5.25	V
VIH	High-level Input voltage	2.0		V
VIL	Low-Level Input Voltage		0.8	V
IOH	High-Level Output Current		-1.0	mA
IOL	Low-Level Output Current		20	mA
PW	Pulse Width % of Total Delay	40		%
TA	Operating Free-Air Temperature	0	+70	°C

ALSO AVAILABLE IN FOLLOWING LOGIC FAMILIES

CMOS (HCT)
FAST
LSTTL

AKC-SERIES
AKF-SERIES
AKL-SERIES

Input Pulse Test Conditions @ 25°C		Unit
Parameter		
EIN	Pulse Input Voltage	3.2
PW	Pulse Width % of Total Delay	110
TRI	Pulse Rise Time(0.75 - 2.4 Volts)	2.0
PRR	Pulse Repetition Rate @ Td ≤ 200 nS	1.0
	Pulse Repetition Rate @ Td > 200 nS	100
VCC	Supply Voltage	5.0



TOTAL DELAY ± 2 nS OR 5%	TAP DELAYS ± 2 nS OR 5%	POLARA P/N AKA - PINOUT
25	5	AKA-0025
30	6	AKA-0030
35	7	AKA-0035
40	8	AKA-0040
45	9	AKA-0045
50	10	AKA-0050
55	11	AKA-0055
60	12	AKA-0060
65	13	AKA-0065
70	14	AKA-0070
75	15	AKA-0075
80	16	AKA-0080
85	17	AKA-0085
90	18	AKA-0090
100	20	AKA-0100
125	25	AKA-0125
150	30	AKA-0150
175	35	AKA-0175
200	40	AKA-0200
250	50	AKA-0250
300	60	AKA-0300
400	80	AKA-0400
500	100	AKA-0500

ADD "S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.

* ANY VALUES NOT SHOWN, AVAILABLE UPON REQUEST.