May 2010 - Rev 25-May-10

#### Features

- ✗ High Linearity, 47 dBm OIP3
- 🗡 High Gain, 10 dB Gain @ 4.9 GHz
- 🗙 34 dBm P1dB
- X Low Thermal Resistance
- ✗ Surface Mount 3x6mm DFN

## **General Description**

The XF1000-BD is a high linearity Hetrojunction Field Effect Transistor (HFET) housed in a 3x6mm DFN package. Optimum performance is achieved when the device is biased at a drain voltage of 8V and drain current of 600 mA. At this bias point and at 4.9 GHz the device is capable of more than 34 dBm of P1dB and OIP3 of more than 46 dBm. The XF1000-DB is suitable for applications up to 5 GHz where it has 10 dB of gain.

#### Typical Performance: 8V, 25 °C

Parameter	Тур	oical	Units
Frequency (F)	3600	4900	MHz
Gain (S21)	10.5	10.0	dB
Ourput IP3 (OIP3)	47.0	46.0	dBm
Output P1dB	34.0	33.7	dBm
Quiescent Current (Idq)	600	600	mA
Input Return Loss (S11)	-6.3	-8.0	dB
Output Return Loss (S22)	-12.6	-11.5	dB

#### **Absolute Maximum Ratings**

Device Voltage (Vdd)	+9.0 V
Current (ldd)	750 mA
Power Dissipation (PDC)	6.0W
RF Input Power (RFin)	+25 dBm
Junction Temperature	175 °C
Thermal Resistance	18 °C/W
Storage Temperature (Tstg)	-55 °C to +150 °C
Operating Temperature	-40 °C to +85 °C
ESD (HBM)	Class 1A
Moisture Sensitivity Level (MSL)	MSL 3

Operation of this device beyond any of these parameters may cause permanent damage.

## Electrical Characteristics (T=25°C,Vdd=8V)

Unless otherwise specified, the following specifications are guaranteed at room temperature in a Mimix test fixture.

Parameter	Condition	Units	Min	Тур	Max
Frequency Range (F)		MHz	DC		5000
Test Frequency		MHz		2000	
Small Signal Gain (S21)	Externally matched	dB	12.5	14.5	
Output Intercept Point (OIP3)	Pout/Tone=22 dBm, Spacing=5 MHz	dBm	+44.0	+46.5	
Output P1dB		dBm	+33.0	+34.0	
Quiescent Current (Icq)		mA		600.0	700.0
Noise Figure (NF)		dB		5	

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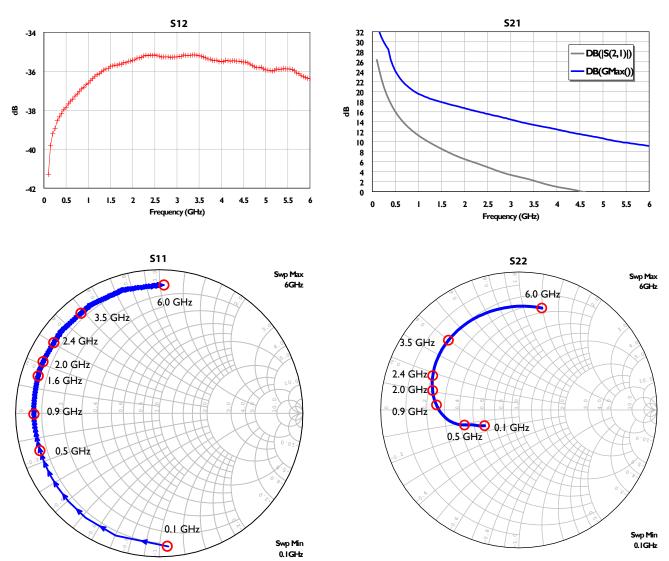
🗙 F1000-DB

RoHS

May 2010 - Rev 25-May-10



## S-parameters: (Vdd=8V, Idd=600 mA)



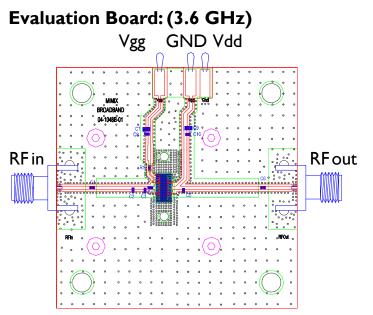
S-Parameters files are available for download at www.mimixbroadband.com.

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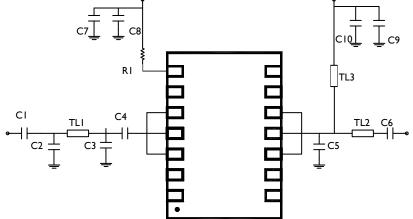
May 2010 - Rev 25-May-10



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#### **Recommended Schematic**



#### **Component Values**

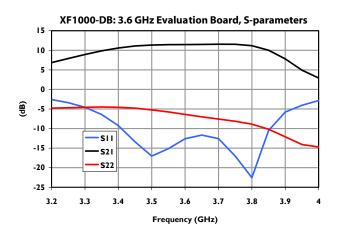
REF. Designator	DESCRIPTION	QTY	ТҮР	TOLERANCE
RI	RESISTOR, 100Ohm 0402	Ι	100 Ω	
СІ	0 ohm link	-	0Ω	
C2	CAPACITOR, 2.2pF 0402	Ι	2.2pF	0.05pF
C3	CAPACITOR, 4.0pF 0402	Ι	4.0pF	0.05pF
C4	CAPACITOR, 2.2pF 0402	Ι	2.2pF	0.05pF
C5	CAPACITOR, I.6pF 0402	Ι	I.6pF	0.05pF
C6	CAPACITOR, 20pF 0402	Ι	20pF	
C7, 9	CAPCITOR, 1uF 0603	Ι	IuF	
C8, C10	CAPACITOR, 10nF 0402	I	10nF	

<b>Ref Designator</b>	Electrical Length
TL1	50Ω, 20.8°
TL2	50Ω, 9.3°
TL3	50Ω, 77°

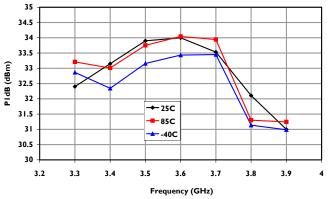
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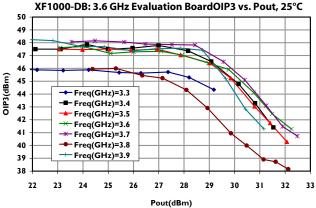
May 2010 - Rev 25-May-10

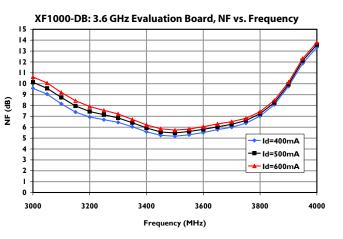
## Typical Performance: 3.6 GHz (Evaluation Board)

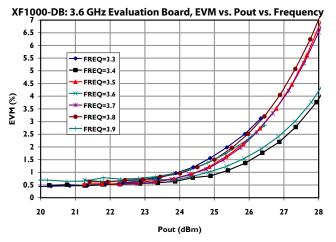


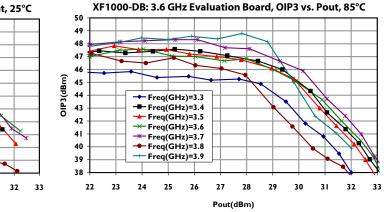












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**XRoHS** 

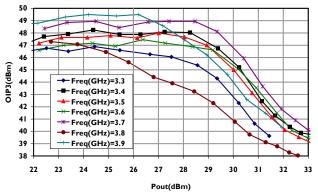
May 2010 - Rev 25-May-10



RoHS

## Typical Performance: 3.6 GHz (Evaluation Board) (cont.)

XF1000-DB: 3.6 GHz Evaluation Board, OIP3 vs. Pout, -40 °C

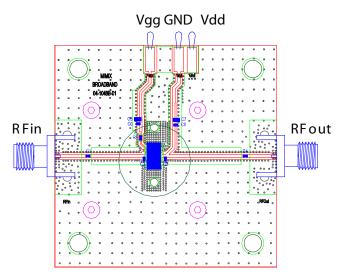


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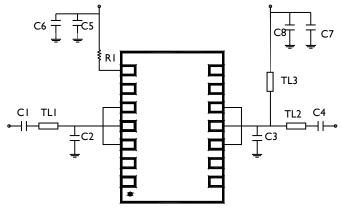
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# BROADBAND **FI000-DB RoHS**

## Evaluation Board: (4.9 GHz)



#### **Recommended Schematic**

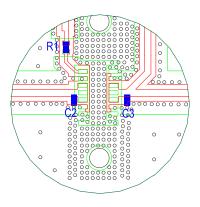


#### **Component Values**

REF. Designator	DESCRIPTION	QTY	ТҮР	Tolerance
RI	Resistor, 100Ohm 0402	I	I00 Ω	
СІ	CAPACITOR, 0402, 50V	I	20pF	5%
C2	CAPACITOR, 0402, 50V	I	I.3pF	0.05pF
C3	CAPACITOR, 0402, ,50V	I	I.4pF	0.05pF
C4	CAPACITOR, 0402, 50V	I	20pF	5%
C5, C7	CAPACITOR, 0603, 16V	I	luF	10%
C6, C8	CAPACITOR, 0402, 25V	I	10nF	10%

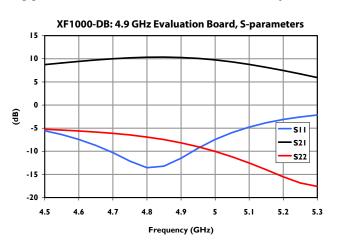
Ref Designator	nator Electrical Length	
TL1	$50\Omega$ , (not critical)	
TL2	50Ω, (not critical)	
TL3	50Ω, 66°	

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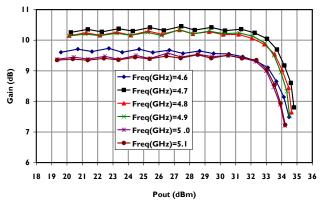


May 2010 - Rev 25-May-10

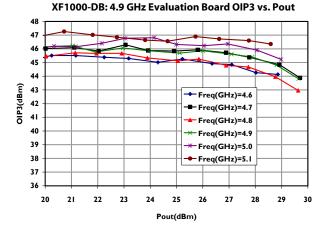
## Typical Performance: 4.9 GHz (Evaluation Board)



XF1000-DB: 4.9 GHz Evaluation Board, Gain vs. Pout



XF1000-DB: 4.9 GHz Evaluation Boad, P1dB vs. Frequency 35 34.5 34 PIdB (dBm) 33.5 33 32.5 32 5 5.2 5.3 4.5 4.6 4.7 4.8 4.9 5. I Frequency (GHz)



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**XRoHS** 

May 2010 - Rev 25-May-10

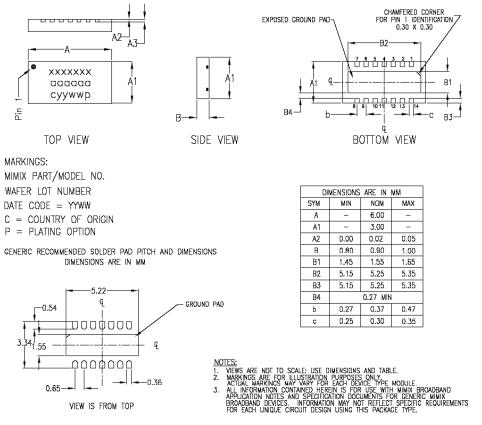


≺ FI000-DB ≺RoHS

**App Note [1] Biasing** - The XF1000-DB requires power supply sequencing. Negative voltage supply (Vgg) needs to be turned on first and then positive voltage can be applied to the drain (Vdd). When turning off the device, the positive supply (Vdd) should be turned off first and then negative voltage (Vgg) can be removed.

The gate voltage is adjusted in order to set the drain current to the desired level. The gate voltage required to achieve a certain current can vary over temperature and from one device to another due to pinch-off voltage variation. Constant drain current can be achieved by Implementing an active bias circuit which allows for temperature compensation and eliminates the effect of pinch off voltage variation.

## **Physical Dimensions**



## **Pin-out Details**

Description	Pin #
Vgg	7
RF Input	3,4,5
RF Output /Vdd	10,11,12
NC	1,2,6,8,9,13,14

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May 2010 - Rev 25-May-10



X FI000-DB XRoHS

## Handling and Assembly Information

**CAUTION!** - Mimix Broadband MMIC Products contain gallium arsenide (GaAs) which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- Do not ingest.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.

**Life Support Policy** - Mimix Broadband's products are not authorized for use as critical components in life support devices or systems without the express written approval of the President and General Counsel of Mimix Broadband. As used herein: (1) Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user. (2) A critical component is any component of a life support device or system, or to affect its safety or effectiveness.

**Package Attachment** - This packaged product from Mimix Broadband is provided as a rugged surface mount package compatible with high volume solder installation. Care should be taken not to apply heavy pressure to the top or base material to avoid package damage. Vacuum tools or other suitable pick and place equipment may be used to pick and place this part. Care should be taken to ensure that there are no voids or gaps in the solder connection so that good RF, DC and ground connections are maintained. Voids or gaps can eventually lead not only to RF performance degradation, but reduced reliability and life of the product due to thermal stress.

**Mimix Lead-Free RoHS Compliant Program** - Mimix has an active program in place to meet customer and governmental requirements for eliminating lead (Pb) and other environmentally hazardous materials from our products. All Mimix RoHS compliant components are form, fit and functional replacements for their non-RoHS equivalents. Lead plating of our RoHS compliant parts is 100% matter tin (Sn) over copper alloy and is backwards compatible with current standard SnPb low-temperature reflow processes as well as higher temperature (260°C reflow) "Pb Free" processes.

## **Ordering Information**

Part Number for Ordering XF1000-DB-0G00 XF1000-DB-0G0T XF1000-DB-EV1

XF1000-DB-EV2

#### Description

Matte Tin plated RoHS compliant 3x6mm surface mount package in bulk quantity Matte Tin plated RoHS compliant 3x6mm surface mount package in tape and reel Evaluation Board @ 3600 MHz Evaluation Board @ 4900 MHz



Proper ESD procedures should be followed when handling this device.

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