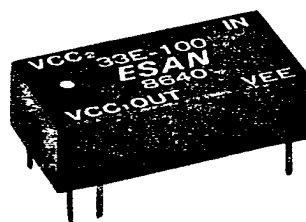


- ULTRA-Fast Rise Time
- Single Output
- 16 Pin Dip Package



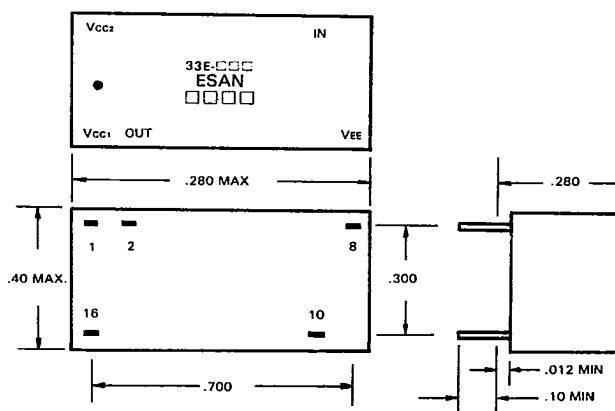
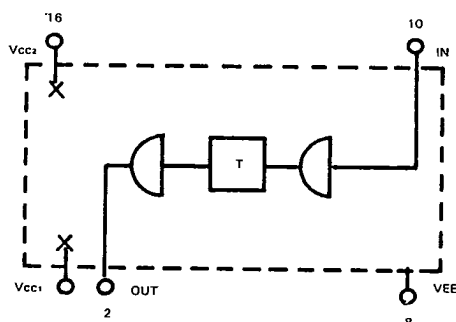
Specifications:

- Delay tolerance : $\pm 5\%$ or 1ns whichever is greater
- Rise time : 3ns max.
- Operating Temp. : 0°C to 70°C
- Supply Voltage : $-5.2\text{V} \pm 5\%$
- Logic 1 Output : -0.96V min
- Logic 0 Output : -1.65 max.
- Logic 1 Input : -0.98V min
- Logic 0 Input : -1.63V max
- Power Dissipation : 200mW typ

Test Conditions:

- Input pulse Amplitude : -1.0V (-0.75V to -1.75V)
- Pulse width : $3 \times \text{max Td}$
- Pulse Spacing : $10 \times \text{max Td}$
- Time delay measured : @ -1.3V level
- Output connected through an external pull down resistor of 100Ω to 2.0VDC
- Input pulse provide by standard open emitter ECL 10K gate.

PART NO.	DELAY TIME (ns)
33E-010	10
33E-020	20
33E-030	30
33E-040	40
33E-050	50
33E-060	60
33E-070	70
33E-080	80
33E-090	90
33E-100	100



Unit: Inch