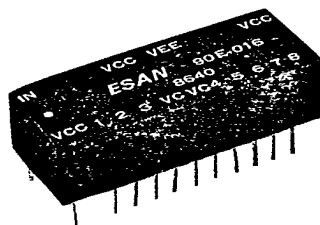


Active Delay Line (ECL 100K Series Interface)

90E Series

- 24 Pin Dip Package
- 8 Tap Output

**Specifications:**

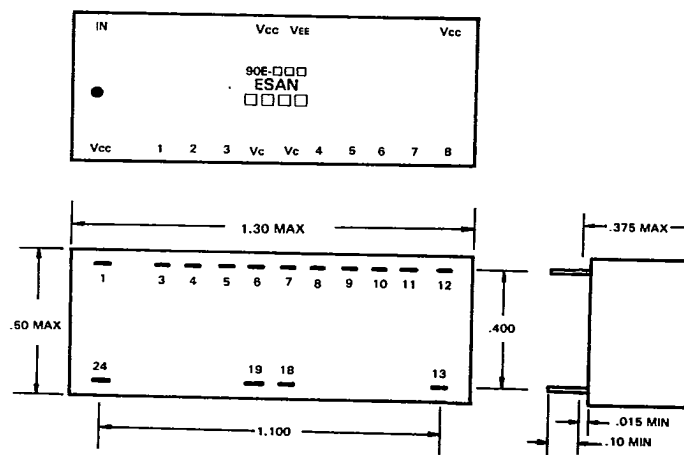
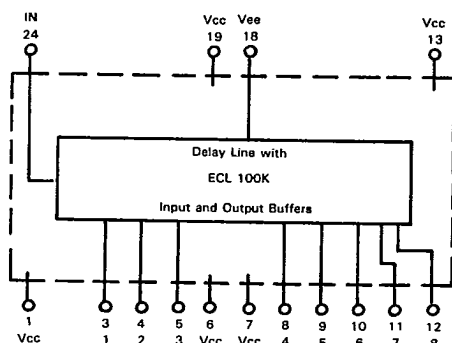
- Operating Temp. Range : 0°C to 85°C
- Storage Temp. : -55°C to +125°C
- Vee : -4.5Vdc $\pm$ 5%
- Logic 1 Output : -1.025Vdc min
- Logic 0 Output : -1.620Vdc max
- Rise Time (20% to 80%) : 2ns max.
- 100K ECL input and output

Test Conditions:

- Input pulse Amplitude : -1.0V (-0.75V to -1.75V)
- Pulse width : 3 * max Td
- Pulse Spacing : 10 * max Td
- Time delay measured : @ -1.3V level
- Output connected through an external pull down resistor of 100 Ω to 2.0VDC
- Input pulse provide by standard open emitter ECL 100K gate.

PART NO.	TOTAL DELAY (ns)	TAP DELAY (ns)
90E-010	10 $\pm$ 1	** 1 $\pm$ 0.5
90E-017	17 $\pm$ 1.5	** 2 $\pm$ 0.5
90E-024	24 $\pm$ 1.5	3 $\pm$ 1.0
90E-032	32 $\pm$ 1.6	4 $\pm$ 1.5
90E-040	40 $\pm$ 2	5 $\pm$ 1.5
90E-048	48 $\pm$ 2.4	6 $\pm$ 1.5
90E-056	56 $\pm$ 2.8	7 $\pm$ 1.5
90E-064	64 $\pm$ 3.2	8 $\pm$ 1.5
90E-072	72 $\pm$ 3.6	9 $\pm$ 1.5
90E-080	80 $\pm$ 4	10 $\pm$ 1.5
90E-160	160 $\pm$ 8	20 $\pm$ 1.5
90E-200	200 $\pm$ 10	25 $\pm$ 2.0

**First tap is 3.0ns



Unit: Inch