

A/D Converters**Successive-Approximation Types**

Type	Special Features	Digital Output Format	Conversion Speed (μs)	V _{SUPPLY}	Operating Temperature Range (T _A) (°C)	Package Number of Pins*
				I _{SUPPLY}		
8-Bit						
ADC0802	Differential inputs	8-bit binary	114 (max.)	+5V (typ.)	0 to +70, -40 to +85 -55 to +125	LCN, LCD, LD
				2.5mA (max.)		
ADC0803	Differential inputs	8-bit binary	114 (max.)	+5V (typ.)	0 to +70, -40 to +85 -55 to +125	LCN, LCD, LD
				2.5mA (max.)		
ADC0804	Differential inputs, 8-bit resolution, max. linearity ½ LSB, TTL, 3-state output	8-bit binary	114 (max.)	+5V (typ.)	0 to +70, -40 to +85 -55 to +125	LCN, LCD
				2.5mA (max.)		
For operation with 8080A or 280μP control bus: ADC0802 Total unadjusted error: ±½ LSB. ADC0803 Total full-scale adjusted error: ±½ LSB. ADC0804 Total unadjusted error: ±1 LSB.						
12-Bit						
ICL7112 J/K/L	High-speed, 12-bit resolution, ½, 1 LSB max. linearity, TTL, 3-state output	8/16 bits	40 (max.)	+5V (typ.)	0 to +70, -25 to +75 -55 to +125	CDL, IDL, MDL
				2mA (typ.)		
14-Bit						
ICL7115 J/K/L	High-speed, 14-bit resolution, 2, 3 LSB max. linearity, TTL, 3-state output	8/16 bits	40 (max.)	+5V (typ.)	0 to +70, -25 to +85 -55 to +125	CDL, IDL, MDL
				8mA (typ.)		

NOTE: Input voltage range = 0 to -5 volts. For ICL7115 also 0 to -5 volts.

* See interpretation guide and packaging section