

Features

- Lead free device (RoHS compliant*)
- Glass passivated chip
- Low reverse leakage current
- Low forward voltage drop
- High current capability

CD214C-F350~F3600 Fast Response Rectifiers

General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components. Bourns offers Glass Passivated Rectifiers for rectification applications, in compact chip DO-214AB (SMC) size format, which offers PCB real estate savings and are considerably smaller than competitive parts. The Glass Passivated Rectifier Diodes offer a forward current of 3.0 A with a choice of repetitive peak reverse voltage of 50 V up to 600 V.

Bourns Chip Diodes® conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

Electrical Characteristics (@ TA = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CD214C-					11
		F350	F3100	F3200	F3400	F3600	Unit
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	V
Maximum Average Forward Rectified Current (@ T _L = 100 °C)¹	I _(AV)	3.0				А	
DC Reverse Current @ Rated DC Blocking Voltage (@T _J = 25 °C)	IR	10.0			μА		
DC Reverse Current @ Rated DC Blocking Voltage (@T _J = 125 °C)	IR	500.0			μА		
Typical Junction Capacitance ²	СЈ	45			pF		
Maximum Instantaneous Forward Voltage @ 3 A	VF	0.92 1.25			1.30	V	
Typical Thermal Resistance ³	$R_{\theta JA}$	15			°C/W		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	100			А		
Maximum Reverse Recovery Time	T _{rr}	25			ns		

Notes:

- 1 See Forward Derating Curve.
- 2 Measured at 1 MHz and an applied reverse voltage of 4.0 V.
- 3 Thermal resistance from junction to lead.

Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

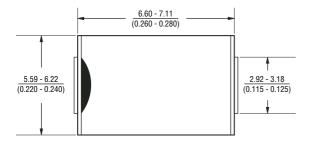
Parameter	Symbol	CD214C-F350~F3600	Unit
Operating Temperature Range	TJ	-55 to +150	°C
Storage Temperature Range	Тѕтс	-55 to +150	°C

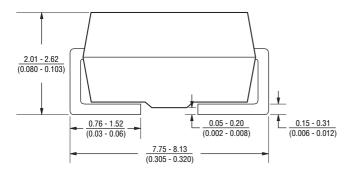
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Product Dimensions

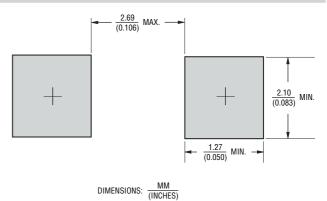
This is a lead free product using 100 % Sn termination. It is a molded plastic package. A cathode band indicates the polarity. The package weighs approximately 0.21 g. The package and dimensions are shown below.

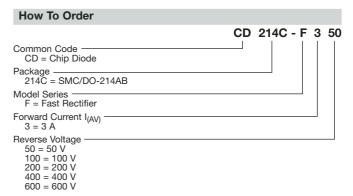




DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

Recommended Pad Layout





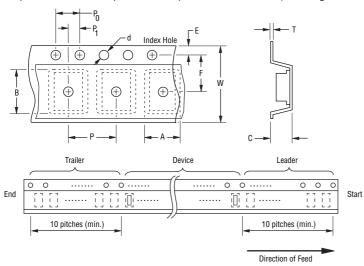
Typical Part Marking	
CD214C-F350	F3A
CD214C-F3100	F3B
CD214C-F3200	F3D
CD214C-F3400	F3G
CD214C-F3600	F3J

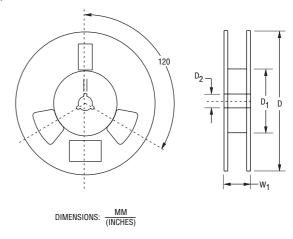
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Packaging Information

The product will be dispensed in Tape and Reel format (see diagram below).





Devices are packed in accordance with EIA standard RS-481-A and specifications shown here.

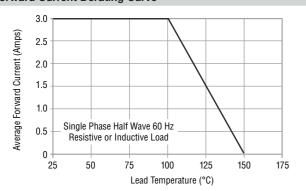
Item	Symbol	SMC (DO-214AB)
Carrier Width	Α	7.22 ±0.10
Carrier Width	A	(0.284 ±0.004)
Carrier Length	В	8.11 ±0.10
Carrier Lerigin		(0.319 ±0.004)
Carrier Depth	С	2.36 ±0.10
Carrier Bepari		(0.093 ±0.004)
Sprocket Hole	d	1.55 ±0.05
oproduct risio		(0.061 ±0.002)
Reel Outside Diameter	D	330
		(12.992)
Reel Inner Diameter	D ₁	$\frac{50.0}{(1.000)}$ Min.
	'	(1.969)
Feed Hole Diameter	D ₂	13.0 ±0.20
	_	(0.512 ±0.008) 1.75 ±0.10
Sprocket Hole Position	E	$\frac{1.75 \pm 0.10}{(0.069 \pm 0.004)}$
		7.50 ±0.004)
Punch Hole Position	F	$\frac{7.30 \pm 0.10}{(0.295 \pm 0.004)}$
		4.00 ±0.10
Punch Hole Pitch	P	(0.157 ±0.004)
		4.00 ±0.10
Sprocket Hole Pitch	P ₀	(0.157 ±0.004)
Facility and the second of the		2.00 ±0.10
Embossment Center	P ₁	(0.079 ±0.004)
Overell Tapa Thickness	Т	0.30 ±0.10
Overall Tape Thickness	'	(0.012 ±0.004)
Tape Width	W	16.00 ±0.20
Tape Width	VV	(0.630 ±0.008)
Reel Width	W ₁	$\frac{22.4}{(0.882)}$ Max.
	-	(0.002)
Quantity per Reel	-	3,000

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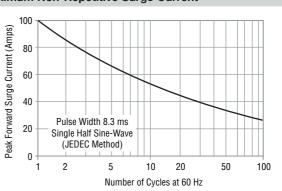
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Performance Graphs

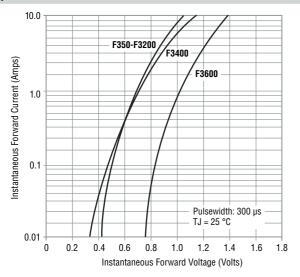
Forward Current Derating Curve



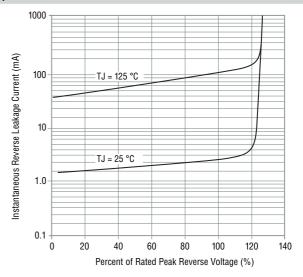
Maximum Non-Repetitive Surge Current



Typical Forward Characteristics



Typical Reverse Characteristics





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