

Silicon PNP Power Transistors

2SA1041

DESCRIPTION

- With TO-3 package
- High transition frequency
- Excellent safe operating area

APPLICATIONS

- Power switching applications
- High frequency power amplifier
- Switching regulators
- DC-DC converters

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

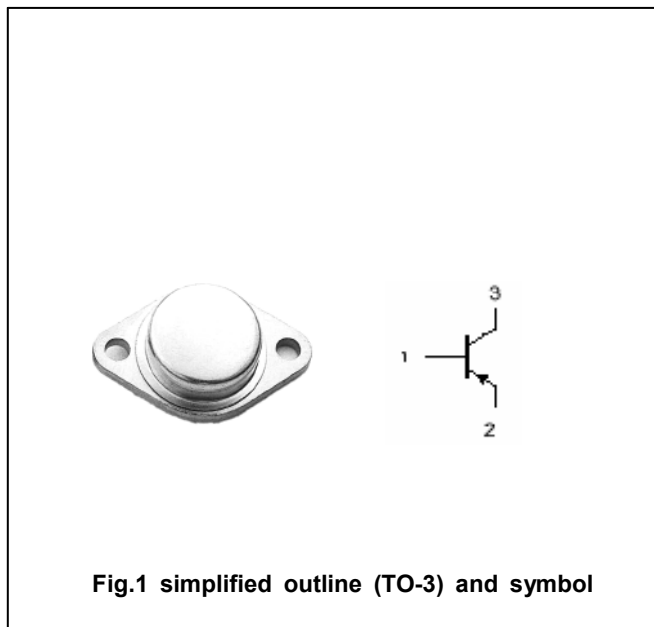


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings($T_a = \square$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-120	V
V_{CEO}	Collector-emitter voltage	Open base	-120	V
V_{EBO}	Emitter-base voltage	Open collector	-7	V
I_C	Collector current		-15	A
P_C	Collector power dissipation	$T_C = 25 \square$	100	W
T_j	Junction temperature		175	\square
T_{stg}	Storage temperature		-55~200	\square

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-10mA ; I _B =0	-120			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-0.1mA ; I _E =0	-120			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-0.1mA ; I _C =0	-7			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-7A ; I _B =-0.7A			-1.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-7A ; I _B =-0.7A			-1.8	V
I _{CBO}	Collector cut-off current	V _{CB} =-120V ; I _E =0			-50	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-7V ; I _C =0			-50	μA
h _{FE}	DC current gain	I _C =-1.5A ; V _{CE} =-5V	35		200	
C _{OB}	Output capacitance	I _E =0 ; V _{CB} =-10V ; f=1.0MHz		350		pF
f _T	Transition frequency	I _C =-1A ; V _{CE} =-10V		60		MHz

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PACKAGE OUTLINE

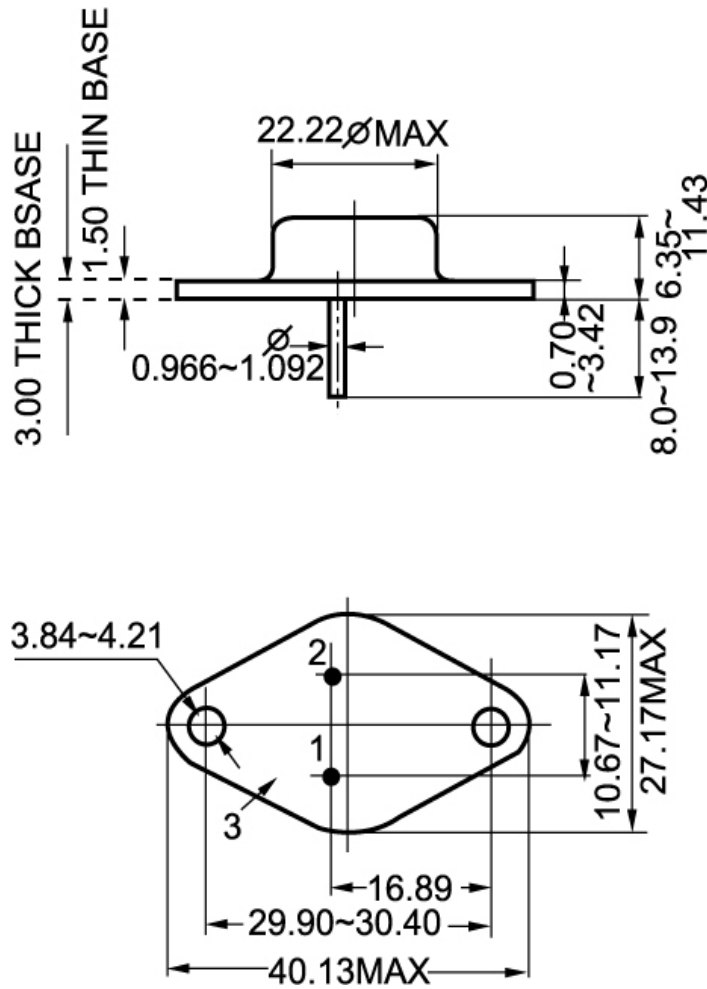


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)