# LA72670BM <br> <br> Monolithic Linear IC <br> <br> Monolithic Linear IC US multiplex modulation for VCR HiFi Sound Signal Processor 

## Overview

The LA72670BM is a HiFi sound signal processor with a built-in US multiplex modulation for VCR.

## Functions

- US multiplex modulation
- HiFi sound signal processor


## Specifications

Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Maximum power supply voltage 1 | $\mathrm{V}_{\mathrm{CCH}} \mathrm{max}$ |  | 9.6 | V |
| Maximum power supply voltage 2 | $\mathrm{V}_{\text {CCL }} \mathrm{max}$ |  | 6 | V |
| Always power supply voltage | $\mathrm{V}_{\text {CCA }} \mathrm{max}$ |  | 6 | V |
| Allowable power dissipation | Pd max | $\mathrm{Ta}=70^{\circ} \mathrm{C}$ * | 1300 | mW |
| Operating ambient temperature | Topr |  | -10 to +70 | ${ }^{\circ} \mathrm{C}$ |
| Storage ambient temperature | Tstg |  | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

[^0]$\square$ Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.
■ Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

Operating Conditions at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Recommended operating voltage 1 | $\mathrm{V}_{\mathrm{CCH}}$ |  | 9 | V |
| Recommended operating voltage 2 | $\mathrm{V}_{\mathrm{CC}} \mathrm{L}$ |  | 5 | V |
| Recommended always voltage | $\mathrm{V}_{\mathrm{CC}} \mathrm{A}$ |  | 5 | V |
| Allowable operating voltage 1 | $\mathrm{V}_{\mathrm{CCH}}$ op1 |  | 8.5 to 9.5 | V |
| Allowable operating voltage 2 | $V_{C C L}$ op2 |  | 4.8 to 5.3 | V |
| Allowable operating always voltage | $V_{C C A}$ op3 |  | 4.5 to 5.5 | V |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CCH}}=9 \mathrm{~V}, \mathrm{~V}_{\mathrm{CCL}}=5 \mathrm{~V}, \mathrm{~V}_{\mathrm{CCA}}=5 \mathrm{~V}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Current dissipation REC\&EE 9V | ICCR1 | No signal, Inflow current at Pin 3/54 G1D8D7:00 |  | 57 | 65 | mA |
| Current dissipation EE 5V | ${ }^{\prime} \mathrm{CCE}{ }^{\text {P }}$ | No signal, Inflow current at Pin 15/32/36/46 G1D8D7:00 |  | 72 | 84 | mA |
| Current dissipation REC 5 V | $\mathrm{I}_{\mathrm{CCR}}$ | No signal, Inflow current at Pin 15/32/36/46 G1D8D7:00, G1D4:1 |  | 100 | 115 | mA |
| Current dissipation PB 9V | $\mathrm{I}_{6} \mathrm{P} 1$ | No signal, Inflow current at Pin $3 / 54$ G1D8D7:01 |  | 11 | 13 | mA |
| Current dissipation PB 5V | $\mathrm{I}_{\mathrm{CCP}}{ }^{\text {P }}$ | No signal, Inflow current at Pin 15/32/36/46 G1D8D7:01 |  | 85 | 97 | mA |
| Current dissipation always power supply | ${ }^{\text {I CCAL }}$ | No signal, Inflow current at Pin 5, Mute H at Pin 49 |  | 1.6 | 2 | mA |
| [EE through] (LINE IN (EXT1,2,3) to LINE OUT), EE mode, $\mathrm{f}=1 \mathrm{kHz}$, L/R-ch |  |  |  |  |  |  |
| Output level 1 | $\mathrm{V}_{\mathrm{O}} 1$ | $\mathrm{V}_{\text {IN }}=-28.2 \mathrm{dBV}$, Gain1 (G3 D4:0) | -7.5 | -9 | -10.5 | dBV |
| Output level 2 | $\mathrm{V}_{\mathrm{O}} 2$ | $\mathrm{V}_{\text {IN }}=-28.2 \mathrm{dBV}$, Gain2 (G3 D4:1) | -6.5 | -8 | -9.5 | dBV |
| Output distortion | THD | $\mathrm{V}_{\text {IN }}=-28.2 \mathrm{dBV}$, Gain1, 2 |  | 0.05 | 0.15 | \% |
| Channel gain difference | $\Delta \mathrm{V}_{\mathrm{O}}$ | $\mathrm{V}_{\text {IN }}=-28.2 \mathrm{dBV}$, Gain1, 2 | -1 | 0 | 1 | dB |
| Maximum output level | $\mathrm{V}_{\mathrm{O}} \mathrm{M}$ | THD=1\%, Gain1, 2 | 7 | 8.5 |  | dBV |
| Output noise level | VNO | $\mathrm{Rg}=1 \mathrm{k} \Omega$, JIS-A filter, Gain1 |  | -89 | -85 | dBV |
| Mute attenuation value | MU | $\mathrm{V}_{\text {IN }}=-18.2 \mathrm{dBV}$ |  | -91 | -80 | dB |
| Input switch cross-talk | CT | $\mathrm{V}_{\text {IN }}=-18.2 \mathrm{dBV}$ |  | -75 | -68 | dB |
| [Normal output] (LINE IN(EXT1/2/3) to NORMAL OUT(Pin 6)), EE mode, f=1kHz,G1D8D7:00 |  |  |  |  |  |  |
| Output level for Normal | $\mathrm{V}_{\mathrm{O}}$ NOR | $\mathrm{V}_{\text {IN }}=-28.2 \mathrm{dBV}$ | -22 | -21 | -20 | dBV |
| [BS through] (BS, IN to LINE OUT), $\mathrm{f}=1 \mathrm{kHz}$, G2D6:1 |  |  |  |  |  |  |
| Output level | $\mathrm{V}_{\mathrm{O}}$ TH | $\mathrm{V}_{\text {IN }}=-21.2 \mathrm{dBV}$, Gain1 (G3D4:0) | -10.5 | -9 | -7.5 | dBV |
| [RFC output] (NORMAL IN to RFC OUT), $\mathrm{f}=1 \mathrm{kHz}, \mathrm{G} 2 \mathrm{D} 3 \mathrm{D} 4: 10, \mathrm{G4D7} \mathrm{l}^{0}$ |  |  |  |  |  |  |
| Output level | $\mathrm{V}_{\mathrm{O}} \mathrm{R}$ | $V_{\text {IN }}=-21.2 \mathrm{dBV}, \mathrm{G} 4 \mathrm{D} 2: 0$ | -11.0 | -9.5 | -8.0 | dBV |
| Output distortion | THDR | $V_{\text {IN }}=-21.2 \mathrm{dBV}, \mathrm{G} 4 \mathrm{D} 2: 0$ |  | 0.05 | 0.2 | \% |
| ALC level (1) | $V_{\text {OAR1 }}$ | VIN=-11.2dBV, G4D2:0 | -7.0 | -5.5 | -4.0 | dBv |
| ALC distortion (1) | THDAR1 | $\mathrm{V}_{\text {IN }}=-11.2 \mathrm{dBV}, \mathrm{G} 4 \mathrm{D} 2: 0$ |  | 0.3 | 0.5 | \% |
| ALC level (2) | $V_{\text {OAR2 }}$ | $\mathrm{V}_{\text {IN }}=-11.2 \mathrm{dBV}, \mathrm{G4D} 2: 1$ | -3.0 | -1.5 | 0 | dBv |
| ALC distortion (2) | THDAR2 | $\mathrm{V}_{\mathrm{IN}}=-11.2 \mathrm{dBV}, \mathrm{G} 4 \mathrm{D} 2: 1$ |  | 0.3 | 0.5 | \% |
| [LINE AMP] (NORMAL IN to LINE OUT), EE mode, f=1kHz, Left channel and Right channel, G2D4D3:10 |  |  |  |  |  |  |
| Line amp gain | GVL | Gain1 mode, $\mathrm{V}_{\text {IN }}=-21 \mathrm{dBV}$ | 11 | 12 | 13 | dB |
| [REC system] (LINE IN to VCO OUT), f=1kHz, G1D4:1(REC_MODE), G1D3D2;O1(EXT1), G3D8;0(NTSC), G4D5D6;00 |  |  |  |  |  |  |
| Free-running frequency L | $\mathrm{f}_{\mathrm{O}} \mathrm{L}$ | Input no signal | 1.294 | 1.300 | 1.306 | MHz |
| Free-running frequency $R$ | $\mathrm{f}_{\mathrm{O}} \mathrm{R}$ | Input no signal | 1.694 | 1.700 | 1.706 | MHz |
| Standard frequency deviation L\&R | DEV | $V_{\text {IN }}=-28.2 \mathrm{dBV}$ | $\pm 46$ | $\pm 50$ | $\pm 54$ | kHz |
| Carrier output level Lch | VfoL | Non modulation | 450 | 500 | 550 | mVp-p |
| Continued on next page |  |  |  |  |  |  |

LA72670BM
Continued from preceding page.

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| FM R/Lch MIX ratio 1 | MIX1 | Non modulation, VfoR/VfoL, P23:OV,P34.Measure, G3D7D6;01 | 7.1 | 7.6 | 8.1 | dB |
| FM R/Lch MIX ratio 2 | MIX2 | Non modulation, VfoR/VfoL , P23:OV,P34.Measure,G3D7D6;00 | 8.1 | 8.6 | 9.1 | dB |
| FM R/Lch MIX ratio 3 | MIX3 | Non modulation, VfoR/VfoL, P23:OV,P34.Measure, G3D7D6;10 | 9.1 | 9.6 | 10.1 | dB |
| FM R/Lch MIX ratio 4 | MIX4 | Non modulation, VfoR/VfoL, P23:OV, P34.Measure, G3D7D6;11 | 10.1 | 10.6 | 11.1 | dB |
| Carrier 2nd high frequency | 2HD | Non modulation, To each basic frequency. |  | -48 | -34 | dB |
| Carrier 3rd high frequency | 3HD | Non modulation, To each basic frequency. |  | -43 | -34 | dB |
| REC current | ${ }_{10} \mathrm{R}$ | P23:0V, Inflow current at P26, with current probe | 34 | 37 | 40 | mAp-p |
| Cross modulation distortion 0.4M component | CMDO4 | P23:0V, Compare 0.4 MHz with Rch Carrier Level |  | -48 | -40 | dB |
| modulation distortion 0.9M component | CMDO9 | P23:0V, Compare 0.9MHz with Rch Carrier Level |  | -55 | -40 | dB |
| current ratio -1.5 dB | $\mathrm{I}^{\text {OR-1.5dB }}$ | P23:0V G4D6D5:01(-1.5dB) | -2.3 | -1.3 | -0.3 | dB |
| current ratio - 5.5 dB | $\mathrm{I}_{0} \mathrm{R}-5.5 \mathrm{~dB}$ | P23:0V G4D6D5:10(-5.5dB) | -6.8 | -5.8 | -4.8 | dB |
| MUTE attenuation value | IoRMU | REC MUTE ON(Pin 17=H) |  |  | -40 | dB |
| [FM modulation system] (PB FM IN to LINE OUT), PB mode(G1D8D7;01), FM standard input=300mVp-p(R/Lch ratio=1:1) |  |  |  |  |  |  |
| Output level Lch | $\mathrm{V}_{\mathrm{O}} \mathrm{PL}$ | $\mathrm{fc}=1.3 \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz}, \mathrm{G}=1$ | -11 | -9 | -7 | dBV |
| Output level Rch | $\mathrm{V}_{\mathrm{O}} \mathrm{PR}$ | $\mathrm{fc}=1.7 \mathrm{MHz} \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz}, \mathrm{G}=1$ | -11 | -9 | -7 | dBV |
| Output level difference | VDEM | $\begin{aligned} & \mathrm{fc}=1.3 \mathrm{MHz} \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz} \\ & \mathrm{fc}=1.7 \mathrm{MHz} \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz} \\ & \text { Lch-Rch } \end{aligned}$ | -1.5 | 0 | 1.5 | dB |
| Output distortion Lch | THDPL | $\mathrm{fc}=1.3 \mathrm{MHz} \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz}$, DIN |  | 0.3 | 0.5 | \% |
| Output distortion Rch | THDPR | $\mathrm{fc}=1.7 \mathrm{MHz} \pm 50 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz}$, DIN |  | 0.3 | 0.5 | \% |
| [DO detector / HiFi detector] PB mode(DO DET : fc=1.3MHz / HiFi DET : fc=1.7MHz), G1D8D7:01 |  |  |  |  |  |  |
| DO detection level | DOC | The ratio with Standard input 300mVp-p |  | -26 | -23 | dB |
| DO detection hysteresis | DOCH |  | 0.5 | 3 | 5 | dB |
| HiFi recovery delay time | HIDEL | The delay time that is changed from NORMAL to HiFi . | 110 | 125 | 140 | ms |
| HiFi detection DC output 1 | VTRS1 | Pin 34 INPUT_Level=100mVp-p, 1.3MHz | 2.1 | 2.6 | 3.1 | V |
| HiFi detection DC output 2 | VTRS2 | Pin 34 INPUT_Level=300mVp-p, 1.3MHz | 3.3 | 3.8 | 4.3 | V |
| HiFi detection DC output 3 | VTRS3 | Pin 34 INPUT_Level=1Vp-p, 1.3MHz | 4.3 | 4.8 | 5.3 | V |
| NORMAL detection DC output | NORDC | Pin 34 INPUT_Level=0mVp-p |  |  | 0.4 | V |
| [Hold pulse occurrence] PB mode, G1D8D7:01 |  |  |  |  |  |  |
| Hold pulse delay time | HPD | AUDIO HEAD PULSE IN | 0.8 | 1.0 | 1.2 | $\mu \mathrm{s}$ |
| Hold pulse width | HPW | AUDIO HEAD PULSE IN | 7.0 | 8.3 | 9.5 | $\mu \mathrm{s}$ |
| [Band pass filter] PB mode, PB IN = 150mVp-p(R/L MIX ratio 1:1)at Pin 31, Pin 17;2.5V, G1D8D7;01, |  |  |  |  |  |  |
| 1.3 MHz BPF monitor level | V13 | G2D2D1;01 | 80 | 105 | 130 | mVp-p |
| 1.7MHz BPF monitor level | V17 | G2D2D1;10 | 65 | 90 | 115 | mVp-p |
| 1.3 MHz BPF <br> frequency characteristics 1 | L115N | Level difference between $1.15 \mathrm{M} / 1.3 \mathrm{MHz}$ G2D2D1;01 | -9 | -3 |  | dB |
| 1.3 MHz BPF <br> frequency characteristics 2 | L145N | Level difference between $1.45 \mathrm{M} / 1.3 \mathrm{MHz}$ G2D2D1;01 | -18 | -8 |  | dB |
| 1.3MHz BPF <br> frequency characteristics 3 | L155N | Level difference between $1.55 \mathrm{M} / 1.3 \mathrm{MHz}$ G2D2D1;01 |  | -27 | -9 | dB |
| 1.7MHz BPF <br> frequency characteristics 1 | R145N | Level difference between $1.45 \mathrm{M} / 1.7 \mathrm{MHz}$ G2D2D1;10 |  | -18 | -8 | dB |
| 1.7MHz BPF <br> frequency characteristics 2 | R155N | Level difference between $1.55 \mathrm{M} / 1.7 \mathrm{MHz}$ G2D2D1;10 | -9 | -3 |  | dB |
| 1.7MHz BPF <br> frequency characteristics 3 | R185N | Level difference between $1.85 \mathrm{M} / 1.7 \mathrm{MHz}$ G2D2D1;10 | -12 | -2 |  | dB |

Continued on next page.

Continued from preceding page.

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| [Playback head amp system] (PB HEAD AMP IN to OUT), PB mode,G1D8D7;01 |  |  |  |  |  |  |
| Voltage gain | GVP | $\mathrm{V}_{\mathrm{IN}}=100 \mu \mathrm{Vp}-\mathrm{p}, \mathrm{f}=1.5 \mathrm{MHz}, \mathrm{CH} 1 \& 2$ | 69 | 72 | 75 | dB |
| Voltage gain difference $\mathrm{CH} 1 / \mathrm{CH} 2$ | $\triangle \mathrm{GVP}$ |  | -2 | 0 | 2 | dB |
| EP gain boost value | $\triangle \mathrm{GEP}$ | $\mathrm{V}_{\text {IN }}=100 \mu \mathrm{Vp}-\mathrm{p}, \mathrm{f}=1.5 \mathrm{MHz}$ | 1 | 2 | 3.2 | dB |
| Frequency characteristics | $\Delta \mathrm{fP}$ | $\mathrm{V}_{\mathrm{IN}}=100 \mu \mathrm{Vp}-\mathrm{p}, \mathrm{f}=1.8 \mathrm{M} / 1.0 \mathrm{MHz}, \mathrm{CH} 1 \& 2$ | -3 | -1 | 1 | dB |
| Input conversion noise voltage | VNINP | The value(1 / GVP) of 1.1 MHz LPF output level |  | 1.7 | 2 | $\mu \mathrm{Vrms}$ |
| Output DC offset | $\Delta \mathrm{V}_{\mathrm{O}} \mathrm{DC}$ | CH1/CH2 | -30 | 0 | 30 | mV |
| [SIF system] (SIF IN to SIF OUT), EE/REC mode |  |  |  |  |  |  |
| Input level | VILIM | $\mathrm{fc}=4.5 \mathrm{MHz}$ | 80 | 90 | 100 | $\mathrm{dB} \mathrm{\mu} \mathrm{~V}$ |
| Output level | $\mathrm{V}_{\mathrm{O}} \mathrm{SI}$ | $\mathrm{fc}=4.5 \mathrm{MHz} \pm 25 \mathrm{kHz}, \mathrm{fm}=1 \mathrm{kHz}$ | 420 | 530 | 660 | mVp-p |
| Distortion | THDSI | MONO 1kHz 100\% modulation |  | 0.3 | 0.8 | dB |
| S/N | SNSI | $75 \mu$ De-emphasis | 57 | 62 |  | dB |
| [TV multiplex demodulation system] (BASE-BAND IN to LINE OUT), EE/REC mode, L/Rch, LINE AMP GAIN(1) Deviation of SIF input MONO: $300 \mathrm{~Hz} 100 \% \rightarrow \pm 25 \mathrm{kHz}$ |  |  |  |  |  |  |
| MONO output level | $\mathrm{V}_{\mathrm{O}} \mathrm{MN}$ | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, Pre-em. ON | -8.5 | -7 | -5.5 | dBV |
| Output L/R level difference | $\Delta \mathrm{V}_{\mathrm{O}} \mathrm{MN}$ | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, Pre-em.ON | -1.5 | 0 | 1.5 | dB |
| MONO distortion | THDM | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, Pre-em. ON |  | 0.15 | 0.6 | \% |
| MONO frequency characteristics 1 | FCM1 | $\mathrm{fm}=8 \mathrm{kHz}, 30 \%$ modulation, Pre-em.ON | -2 | 0 | 2 | dB |
| MONO S/N | SNM | $\mathrm{S}=\mathrm{V}_{\mathrm{O}} \mathrm{MN}, \mathrm{N}=0 \%$ modulation, 15kHz LPF+JIS-A | 57 | 62 |  | dB |
| STEREO output level | $\mathrm{V}_{\mathrm{O}} \mathrm{ST}$ | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, 15kHzLPF | -9 | -7 | -5 | dBV |
| STEREO distortion | THDS | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, 15 kHz LPF |  | 1.0 | 2.5 | \% |
| STEREO S/N | SNS | $\mathrm{S}=\mathrm{V}_{\mathrm{O}} \mathrm{ST}, \mathrm{N}=0 \%$ modulation, 15kHz LPF+JIS-A | 50 |  |  | dB |
| STEREO separation | STSE1 | $\mathrm{f}=300 \mathrm{~Hz}$ (R/L), $30 \%$ modulation, 15 kHz LPF | 20 | 25 |  | dB |
| STEREO separation2 | STSE2 | $\mathrm{f}=3 \mathrm{kHz}(\mathrm{R} / \mathrm{L}), 30 \%$ modulation, 15 kHz LPF | 20 | 25 |  | dB |
| Input Pilot level1 for STEREO detection | $\mathrm{V}_{\text {IN }} \mathrm{SD}$ | Pilot $(\mathrm{fH})=15.73 \mathrm{kHz}, 100 \%=110 \mathrm{mVp}-\mathrm{p}$ |  | 40 |  | \% |
| Input Pilot level1 hysteresis for STEREO detection | HYST | Pilot $(\mathrm{fH})=15.73 \mathrm{kHz}, 0 \mathrm{~dB}=\mathrm{V}_{\text {IN }} \mathrm{SD}$ |  | 3 |  | dB |
| Stereo VCO free-running frequency | FSTVCO | No signal, the monitor output of Pin 51 is measured. | 60.0 | 63 | 66.8 | kHz |
| SAP output level | $V_{O} S A$ | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, 15 kHz LPF | -10 | -7 | -4 | dBV |
| SAP distortion | THDSA | $\mathrm{fm}=1 \mathrm{kHz}, 100 \%$ modulation, 15 kHz LPF 2nd+3rd harmonic distortion, 15 kHz LPF |  | 1.5 | 3.5 | \% |
| SAP S/N | SNSA | $\mathrm{S}=\mathrm{V}_{\mathrm{O}} \mathrm{SA}, 100 \%$ modulation, 15 kHz LPF+JIS-A | 55 | 65 |  | dB |
| SAP detection input level | $\mathrm{V}_{\text {IN }}$ SA | SAP Carrier=5fH, 0dB=300 mVp-p | -26 | -20 | -15 | dB |
| SAP detection hysterisis | HYSA | SAP Carrier=5fH |  | 3 |  | dB |
| MODE output MONO | LEDMO | MONO:f=1kHz, 0\% modulation |  | 1.0 | 1.3 | V |
| MODE output SAP | LEDSA | SAP:Carrier | 1.7 | 2.0 | 2.3 | V |
| MODE output STEREO | LEDST | STEREO:Pilot | 2.7 | 3.0 | 3.3 | V |
| MODE output ST+SAP | LEDSS | STEREO:Pilot, SAP:Carrier | 3.5 | 3.8 | 4.2 | V |
| [Control hold voltage] |  |  |  |  |  |  |
| CLOCK Low voltage | VCL |  | 0 |  | 1 | V |
| CLOCK High voltage | VCH |  | 2.5 |  | $\mathrm{V}_{\mathrm{CC}}{ }^{2}$ | V |
| DATA Low voltage | VDL |  | 0 |  | 1 | V |
| DATA High voltage | VDH |  | 2.5 |  | $\mathrm{V}_{\mathrm{Cc}}{ }^{2}$ | V |
| MUTE ON voltage | MON |  | 3.0 |  | $\mathrm{V}_{\mathrm{CC}}{ }^{2}$ | V |
| MUTE OFF voltage | MOFF |  | 0 |  | 1.0 | V |
| REC MUTE ON voltage | RMON |  | 3.0 |  | $\mathrm{V}_{\mathrm{CC}}{ }^{2}$ | V |
| REC MUTE OFF voltage | RMOFF |  | 0 |  | 1.0 | V |

## Package Dimensions

unit : mm (typ)
5255


Block Diagram


LA72670BM
Pin Description

\begin{tabular}{|c|c|c|c|c|}
\hline Pin \& \multirow{2}{*}{Pin Function Name} \& DC voltage \& \multirow{2}{*}{Function} \& \multirow[b]{2}{*}{Equivalent circuit} \\
\hline No. \& \& AC level \& \& \\
\hline 1
77 \& \begin{tabular}{l}
Line Mute terminal(L) \\
Line Mute terminal(R)
\end{tabular} \& \& \begin{tabular}{l}
When the power supply \(\mathrm{V}_{\mathrm{CC}}\) is on, the switch of Pin 77 and Pin 1 is turned to ON to reduce the line out noise. \\
In this case, it is necessary to apply 5 fixed DC to Pin 5.
\end{tabular} \&  \\
\hline 2 \& Output terminal for RF modulator \& \begin{tabular}{l}
DC; 4.2V \\
AC; -9.5dBV
\end{tabular} \& Output terminal for RF modulator. ALC level can be settled to \(-1 d B V\) and \(-5 d B V\) by serial control. \&  \\
\hline 3 \& \(\mathrm{V}_{\text {CC }} 9 \mathrm{~V}\) \& \& Power supply of Line Out. \& \\
\hline 5 \& ALWAYS VCC \& \& Power supply for the noise elimination mute control when power is on. \& \\
\hline 15 \& \(\mathrm{V}_{\text {CC }} 5 \mathrm{~V}\) (Lch) \& \& 5 V power supply of Lch. \& \\
\hline 32 \& \(\mathrm{V}_{\mathrm{CC}} 5 \mathrm{~V}\) \& \& 5 V power supply of HEAD AMP. \& \\
\hline 36 \& Power supply for Logic \& \& Power supply for Logic. \& \\
\hline 46 \& \(\mathrm{V}_{\mathrm{CC}} 5 \mathrm{~V}\) (Rch) \& \& 5 V power supply of Rch. \& \\
\hline 54 \& 9 V power supply for MTS \& \& 9 V power supply of MTS. \& \\
\hline 4 \& NORMAL input terminal \& DC; 2.5V
\[
\mathrm{AC} ;-21.2 \mathrm{dBV}
\] \& NORMAL IC output signal is entered and output to Line Out through output changeover.
\[
\text { G4D7/0:0dB } \frac{1: 3 \mathrm{~dB}}{}
\] \&  \\
\hline 6 \& NORMAL output terminal \& DC; 2.5V
\[
\mathrm{AC} ;-21.2 \mathrm{dBv}
\] \& This is connected to input of NORMAL AUDIO IC. \&  \\
\hline \[
\begin{gathered}
7 \\
9 \\
11 \\
69 \\
71 \\
73
\end{gathered}
\] \& \begin{tabular}{l}
Audio input terminal \\
EXT1_IN(L) \\
EXT2_IN(L) \\
EXT3_IN(L) \\
EXT1_IN(R) \\
EXT2_IN(R) \\
EXT3_IN(R)
\end{tabular} \& DC ; 0V

$A C ;-28.2 d B V$ \& Audio input terminal. \&  <br>
\hline
\end{tabular}

Continued on next page.

LA72670BM
Continued from preceding page.

| Pin <br> No. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 8 | ALC detection terminal for RF converter | DC; | This is ALC detector terminal for RF converter and always ready for operation. |  |
| $\begin{aligned} & 10 \\ & 25 \\ & 35 \\ & 50 \\ & 70 \\ & 79 \\ & \hline \end{aligned}$ | L-GND <br> HEADAMP-GND <br> LOGIC-GND <br> R-GND <br> MTS-GND <br> AUDIO-GND |  |  |  |
| 12 74 | BS monitor input terminal(L) <br> BS monitor input terminal( R ) | DC ; 2.5V <br> $A C ;-21.2 d B v$ | BS monitor input terminal |  |
| 13 48 | Input changeover switch output(L) <br> Input changeover switch output(L) | $\text { DC; } 2.2 \mathrm{~V}$ <br> AC; -21.2 dBv | PB/REC switch output to transform REC and PB signals into DC through a coupling capacitor. |  |
| 14 47 | HiFi input terminal(L) <br> HiFi input terminal(R) | DC ; 2.5V <br> AC; -21.2dBv | HiFi input terminal after passing through a coupling capacitor. |  |
| 16 | $1 / 2 \mathrm{~V}_{\text {CC }}$ terminal | DC; 2.5 V | $1 / 2 \mathrm{~V}_{\mathrm{C}}$ terminal. <br> Serially-set reset is made with the external capacitance $C$ and internal resistance $R(15 \mathrm{k} \Omega)$ at rise of power supply. The reset time $t$ is expressed as follows: $\mathrm{t}=-\mathrm{CR} \ln (0.2)$ |  |

Continued on next page.

LA72670BM
Continued from preceding page.

\begin{tabular}{|c|c|c|c|c|}
\hline \multirow[t]{2}{*}{Pin
No.} \& \multirow[t]{2}{*}{Pin Function Name} \& DC voltage \& \multirow[t]{2}{*}{Function} \& \multirow{2}{*}{Equivalent circuit} \\
\hline \& \& AC level \& \& \\
\hline 17 \& REC mute terminal \& DC; Unsettled \& \begin{tabular}{l}
Two levels control terminal. \\
\(\mathrm{Hi} ; 3.0 \mathrm{~V}\) to Vcc \\
Low ; 0 V to 1.5 V \\
Hi: Pin 26 signal at REC is OFF(Mute).
\end{tabular} \&  \\
\hline 18
45 \& \begin{tabular}{l}
NR waiting DET terminal(L) \\
NR waiting DET terminal( R )
\end{tabular} \& DC; \& \begin{tabular}{l}
Terminal for waiting detector. \\
The recommended external capacity is \(10 \mu \mathrm{~F}\).
\end{tabular} \&  \\
\hline 19 \& \begin{tabular}{l}
NR waiting filter terminal1(L) \\
NR waiting filter terminal1(R) \\
NR waiting filter terminal2(L) \\
NR waiting filter terminal2(R)
\end{tabular} \& DC; 2.5 V \& \begin{tabular}{l}
(Pin 19,Pin 44)between GND;4.7 \(\mu \mathrm{F}\) \\
(Pin 20,Pin 43)between GND; \(0.01 \mu \mathrm{~F}\)
\end{tabular} \&  \\
\hline 21
22

42
42

41 \& | CCA reference terminal(L) |
| :--- |
| NR emphasis terminal(L) |
| NR emphasis terminal(R) |
| CCA reference terminal(R) | \& \[

$$
\begin{aligned}
& \mathrm{DC} ; 2.5 \mathrm{~V} \\
& {\left[\begin{array}{l}
\text { Pin21 } \\
\operatorname{Pin} 41
\end{array}\right]} \\
& \hline \mathrm{AC} ;
\end{aligned}
$$
\] \& By connecting $22 \mu \mathrm{~F}$ between Pin 21, Pin 41 and GND, 4700pF between Pin 22, Pin 42 and GND, form the NR emphasis. \&  <br>

\hline 23 \& | HiFi/Nor selecting terminal |
| :--- |
| (PB) |
| (2)Monitor control terminal at Pin 34 (EE) | \& $\mathrm{DC} ; \mathrm{Nor}$ at 0.1 V

; HiFi at
TRACKING

DC \& | In PB mode, Pin 23 becomes "TRACKING_DC" when inputting HiFi audio signal and becomes "L" when inputting Normal signal. |
| :--- |
| In EE mode, this is used as the terminal for monitor control of Pin 34. $\begin{aligned} & \text { Low(0 to } 0.8 \mathrm{~V}) ; \mathrm{VCO} \mathrm{MIX} \\ & \text { Middle }(1.4 \mathrm{~V} \text { to } 3.6 \mathrm{~V}) \text {; Lch VCO } \\ & \operatorname{High}\left(4.2 \mathrm{~V} \text { to } \mathrm{V}_{\mathrm{CC}}\right) \text {; Rch VCO } \end{aligned}$ | \&  <br>

\hline
\end{tabular}

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 24 27 | HEAD AMP input terminal (Hch) <br> HEAD AMP input terminal (Lch) | DC <br> PB; 2.0V <br> REC; 4.1V | It becomes HEAD AMP input in PB mode. Hch is Pin24, and Lch is Pin 27. <br> And, it becomes supply source of REC current in REC mode. |  |
| 26 | REC CURRENT AMP output terminal | DC; 4.1 V <br>  <br>  <br>  <br> AC; $2.1 \mathrm{Vp}-\mathrm{p}$ | CURRENT AMP output in REC mode. Common input terminal in PB mode. |  |
| 28 | CURRENT AMP ADJUST terminal | $\begin{gathered} \text { DC; } 2.4 \mathrm{~V} \\ \\ \\ \hline \text { AC:1.3Vp-p } \\ \text { (L/R_MIX) } \end{gathered}$ | Terminal for adjusting the recording current. |  |
| 29 | SAP detection terminal | DC ; | Filter terminal in the SPA detector circuit. |  |
| 30 | HiFi/NORMAL detection terminal | DC: <br> Nor; 2.5 V or more <br> HiFi; 2.2V or less | This terminal is for detecting demodulation noise and output which has passed through the primary HPF(fc=140kHz). |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 31 | Monitor terminal | DC ; 2.5 V $\mathrm{AC} ;$ $800 \mathrm{mVp}-\mathrm{p}$ $(\mathrm{L} / \mathrm{R}$ MIX) | FM MIX output(Low), Lch VCO(middle), Rch VCO(High) can be monitored by controlling Pin 23 in REC mode. <br> HOLD and DO pulses can be monitored by Pin 17 in PB mode. BPF of Lch and Rch can be monitored by serial control in PB mode ( $\operatorname{Pin} 17=2.5 \mathrm{~V}$ ). |  |
| 33 | PB AMP output | DC; 2.5 V | Output of HEAD AMP in PB mode. |  |
|  |  | $\begin{aligned} & \text { AC; } \\ & 100 \mathrm{mVp} \text {-p to } \\ & 600 \mathrm{mVp}-\mathrm{p} \end{aligned}$ |  |  |
| 34 | PB FM input terminal | DC; OPEN | Input pin of FM in PB mode. |  |
|  |  | $\begin{aligned} & \text { AC; } \\ & 100 \mathrm{mVp}-\mathrm{p} \text { to } \\ & 600 \mathrm{mVp}-\mathrm{p} \\ & (\mathrm{~L} / \mathrm{R} \text { MIX) } \end{aligned}$ |  |  |
| 37 | Serial data input terminal | $\square \underbrace{5 \mathrm{~V}}_{0 \mathrm{~V}}$ | $\mathrm{Hi} ; 3.5 \mathrm{~V}$ to 5 V <br> Low ; 0 V to 1.5 V |  |
| 38 | CLK input terminal |  | $\begin{aligned} & \mathrm{Hi} ; 3.5 \mathrm{~V} \text { to } 5 \mathrm{~V} \\ & \text { Low } ; 0 \mathrm{~V} \text { to } 1.5 \mathrm{~V} \end{aligned}$ |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 39 | AF SW pulse input terminal |  | Input terminal of AF SW pulse. <br> $\mathrm{Hi} ; 3.5 \mathrm{~V}$ to 5 V <br> Low ; 0 V to 1.5 V |  |
| 40 | MTS MODE OUT | DC; | Detection result output for M.T.S. signal. $\begin{aligned} & \text { STEREO+SAP }: 3.8 \mathrm{~V} \\ & \text { STEREO }: 3.0 \mathrm{~V} \\ & \text { MONO+SAP }: 2.0 \mathrm{~V} \\ & \text { MONO }: 1.0 \mathrm{~V} \end{aligned}$ |  |
| 49 | MUTE control terminal | DC; | Mute control terminal. $\begin{aligned} & \text { Mute_ON : 3.0V to Vcc2 } \\ & \text { Mute_OFF : } 0.0 \mathrm{~V} \text { to } 1.0 \mathrm{~V} \end{aligned}$ |  |
| 51 | FSC IN |  | Input terminal for FSC ( 3.58 MHz ). <br> Recommended operating input level : 150 to 350mVp-p |  |
| 52 | PCREGBGP | DC; | Power supply terminal of M.T.S. block. This power supply does not operate in PB mode. |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| Pin <br> No. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 53 | STEREO PLL FILTER | DC ; 3.8V | LPF terminal for STEREO PLL. |  |
| 55 | PILOT CANCELLER FILTER | DC ; 3.8V | Control terminal of cancel signal for PILOT CANCELLER. <br> DC voltage at this terminal is changed depending on amplitude of pilot signal, and controlled automatically to be small the pilot signal. |  |
| 56 | FM FILTER |  | Filter terminal for making stable DC voltage of FM detection output in SIF part. Normally, use a condenser of $1 \mu \mathrm{~F}$. Increase the capacity value with concerning frequency characteristics of low. This terminal becomes composite signal input terminal of MTS by changing to 5 V at Pin 57. |  |
| 57 | SIF INPUT |  | Input terminal for SIF. The input impedance is about $1 \mathrm{k} \Omega$. Take care about pattern layout of the input circuit, because of causing buzz-beat and buzz by leaking noise signal into the input terminal. (The noise signal depending on sound is particularly video signal and chroma signal and so on. VIF carrier becomes noise signal.) Composite signal of MTS can be input by adding 5 V to this terminal directly. (For test) |  |
| 58 | REG FILT | DC; 4.5V | Filter terminal of reference voltage source. |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 59 | FILTER AUTO ADJ | DC; 3.8V | Loop filter terminal of PLL for automatic adjusting. |  |
| 60 | PILOT DET FILTER | DC; 3.8V | Detection terminal for PILOT detection circuit. |  |
| $\begin{aligned} & 61 \\ & 76 \end{aligned}$ | $\begin{aligned} & \text { PC_DC_MO } \\ & \text { PC_OUT_DBX } \end{aligned}$ | DC; 3.3V | Absorbing the DC offset of signal line by external capacity. |  |
| 62 | PCDCOUT | DC; 3.8V | Absorbing the DC offset of signal line by external capacity. |  |
| 63 | PCDCIN | DC; 3.8V | Absorbing the DC offset of signal line by external capacity. |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 64 | PCDBXIN | DC; 2.6V | Absorbing the DC offset of signal line by external capacity. |  |
| 65 <br> 67 <br> 72 | MAIN V/I CONVERT <br> SPE DET V/I CONVERT <br> WID DEP V/I CONVERT | DC; 3.8V | Converting the voltage of signal into its current by external capacity. |  |
| 66 | SPECTRAL DET <br> WIDE BAND DET | DC; | Connecting terminal of smooth capacity of detection circuit for effective value. |  |
| 75 | PCDCOSPE | DC; | Absorbing the DC offset of signal line by external capacity. |  |

Continued on next page.

LA72670BM
Continued from preceding page.

| PinNo. | Pin Function Name | DC voltage | Function | Equivalent circuit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | AC level |  |  |
| 78 | Line $\operatorname{Out}(\mathrm{R})$ terminal | DC; 4.15V |  |  |
| 80 | Line Out(L) terminal |  |  |  |
|  |  |  |  |  |
|  |  |  |  |  |

Input selecting switch mode table (Switch output signal )

| Sub address | 01 |  |  |  |  |  | HiFi(80Pin <br> Lch Output | HiFi(78Pin) <br> Rch Output | NORMAL OUT (6Pin) | Reference |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Data <br> byte | D8 | D7 | D3 | D2 | D8 | D7 |  |  |  |  |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | TU L | TU R | TU L+TU R |  |
| 2 | 0 | 0 | 0 | 1 | 0 | 0 | EXT1 L | EXT1 R | EXT1 L+EXT1 R |  |
| 3 | 0 | 0 | 1 | 0 | 0 | 0 | EXT2 L | EXT2 R | EXT2 L+EXT2 R |  |
| 4 | 0 | 0 | 1 | 1 | 0 | 0 | EXT3 L | EXT3 R | EXT3 L+EXT3 R |  |
| 5 | 0 | 0 | 0 | 0 | 0 | 1 | TUL | TU R | TU L |  |
| 6 | 0 | 0 | 0 | 1 | 0 | 1 | EXT1 L | EXT1 R | EXT1 L |  |
| 7 | 0 | 0 | 1 | 0 | 0 | 1 | EXT2 L | EXT2 R | EXT2 L |  |
| 8 | 0 | 0 | 1 | 1 | 0 | 1 | EXT3 L | EXT3 R | EXT3 L |  |
| 9 | 0 | 0 | 0 | 0 | 1 | 0 | TUL | TU R | TU R |  |
| 10 | 0 | 0 | 0 | 1 | 1 | 0 | EXT1 L | EXT1 R | TU R |  |
| 11 | 0 | 0 | 1 | 0 | 1 | 0 | EXT2 L | EXT2 R | TU R |  |
| 12 | 0 | 0 | 1 | 1 | 1 | 0 | EXT3 L | EXT3 R | TU R |  |
| 13 | 0 | 1 | 0 | 0 | 0 | 0 | PBL | PB R | TU L+TU R |  |
| 14 | 0 | 1 | 0 | 1 | 0 | 0 | PBL | PB R | EXT1 L+EXT1 R |  |
| 15 | 0 | 1 | 1 | 0 | 0 | 0 | PBL | PBR | EXT2 L+EXT2 R |  |
| 16 | 0 | 1 | 1 | 1 | 0 | 1 | PB L | PB R | EXT3 L |  |
| 17 | 0 | 1 | 0 | 0 | 0 | 1 | PBL | PB R | TUL |  |
| 18 | 0 | 1 | 0 | 1 | 0 | 1 | PBL | PB R | EXT1 L |  |
| 19 | 0 | 1 | 1 | 0 | 0 | 1 | PB L | PB R | EXT2 L |  |
| 20 | 0 | 1 | 1 | 1 | 0 | 1 | PB L | PBR | EXT3 L |  |
| 21 | 0 | 1 | 0 | 0 | 1 | 0 | PB L | PB R | TU R |  |
| 22 | 0 | 1 | 0 | 1 | 1 | 0 | PBL | PB R | - |  |
| 23 | 0 | 1 | 1 | 0 | 1 | 0 | PB L | PB R | - |  |
| 24 | 0 | 1 | 1 | 1 | 1 | 0 | PB L | PB R | - |  |
| 25 | 1 | 0 | * | * | * | * | PB L | PB R | PB L+PB R | Audio-dubbing correspond |

NOTE : * is option. ( 1 or 0 )

## LA72670BM

## (US) MULTIPLEX SERIAL MODE

| SIGNAL | SERIAL SETTING SUB ADDRESS |  |  | TUNER OUT <br> (HiFi input source : Tuner Mode) |  |  | $\begin{aligned} & \text { MODE-OUT } \\ & (\text { Pin51) } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{gathered} \text { D8 } \\ \text { ST/SAP } \end{gathered}$ | $\begin{gathered} \text { D7 } \\ \mathrm{L}+\mathrm{R} / \mathrm{SAP} \end{gathered}$ | D6 Forced MONO | Tuner Lch (Pin13) | Tuner Rch (Pin48) | MODE |  |
| STEREO+SAP | 1 | 0 | 0 | SAP | SAP | SAP | TYP 3.8V |
|  | 0 | * | 0 | L | R | STEREO |  |
|  | 1 | 1 | 0 | L+R | SAP | MULTI |  |
|  | * | * | 1 | L+R | L+R | MONO |  |
| STEREO | * | * | 0 | L | R | STEREO | TYP 3.0V |
|  | * | * | 1 | L+R | L+R | MONO |  |
| MONO+SAP | 1 | 0 | 0 | SAP | SAP | SAP | TYP 2.0V |
|  | 1 | 1 | 0 | L+R | SAP | MULTI |  |
|  | 0 | * | 0 | L+R | L+R | MONO |  |
|  | * | * | 1 | L+R | L+R | MONO |  |
| MONO | * | * | * | L+R | L+R | MONO | TYP 1.0V |

## Output selecting switch mode table

| Sub address | 03 | 02 |  |  |  |  | Line out Lch | Line out <br> Rch | RF MOD OUT | Through Monitor | Through Monitor RF MOD SW |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Data <br> byte | D3 | D6 | D4 | D3 | D2 | D1 |  |  |  |  |  |
| 1 | * | 0 | 0 | 0 | 0 | 0 | HiFi L | HiFi R | HiFi L+HiFi R | OFF | OFF |
| 2 | * | 0 | 0 | 0 | 0 | 1 | HiFi L | HiFi L | HiFi L | OFF | OFF |
| 3 | * | 0 | 0 | 0 | 1 | 0 | HiFi R | HiFi R | HiFi R | OFF | OFF |
| 4 | * | 0 | 0 | 1 | 0 | 0 | MIX L | MIX R | MIXL+MIXR | OFF | OFF |
| 5 | * | 0 | 0 | 1 | 0 | 1 | MIX L | MIX L | MIX L | OFF | OFF |
| 6 | * | 0 | 0 | 1 | 1 | 0 | MIX R | MIX R | MIX R | OFF | OFF |
| 7 | * | 0 | 1 | 0 | 0 | 0 | NORMAL | NORMAL | NORMAL | OFF | OFF |
| 8 | * | 0 | 1 | 0 | 0 | 1 | NORMAL | NORMAL | NORMAL | OFF | OFF |
| 9 | * | 0 | 1 | 0 | 1 | 0 | NORMAL | NORMAL | NORMAL | OFF | OFF |
| 10 | 0 | 1 | * | * | 0 | 0 | BS L | BS R | BS L+BS R | BS | ON |
| 11 | 0 | 1 | * | * | 0 | 1 | BS L | BS L | BS L | BS | ON |
| 12 | 0 | 1 | * | * | 1 | 0 | BS R | BS R | BS R | BS | ON |
| 13 | 1 | 1 | 0 | 0 | 0 | 0 | BS L | BS R | HiFi L+HiFi R | BS | OFF |
| 14 | 1 | 1 | 0 | 0 | 0 | 1 | BS L | BS R | HiFi L | BS | OFF |
| 15 | 1 | 1 | 0 | 0 | 1 | 0 | BS L | BS R | HiFi R | BS | OFF |

1.     * is option.(1 or 0 )
2. MIX L=HiFi L+NORMAL, MIX R=HiFi R+NORMAL

## Through_Monitor SW Table

|  | G2D6 | G3D3 | G1D1 | G2D4D3 | LINE(L) | LINE(R) | RFC_OUT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| EE_MODE | 1 | 0 | - | 00 | Monitor(L) | Monitor(R) | Monitor_MIX |
|  | 1 | 1 | - | 00 | Monitor(L) | Monitor(R) | INSEL_MIX |
|  | 0 | 1 | - | 00 | INSEL(L) | INSEL(R) | INSEL_MIX |
|  | 0 | 0 | - | 00 | INSEL(L) | INSEL(R) | INSEL_MIX |
| PB_MODE <br> HiFi_Tape | 1 | 0 | - | 00 | Monitor(L) | Monitor(R) | Monitor_MIX |
|  | 1 | 1 | - | 00 | Monitor(L) | Monitor(R) | PB_MIX |
|  | 0 | 1 | - | 00 | $\mathrm{PB}(\mathrm{L})$ | $\mathrm{PB}(\mathrm{R})$ | PB_MIX |
|  | 0 | 0 | - | 00 | PB(L) | $\mathrm{PB}(\mathrm{R})$ | PB_MIX |
| PB_MODE <br> Nor_Tape | 1 | 0 | 0 | 00 | Nor | Nor | Nor |
|  | 1 | 1 | 0 | 00 | Monitor(L) | Monitor(R) | Nor |
|  | 0 | 1 | 0 | 00 | Nor | Nor | Nor |
|  | 0 | 0 | 0 | 00 | Nor | Nor | Nor |
|  | 1 | 0 | 1 | 00 | Monitor(L) | Monitor(R) | Monitor_MIX |

Note: When output Monitor to RFC_OUT at Nor_Tape replayed (G2D6:1,G3D3:0), Set G1D1(HiFi auto Distinction) to 1 and select G2D4D3:00 (HiFi).

LA72670BM
Serial data specification ( $\mathrm{I}^{2} \mathrm{C}$ BUS communication )

| Address | Data byte ( Underline is initial setting.) |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{gathered} \text { MSB } \\ \text { D8 } \end{gathered}$ | D7 | D6 | D5 | D4 | D3 | D2 | $\begin{gathered} \text { LSB } \\ \text { D1 } \end{gathered}$ |
| $\begin{gathered} (01) \\ 00000001 \end{gathered}$ | EE/PB/ Audio-dubbing $\frac{00: \mathrm{EE}}{01: \mathrm{PB}}$ <br> 10:Audio-dubbing |  | LINE OUT MUTE $\begin{aligned} & \text { 0:OFF } \\ & \text { 1: ON } \end{aligned}$ | Fixed 0 | REC/EE $\frac{0: E E}{1: \operatorname{REC}}$ | Input sourc $\begin{aligned} & \frac{00: \mathrm{TU}}{01: \mathrm{E}} \\ & 10: \mathrm{E} \\ & 11: \mathrm{E} \end{aligned}$ | selection <br> NER <br> T1 <br> T2 <br> T3 | Auto HiFi DET $\frac{0: \mathrm{ON}}{1: \mathrm{OFF}}$ |
| $\begin{gathered} (02) \\ 00000010 \end{gathered}$ | $\begin{gathered} \text { 01:Lch } \\ \text { 10:TU(R) } \end{gathered}$ |  | Trough Monitor BS $\frac{0: \text { OFF }}{1: O N}$ | Fixed 0 | Output mod $\begin{array}{r} \frac{00}{01} \\ \text { (HiFi } \\ \text { 10: } \mathrm{NC} \end{array}$ | e selection <br> HiFi <br> MIX <br> NOR) <br> RMAL | $\begin{aligned} & 01: \mathrm{L}-\mathrm{ch} \\ & \text { 10:R-ch } \end{aligned}$ | el selection <br> REO <br> -ch <br> -ch |
| $\begin{gathered} (03) \\ 00000011 \end{gathered}$ | $\begin{gathered} \text { VCO carrier } \\ (\mathrm{MHz}) \\ \\ \underline{00: 1.3 / 1.7} \\ 01: 1.4 / 1.8 \end{gathered}$ | $\begin{gathered} \text { REC } \\ 0 \\ 0 \\ 1 \\ 1 \end{gathered}$ | M MIX <br> 9dB <br> 8 dB <br> 0dB <br> 1 dB | $\begin{gathered} \text { DO } \\ \text { ON/OFF } \\ \underline{0: O N} \\ 1: \text { OFF } \end{gathered}$ | LINE OUT Signal level $\frac{0:-9 \mathrm{dBv}}{1:-8 \mathrm{dBv}}$ | Through monitor RFC SW $\frac{0: O N}{1: O F F}$ | HiFi DET selection $\frac{0: \text { TYP }}{1:+10 \%}$ | HiFi DET selection $\frac{0: \text { TYP }}{1:-10 \%}$ |
| $\begin{gathered} (04) \\ 00000100 \end{gathered}$ | $\begin{gathered} \text { SAP_Gain } \\ \frac{0: 0 \mathrm{~dB}}{1: 2 \mathrm{~dB}} \end{gathered}$ | NORMAL INPUT Gain $\frac{0: 0 \mathrm{~dB}}{1: 3 \mathrm{~dB}}$ | $\begin{aligned} & 00: 0 \mathrm{~dB} \\ & 01:-1.5 \mathrm{~dB} \\ & 10:-55 \mathrm{~dB} \end{aligned}$ |  | NORMAL OUT MUTE $\frac{0: \text { OFF }}{1: O N}$ | $\begin{gathered} \text { fsc } \\ (\mathrm{MHz}) \\ \\ \frac{0: 3.58}{1: 4.43} \end{gathered}$ | RF MOD ALC level $\frac{0:-5 d B v}{1:-1 d B v}$ | Fixed 0 |
| $\begin{gathered} \hline(05) \\ 00000101 \end{gathered}$ | ST/SAP $\frac{0: S T}{1: S A P}$ | $\mathrm{SAP} /(\mathrm{L}+\mathrm{R})$ $\frac{0: S A P}{1: L+R}$ | Forced MONO $\frac{0: \text { OFF }}{1: O N}$ | MTS MUTE $\frac{0: \mathrm{OFF}}{1: \mathrm{ON}}$ | EP/SP $\frac{0: S P}{1: E P}$ | Fixed 0 | Fixed 0 | Fixed $0 * 1$ |
| 00000110 | Use in investigating the shipment |  |  |  |  |  |  |  |
| 00000111 | Use in investigating the shipment |  |  |  |  |  |  |  |

Note 1: When FSTVCO is measured, D1 in address 00000101 is set to 1.
Note 2: Address 00000110 and 00000111 are used in investigating the shipment, please send " 0 " data to all bits at refreshing. the data.)

## $I^{2} C$ BUS serial interface specification

## (1) DATA TRANSFER MANUAL

This IC adopts control method(IIC-BUS) with serial data, and controlled by two terminals which called SCL(serial clock) and SDA (serial data).At first, set up the condition of starting data transfer ${ }^{* 1}$, and after that, input 8 bit data to SDA terminal with synchronized SCL terminal clock. The order of transferring is first, MSB (the Most Scale of Bit), and save the order. The 9th bit takes ACK (ACKnowledge) period, during SCL terminal takes " H ", this IC pull down the SDA terminal. After transferred the necessary data, two terminals lead to set up and of data transfer stop condition ${ }^{* 2}$, thus the transfer comes to close.
As a part of transfer data write down to internal memory (V latch system), internal control doesn't change just after the transfer.
*1 Defined by SCL rise down SDA during ' H ' period.
*2 Defined by SCL rise up SDA during 'H' period.

## (2) TRANSFER DATA FORMAT

After transfer start condition, transfers slave address( $1110100^{*}$ ) to SDA terminal, next, sub address( $0000^{* * * *}$ ), control data ${ }^{* 3}$, then, stop condition(See figure 1). And this LSI have a auto address increment function, then, next of slave address transfer, transfer sub address $(\mathrm{n})^{* 4}$, group ( n ) data, after that, group ( $\mathrm{n}+1$ ) and so on.
Data works with all of the bit, transfer the stop condition before stop 8bit transfer, and to stop transfer, it will be canceled only the data of group.
*3 There are 1 to 5 groups.
*4 Pointed date by sub address becomes group No. of next control data.
Fig. 1 DATA STRUCTURE "WRITE" mode

| START <br> Condition | Slave <br> Address | $\begin{array}{l:l:l} \hline R / \bar{W} & \mathrm{ACK} \\ & \underline{\underline{L}}^{5} & \\ \hline \end{array}$ | Sub Address(n):ACK |  | $\text { control data(n+1) }: A C K$ | $\ldots$ | STOP condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

*5 It is called $\mathrm{R} / \mathrm{W}$ bit.
data-1 means data for group-1, data-2 means data for group-2.
(3) INITIALIZE

This LSI is initialized for circuit protection.
The initialization period is decided Pin 16 capacity value by internal impedance $15 \mathrm{k} \Omega$, and shown with
$\mathrm{t}=-\mathrm{CR} \times \operatorname{Ln}(0.2)$. Data cannot be accepted for this period.
$\mathrm{t}=530 \mathrm{~ms}$ at $\mathrm{C}=22 \mu \mathrm{~F}$, In this case, Please transmit data in consideration of the uneven after about 700 ms .

LA72670BM
(4) SERIAL INPUT SIGNAL FORMAT

| Parameter | Symbol | min | max | unit |
| :---: | :---: | :---: | :---: | :---: |
| LOW level input voltage | VIL | -0.5 | 1.5 | V |
| HIGH level input voltage | VIH | 3.0 | 5.5 | $\checkmark$ |
| LOW level output current | IOL | - | 3.0 | mA |
| SCL clock frequency | fSCL | 0 | 100 | kHz |
| Set-up time for a repeated START condition | tSU:STA | 4.7 |  | $\mu \mathrm{S}$ |
| Hold time START condition. After this period, the first clock pulse is generated | tHD:STA | 4.0 |  | $\mu \mathrm{S}$ |
| LOW period of the SCL clock | tLOW | 4.7 |  | $\mu \mathrm{s}$ |
| Rise time of both SDA and SDL signals | tR | 0 | 1.0 | $\mu \mathrm{s}$ |
| HIGH period of the SCL clock | tHIGH | 4.0 | - | $\mu \mathrm{s}$ |
| Fall time of both SDA and SDL signals | tF | 0 | 1.0 | $\mu \mathrm{s}$ |
| Data hold time: | tHD:DAT | 0 | - | $\mu \mathrm{s}$ |
| Data set-up time | tSU:DAT | 250 | - | ns |
| Set-up time for STOP condition | tSU:STO | 4.0 | - | $\mu \mathrm{S}$ |
| BUS free time between a STOP and START condition | tBUF | 4.7 | - | $\mu \mathrm{s}$ |

(5) Definition of timing


■ SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
$\square$ SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
$\square$ In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
$\square$ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
$\square$ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
$\square$ Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
■ Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellctual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of April, 2007. Specifications and information herein are subject to change without notice.


[^0]:    * On board: $114.3 \mathrm{~mm} \times 76.1 \mathrm{~mm} \times 1.6 \mathrm{~mm}$, glass epoxy board.

