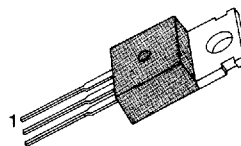


FEATURES

- Lower $R_{DS(ON)}$
- Excellent voltage stability
- Fast switching speeds
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability
- TO-220 Package

TO-220


1. Gate 2. Drain 3. Source

PRODUCT SUMMARY

Part Number	V_{DS}	$R_{DS(on)}$	I_D
IRLZ34	60V	0.07Ω	25A
IRLZ30	50V	0.07Ω	25A

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	IRLZ34	IRLZ30	Unit
Drain-Source Voltage (1)	V_{DS}	60	50	Vdc
Drain-Gate Voltage ($R_{GS}=1M\Omega$) (1)	V_{DGR}	60	50	Vdc
Gate-Source Voltage	V_{GS}	± 15		Vdc
Continuous Drain Current $T_c=25^\circ C$	I_D	25.0		A
Continuous Drain Current $T_c=100^\circ C$	I_D	18.7		A
Drain Current - Pulsed (3)	I_{DM}	100		A
Total Power Dissipation @ $T_c=25^\circ C$	P_D	90		Watts
Derate Above $25^\circ C$		0.60		W/ $^\circ C$
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-55 to $+175$		$^\circ C$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300		$^\circ C$

 Notes : (1) $T_J=25^\circ C$ to $175^\circ C$

 (2) Pulse test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by junction temperature

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage					
	IRLZ34	60	-	-	V	V _{GS} =0V, I _D =250μA
	IRLZ30	50	-	-	V	
V _{GS(th)}	Gate Threshold Voltage	1.0	-	2.0	V	V _{DS} =V _{GS} , I _D =1mA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =15V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-15V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	250	μA	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	1000	μA	V _{DS} =0.8 Max. Rating, V _{GS} =0V, T _C =125°C
R _{DS(on)}	Static Drain-Source On Resistance(2)	-	-	0.07	Ω	V _{GS} =5.0V, I _D =13A
g _{fs}	Forward Transconductance (2)	9.0	-	-	U	V _{DS} ≥ 15V, I _D =13A
C _{iss}	Input Capacitance	-	1400	-	pF	V _{GS} =0V, V _{DS} =25V, f=1.0MHz
C _{oss}	Output Capacitance	-	500	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	200	-	pF	
t _{d(on)}	Turn-On Delay Time	-	25	40	ns	V _{DD} =0.5 BV _{DSS} , I _D =25A, Z _O =18Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	35	50	ns	
t _{d(off)}	Turn-Off Delay Time	-	130	150	ns	
t _f	Fall Time	-	100	120	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	36	nC	V _{GS} =5V, I _D =25A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gs}	Gate-Source Charge	-	25	-	nC	
Q _{gd}	Gate-Drain Charge	-	11	-	nC	

THERMAL RESISTANCE

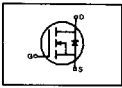
Symbol	Characteristics		All	Units	Remark
R _{thJC}	Junction-to-Case	MAX	1.7	K/W	
R _{thCS}	Case-to-Sink	TYP	0.5	K/W	Mounting surface flat, smooth, and greased
R _{thJA}	Junction-to-Ambient	MAX	62.5	K/W	Free Air Operation

Notes : (1) T_J=25°C to 175°C

(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by max. junction temperature

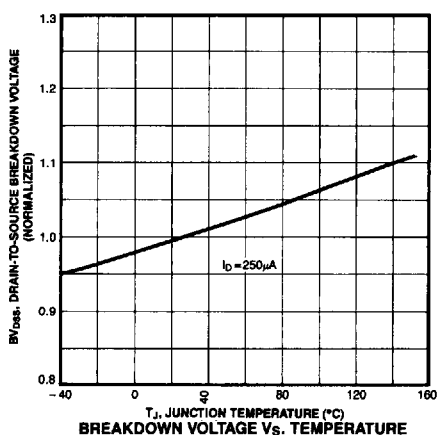
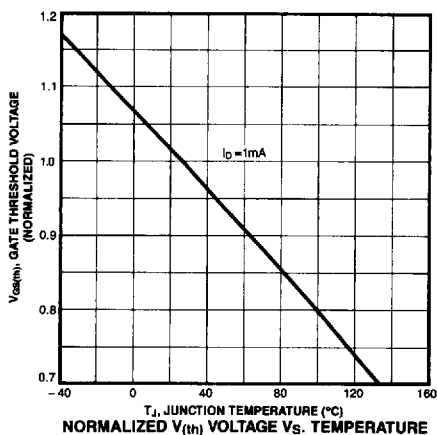
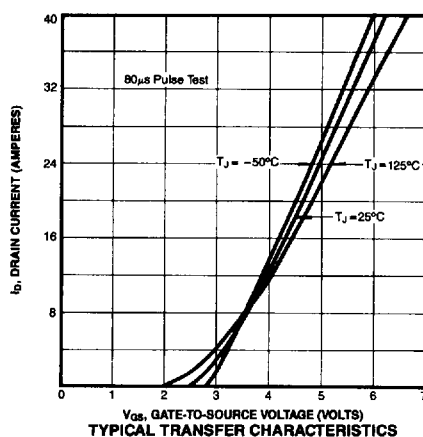
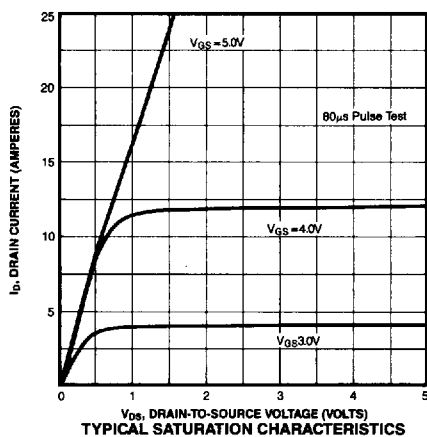
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

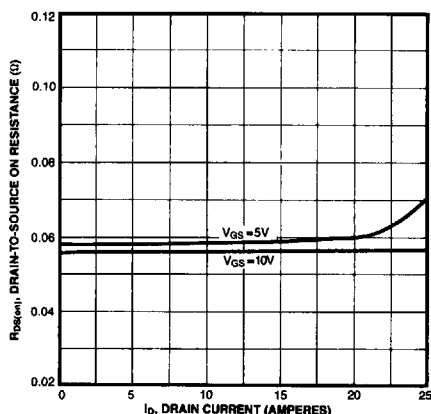
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	25.0	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (Body Diode) (3)	-	-	100	A	
V_{SD}	Diode Forward Voltage (2)	-	1.2	2.5	V	$T_J = 25^\circ\text{C}$, $I_S = 25.0\text{A}$, $V_{GS} = 0\text{V}$
t_{rr}	Reverse Recovery Time	-	-	500	ns	$T_J = 25^\circ\text{C}$, $I_F = 25.0\text{A}$, $dI_F/dt = 100\text{A}/\mu\text{S}$

Notes : (1) $T_J = 25^\circ\text{C}$ to 175°C

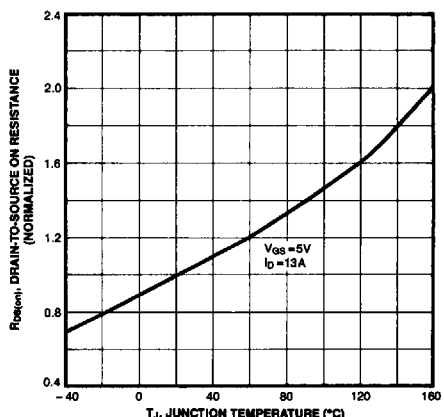
(2) Pulse test : Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by max. junction temperature

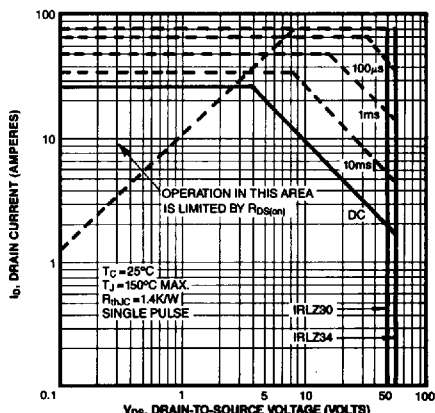




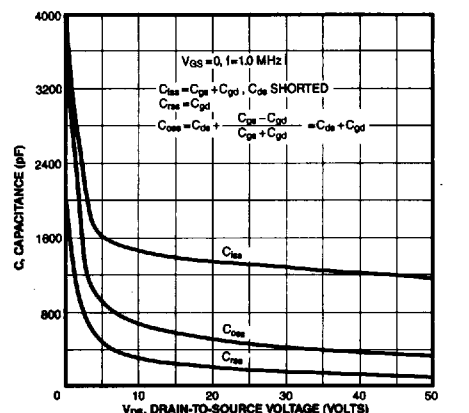
TYPICAL ON-RESISTANCE V_S , DRAIN CURRENT



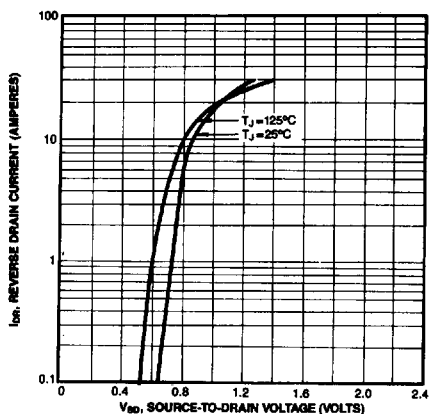
NORMALIZED ON-RESISTANCE V_S , TEMPERATURE



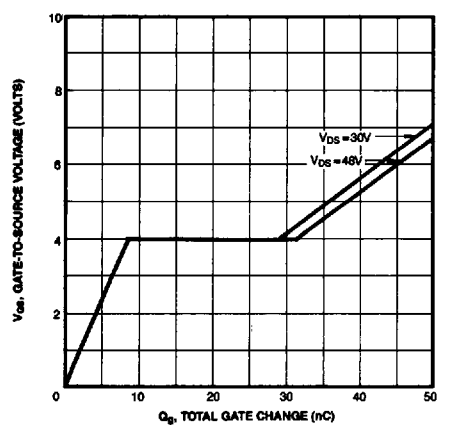
MAXIMUM SAFE OPERATING AREA



TYPICAL CAPACITANCE V_S , DRAIN-TO-SOURCE VOLTAGE



TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE



TYPICAL GATE CHARGE V_S , GATE-TO-SOURCE VOLTAGE