

PBL 3853 Universal Speech Circuit

Description

The PBL 3853 is a bipolar integrated speech circuit with specific parameters making it highly suitable to be used as a line interface and speech circuit in a telephone line powered electronic payphone. Emphasis has been put on low current consumption in the IC thus facilitating that a greater part of the available line current can be used to power other electrical functions in the design. From a minimum line current of 16 mA, 12 mA at 4.5 V can be used to power auxiliary functions. The circuit can optionally create an active impedance towards the telephone line, set by external passive components, to reduce the current consumption for the transmitted signal. The circuit is designed to be used with a low impedance dynamic microphone but can be used with an electret type as well. The receiver drives a low ohm dynamic transducer directly. External clamping diodes are required on the receiver output. Both transmitter and receiver can be muted separately (receiver cut off). Line length regulation of the gain is possible in both receiver and transmitter. It is also possible to use the circuit without regulation. Payphone tones and confidence tone in the receiver at DTMF dialling can be injected directly into the receiver amplifier where their levels are not affected by line length regulation and without the signal going out on the line. All pin numbers refer to DIP package unless otherwise noted.

Key Features

- Low own current consumption
- Can operate both with active and passive impedance towards the line
- Derives a high current from the line for auxiliary functions, 12 mA at 16 mA line current
- Separate mute inputs for transmitter and receiver (receiver cut off)
- Line length regulation possible (line loss compensation)
- Comes in 18 pin DIP and 20-pin SO package
- High line current operation, max. 130 mA
- Especially suitable for payphone applications
- DTMF - confidence tone input
- Excellent RFI performance

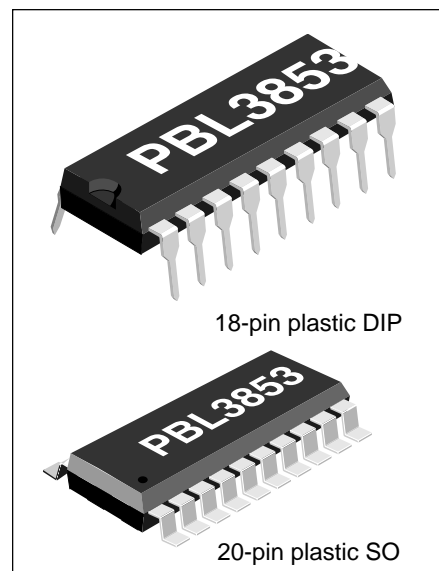
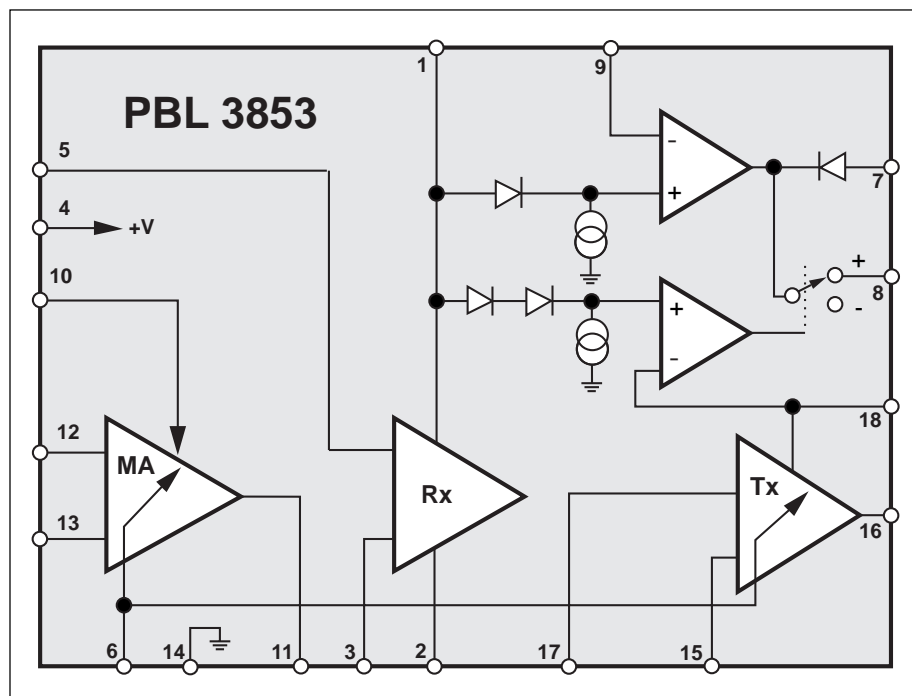


Figure 1. Block diagram. Pin numbers in all figures refer to DIP package.

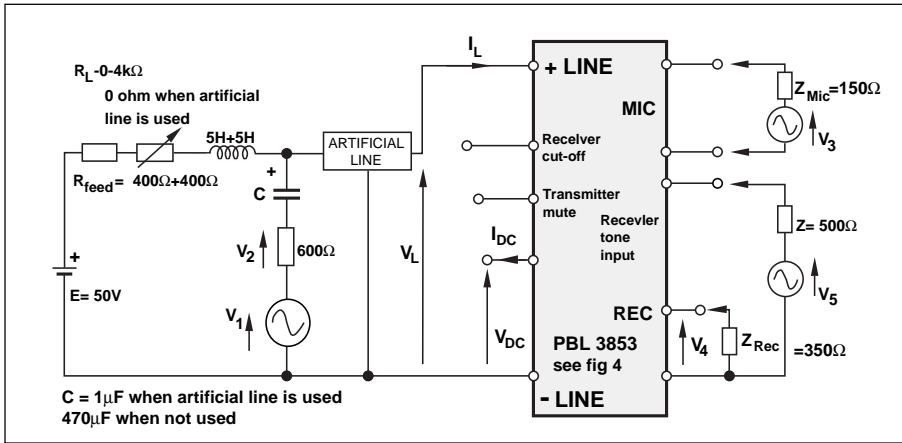


Figure 2. Test set up without rectifier bridge.

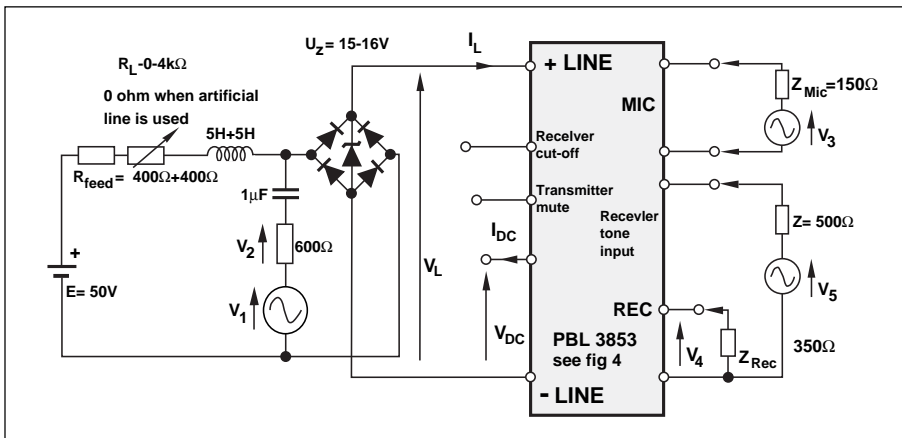


Figure 3. Test set up with rectifier bridge.

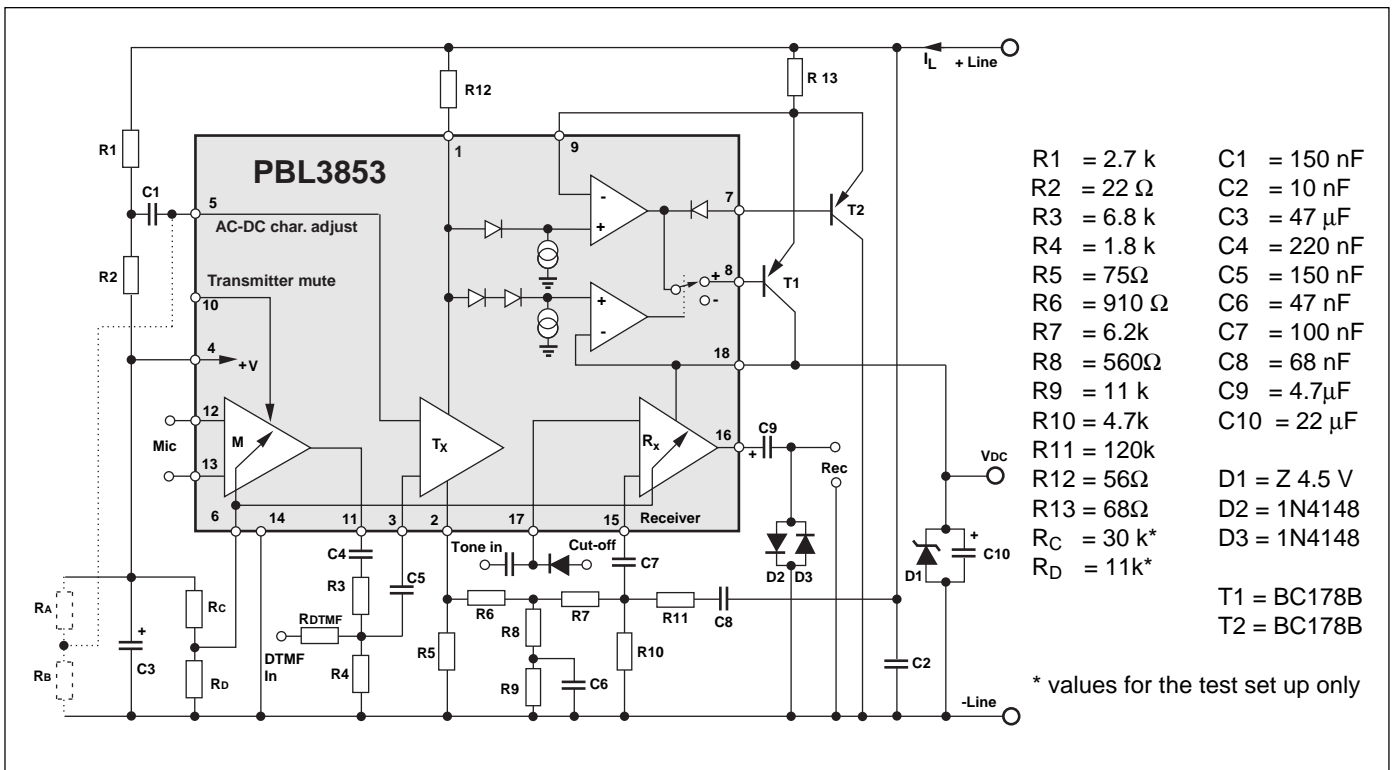


Figure 4. Reference figure with line length regulation. DIP-package.

Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min.	Max.	Unit
Line voltage	V_L			22	V
Continuous operating line current, $T_{amb} = 70^\circ\text{C}$	I_L	Dual In Line package		130	mA
Continuous operating line current, $T_{amb} = 70^\circ\text{C}$	I_L	Small Outline package		100	mA
Input voltage, all inputs	V_{pinx}		-0.5	$V_{pin4} + 0.5$	V
Operation temperature	T_{amb}		-20	+70	$^\circ\text{C}$
Storage temperature	T_{sto}		-55	+125	$^\circ\text{C}$

Electrical Characteristics

$T_{amb} = +25^\circ\text{C}$. No cable and or line rectifier unless otherwise specified.

Parameter	Symbol	Ref fig.	Conditions	Min.	Typ.	Max.	Unit
Terminal voltage		2	$I_L = 20 \text{ mA}$		6		V
Transmitter gain		2	$20 \cdot 10 \log(V_2/V_3), 1 \text{ kHz}$				
			$I_L = 57 \text{ mA}$	41.6	43.0	44.4	dB
			$R_L = 0 \Omega$	41.0	43.0	45.0	dB
			$R_L = 900 \Omega - 2.2 \text{ k}\Omega$	46.0	48.0	50.0	dB
			$I_L = 20 \text{ mA}$	47.0	48.0	49.0	dB
Transmitter gain temp dependence		2	$I_L = 20 \text{ mA}, 0-50^\circ\text{C}$	-1		+1	dB
Transmitter attenuation in mute mode		2		60			dB
Transmitter frequency response		2	200 Hz - 3.4 kHz	-1		+1	dB
Receiver gain		2	$20 \cdot 10 \log(V_4/V_1), 1 \text{ kHz}$				
			$I_L = 57 \text{ mA}$	-17.9	-16.5	-15.1	dB
			$R_L = 0 \Omega$	-18.5	-16.5	-14.5	dB
			$R_L = 900 \Omega - 2.2 \text{ k}\Omega$	-13.5	-11.5	-9.5	dB
			$I_L = 20 \text{ mA}$	-12.5	-11.5	-10.5	dB
Receiver gain temp dependence		2	$I_L = 20 \text{ mA}, 0 - 50^\circ\text{C}$	-1		+1	dB
Receiver tone gain		2	$20 \cdot 10 \log(V_4/V_5), 1 \text{ kHz}$				
			$I_L = 25 \text{ mA}$	4.5	6	7.5	dB
Receiver frequency response		2	200 Hz - 3.4 kHz	-1		+1	dB
Microphone input impedance (Differential)		2	1 kHz	1.3	1.7	2.1	$\text{k}\Omega$
Transmitter input impedance							
Pin 3		4	1 kHz	13	17	21	$\text{k}\Omega$
Transmitter dynamic output voltage		2	200 Hz - 3.4 kHz < 10% distortion				
			$I_L = 20 \text{ mA} - 100 \text{ mA}$		1.8		Vp
			$I_L = 16 \text{ mA}, V_{DC} = 4.5 \text{ V}, I_{DC} = 12 \text{ mA}$		1.4		Vp
			$I_L = 16 \text{ mA}, V_{DC} = 4.0 \text{ V}, I_{DC} = 10 \text{ mA}$		1.8		Vp
Transmitter Max. output voltage		2	200 Hz - 3.4 kHz, $V_3 = 0 - 1 \text{ Vrms}$				
			$I_L = 0 \text{ mA} - 100 \text{ mA}$		3		Vp
Receiver input impedance							
Pin 15		4	1 kHz	28	35	42	$\text{k}\Omega$
Receiver tone input impedance							
Pin 17		2	1 kHz, Not mute. Note 1	7.0	8.8	11	$\text{k}\Omega$
Receiver output impedance		2	1 kHz		6		Ω

Parameter	Symbol	Ref fig.	Conditions	Min.	Typ.	Max.	Unit
Receiver dynamic output voltage		2	200 Hz - 3.4 kHz < 2% distortion $I_L = 20 \text{ mA} - 100 \text{ mA}$ $Z_{rec} = 150 \Omega$		0.6 0.25		V _p V _p
Receiver Max. output voltage		3	Measured with rectifier 200 Hz - 3.4 kHz $I_L = 0 \text{ mA} - 100 \text{ mA}$ $V_1 = 0 - 50 \text{ Vrms}$		0.8		V _p
Transmitter output noise		2	Psoph - weighted rel. 1 V $R_L = 900 \Omega$		-74		dB _{Psoph}
Receiver output noise		2	Psoph - weighted rel. 1 V, with cable: 0 - 5 km, $\varnothing = 0.5 \text{ mm}$ 0 - 3 km, $\varnothing = 0.4 \text{ mm}$		-80		dB _{Psoph}
Mute input voltage at mute (transmit)	V_M	2		2			V
Input voltage at cut off (receive)	V_M	2	Note1	3			V
DC-supply current		2	$I_L = 16 \text{ mA}, V_{DC} = 4.5 \text{ V}$		12		mA

Note: 1. This input has three functions (see page 7). No input should be set on higher level than +V.

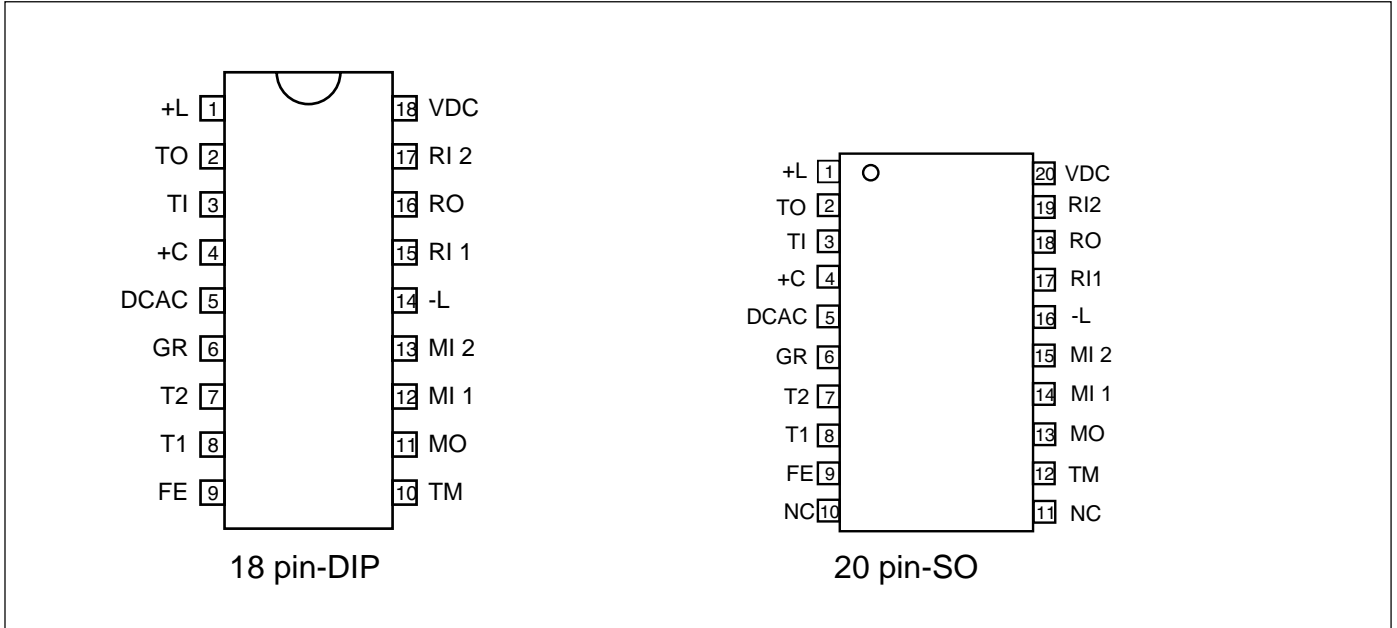


Figure 5. Pin configuration.

Pin Description

DIP	SO	Symbol	
1	1	+L	Output of the transmitter (+Line side)
2	2	TO	Output of the transmitter (side tone signal)
3	3	TI	Input of the transmitter amplifier
4	4	+C	The circuit supply (sinks ~ 0,3 mA)
5	5	DCAC	Adjustment for DC-char. and AC imp. to line
6	6	GR	Gain regulation starting point setting
7	7	T2	Output for transistor 2, active when voltage on the line is too low for VDC
8	8	T1	Output for transistor 1, active when charging current into VDC's reservoir capacitor
9	9	FE	Feedback
	10	NC	No connection
	11	NC	No connection
10	12	TM	Transmitter mute input
11	13	MO	Microphone amplifier output
12	14	MI 1	Microphone amplifier inverting input
13	15	MI 2	Microphone amplifier non inverting input
14	16	-L	Negative terminal of the circuit
15	17	RI1	Receiver amplifier input (gain control)
16	18	RO	Receiver amplifier output
17	19	RI 2	Receiver amplifier input for cut-off, see page 7.
18	20	VDC	VDC supply terminal

Functional Description Design Procedure

The first decision to make is, how much current is needed at what VDC and how much line current is available at longest line length.

1. Set the circuit impedance to the line, either active or passive. C_3 should be big enough to give low impedance compared with R1 in the telephone speech frequency band. Too large C_3 will make the "start up" slow.
2. Set the DC-characteristic that is required in the PTT specification, or in case of a system telephone design, in the PABX specification (R5).
3. If the line length regulation (line loss compensation) is used, set the attack point where it should start (RC and RD). Using the line length regulation makes it in most cases easier to achieve the gain/line length mask in both transmitter and receive function. Note, that in some countries the line length regulation is not allowed.
4. Set the transmitter gain and frequency response. See text for the clipping feature.
5. Set the receiver gain and frequency response.
6. Adjust the side tone balancing network.
7. Apply the RFI suppression components in case necessary. In two piece telephones the often "helically" wound cord acts as an aerial where especially the microphone input with its high gain and input impedance is the more sensitive.

Impedance to the Line

The AC-impedance to the line is set by R1 (+ R2 if active impedance is used) and C_2 . See figure 4. The circuits relatively high parallel impedance will influence it to some extent. At low frequencies the influence of the C_3 can not be neglected. Series resistance of the C_3 that is dependent on temperature and quality will cause that some of the line signal will enter pin 4 and generate a closed loop in the transmitter amplifier that will create an active impedance thus lowering the impedance to the line. The impedance at high frequencies is set by C_2 that also acts as a RFI suppressor.

In many specifications the R1 is specified as a complex network. See figure 6 b) in the example. In case a) the error signal entering pin 4 is set by the ratio $\approx RS/R1$ (909 Ω Swedish spec.), where in case b) the ratio at high frequency will be $RS/220$ because the 820 Ω resistor is bypassed by a capacitor. To help up this situation the complex network capacitor is connected directly to ground, case c) making the ratio $RS/(220+820)$ and thus lessening the influence of the error signal. To save current the circuit can be implemented to have an active impedance to the line, the level is set by resistors R1 and R2. When an active impedance is used the transmitter (see figure 16) amplifier does not feel its own active output-impedance thus using less current to create output swing to the line. Case c) above can not be used together with active impedance.

Do not use the active impedance if not necessary, it complicates things greatly.

A full mathematical expression is found under Detailed Description.

DC - Characteristics

The DC - characteristic that a telephone set has to fulfill is mainly given by the network administrator. Following parameters are useful to know when the DC behaviour of the telephone is to be set:

- The voltage of the feeding system
- The line feeding resistance $2 \times \Omega$
- The maximum current from the line at zero line length
- The minimum current at which the telephone has to work (basic function)
- The lowest and highest voltage across the telephone

The DC-characteristic of the circuit is a function of the voltage on pin 4. There is also a possibility to adjust the DC-characteristic with resistors (dc-voltage) at pin 5 (RA and RB in figure 4). Note that altering the DC-characteristic slope will also influence the line length regulation (when used) and thus the gain of both transmitter and receiver. A closer mathematical study is done under Detailed Description.

Line Length Regulation

The line length regulation is to compensate the gain in both transmitter and receiver with changing line length (impedance). The dynamic range of regulation is ≈ 6 dB. The starting point of the regulation can be set by RC and RD that take the information from the circuits supply voltage which actually mirrors the line current value in voltage. In case line length regulation is not required it can be omitted either in the high or in the low gain mode (6 dB range of regulation).

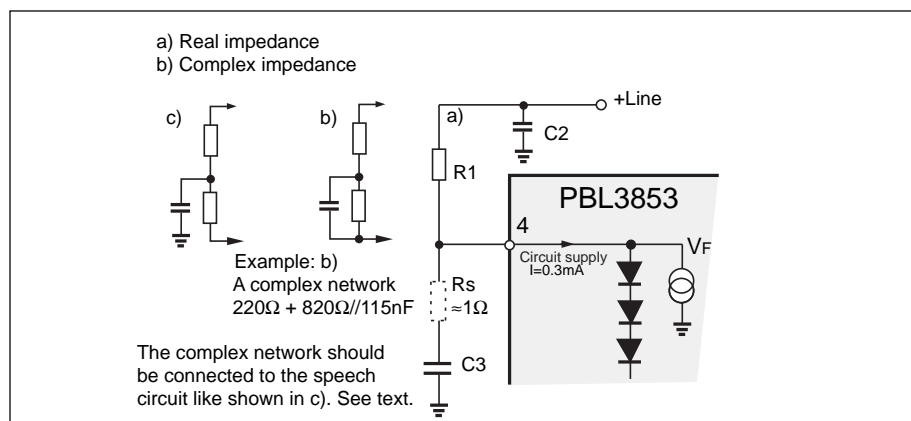


Figure 6. AC-impedance, to the line.

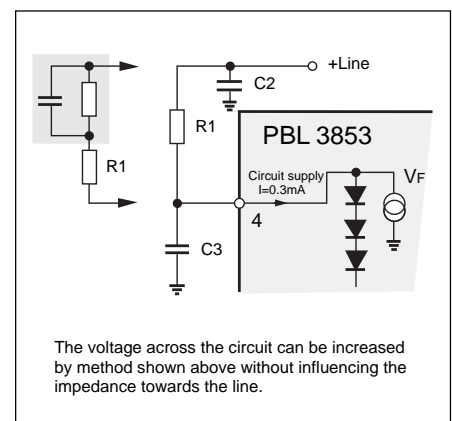


Figure 7. Adjusting voltage level across the circuits.

Connecting the pin 6 to ground disables the line length regulation in the high gain mode, whereas setting the pin 6 on 1.2 V dc level or higher disables the line length regulation in low gain mode.

Microphone Amplifier

The microphone amplifier is primarily designed to be used with dynamic or magnetic microphones but can be used with buffered elctret microphones as well. The input of the amplifier is balanced for good CMRR. The gain of the amplifier can be regulated with the line length. The microphone amplifier has a mute input of its own. Pin 10.

It is possible to use the microphone amplifier as a limiter (added to the limiter in the transmitter output stage) of the transmitted signal. The positive output swing is then limited by the peak output current of the microphone amplifier. The negative swing is limited by the saturation voltage of the output amplifier. The output of the amplifier is DC-wise at internal reference level (1.2 V). The lowest negative level for the signal is reference minus one diode and sat.transistor drop. $(1.2-0.6-0.1=0.5\text{ V})$. The correct clipping level is found by determining the composite AC- and DC-load that gives a maximum symmetrical unclipped signal at the output. This signal is then fed into the transmitter amplifier at a level that renders a symmetrical signal clipping on the line (adjust with ratio R_3, R_4). The total transmitter gain can then be adjusted with the load of the electret microphone buffer amplifier. See figure 9.

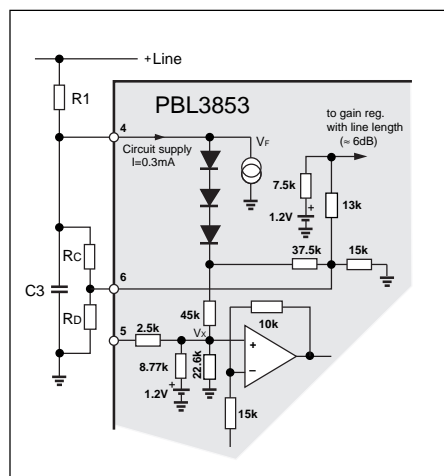


Figure 8. Fixed gain and line length regulation.

Transmitter Amplifier

The transmitter consists of two stages. The output of the transmitter drives a signal with adjustable impedance into the line. The signal at pin 1 (positive line) is 180 degrees out of phase with the signal at pin 2 from where the side tone compensation signal is taken. The transmitter has two inputs. An input at pin 5, where both active impedance and DC-characteristic can be adjusted. Another input at pin 3, which is the microphone signal input, where the filter for the transmitter frequency characteristic is placed. Pin 3 is at the same time used as a summing point to all other audio signals that are transmitted into the line (DTMF tones, hands free signal...). See figure 4.

Receiver Amplifier

The receiver gets its input signal from the summing point where the side tone cancellation takes place. The output drives single sided a low Ω (150 Ω) acoustical transducer. Diodes for absorbing acoustical shock must be provided externally. The amplifier has two inputs. One of them can be regulated with line length, the other has fixed unity gain. The latter can be used to cut off the receiver by forcing the input positive and is also useful for injecting the DTMF confidence tone into the receiver. The receiver amplifier is powered from the regulated DC-supply at pin 18.

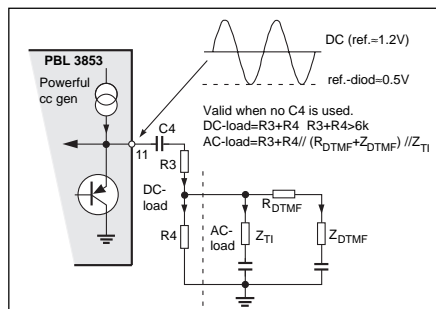


Figure 9. Microphone amplifier output clipping, level.

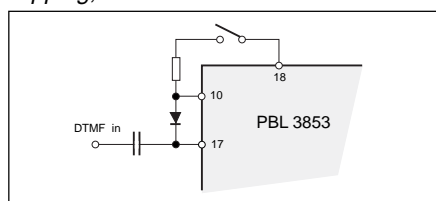


Figure 10. Common receiver and transmitter mute arrangement.

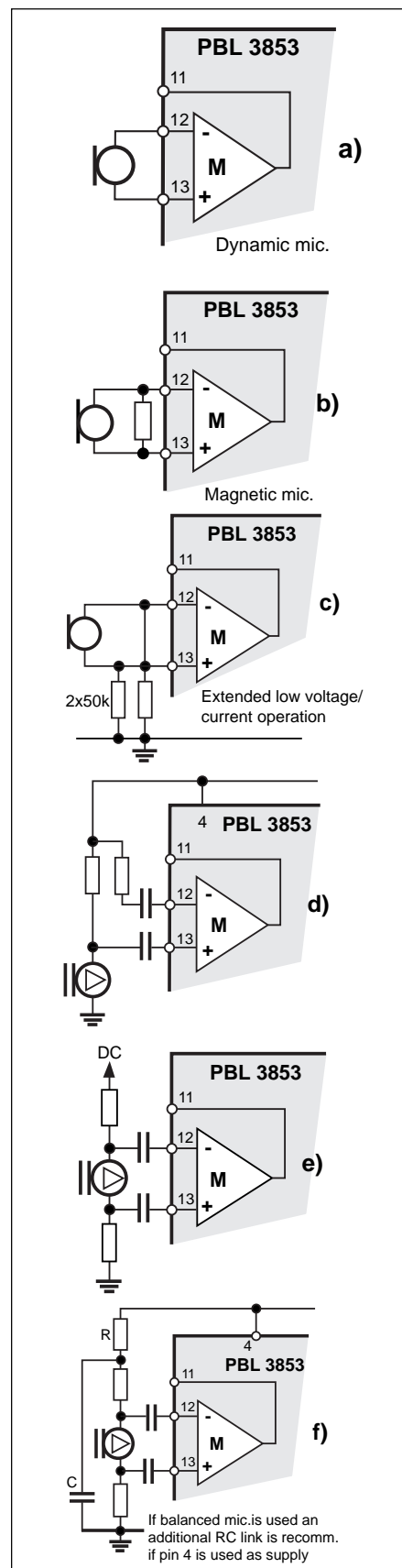


Figure 11. Microphone solutions.

Detailed Description: Expressions for AC-characteristics

Z_{line} is the impedance that the telephone sees in the telephone line. Z_p is the impedance formed by the passive (see fig.15.) elements in the telephone set. R_1, R_2 and C_2 . $1/Z_p = 1/R_1 + 2\pi f C_2$ (R_2 can be omitted if $R_2 \ll R_1$)

Active impedance:

The following expression is derived from fig.12 if $1/Z_{Load} = 1/Z_p + 1/Z_{line}$

$$\left(-\frac{V_Y}{R_5} \left(1 + \frac{R_{12}}{R_{13}} \right) Z_{Load} \gamma + \delta V_S \right) \alpha = V_Y \dots\dots\dots 1$$

Figure 12 and the expression above render the following transmitter gain expression:

$$Transmitter\ gain = \frac{V_L}{V_S} = \frac{V_Y}{V_S} \frac{-1(1+R_{12}/R_{13}) Z_{Load}}{R_5} = \frac{-\alpha \delta (1+R_{12}/R_{13}) Z_{Load}}{R_5 + (1+R_{12}/R_{13}) Z_{Load} \alpha \gamma} \dots\dots\dots 2$$

Figure 12 renders the following expression of the impedance across the circuit:

$$Impedance\ across\ the\ circuit = \frac{V_L}{I_{Load}} = \frac{1}{1/Z_{Load} + (1+R_{12}/R_{13}) Z_{Load} \alpha \gamma / R_5} \dots\dots\dots 3$$

Figure 12 renders also the following expression of the impedance across R_5 :

$$Impedance\ across\ R_5 = \frac{V_Y}{I_1} = \frac{1}{1/R_5 + 1/((1+R_{12}/R_{13}) Z_{Load} \alpha \gamma)} \dots\dots\dots 4$$

$$From\ figure\ 13\ it\ is\ possible\ to\ identify\ \delta: \delta = A_1 \frac{R_4 // 15000}{R_4 // 15000 + R_3} \frac{10}{15} \dots\dots\dots 5$$

From figure 13 it is possible to identify α : $\alpha = 5.8$

From figure 13 it is possible to identify γ :

$$\gamma = 0.7 \frac{R_2 // 8000}{R_2 // 8000 + R_1} \left(1 + \frac{10000}{15000 + R_3 // R_4} \right) \dots\dots\dots 6$$

These expressions together by omitting the impedance of the line Z_{line} from the expression, will render the impedance of the telephone set towards the line. The telephone set impedance towards the line is:

$$\frac{V_L}{I_{Load}} = \frac{1}{\frac{1}{R_1 + 2\pi f C_2} + 0.7 \frac{R_2 // 8000}{R_2 // 8000 + R_1} \frac{5.8}{R_5} \left(1 + \frac{10000}{15000 + R_3 // R_4} \right) \left(1 + \frac{R_{12}}{R_{13}} \right)} \dots\dots\dots 7$$

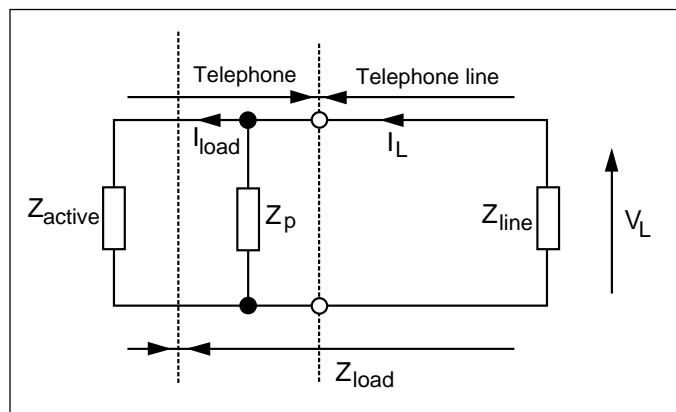


Figure 12.

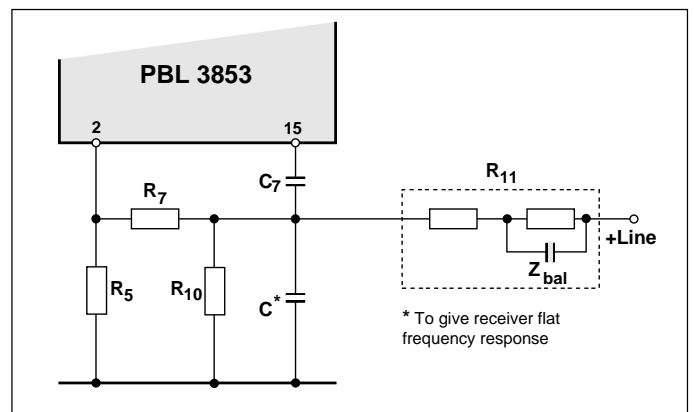


Figure 13.

Expression for DC-characteristics. from figure16, (empirical).

$$V_F = V_L - \left((V_L - 1.3 - V_{DC}) 0.07 \cdot 10^{-3} + 0.6 \cdot 10^{-3} \right) (R_1 + R_2) \dots\dots\dots 8$$

$$V_X = \frac{(V_F - 2.0) / 45 + 0.14}{0.18} \dots\dots\dots 9$$

$$I_L = \frac{5.8 (V_X - 1)}{R_5} \left(1 + \frac{R_{12}}{R_{13}} \right) + \frac{0.65}{R_{13}} \dots\dots\dots 10$$

If the resistors R_A and R_B are connected in the circuit as in figure 16, the V_x will have somewhat more complex expression:

$$V_X = \frac{(V_F - 2.0) / 45 \cdot 10^3 + 0.14 \cdot 10^{-3} + (Z V_L) / (2.5 \cdot 10^3 R_A)}{0.18 \cdot 10^{-3} + 1 / 2.5 \cdot 10^3 - Z (2.5 \cdot 10^3)^2} \dots\dots\dots 11$$

$$\frac{1}{Z} = \frac{1}{R_A} + \frac{1}{R_B} + \frac{0.65}{2.5 \cdot 10^3}$$

If the resistors R_A and R_B are set to infinity in this expression it will take the same form as above.

NOTE

1. These expressions are built on the assumption that the β of the transistors is infinite and that the influence of capacitors except C_2 can be neglected.
2. These expressions are built on an ideal situation and are only for help to understand

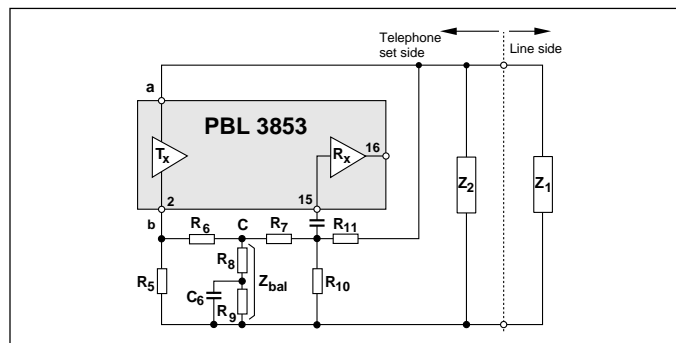


Figure 14.

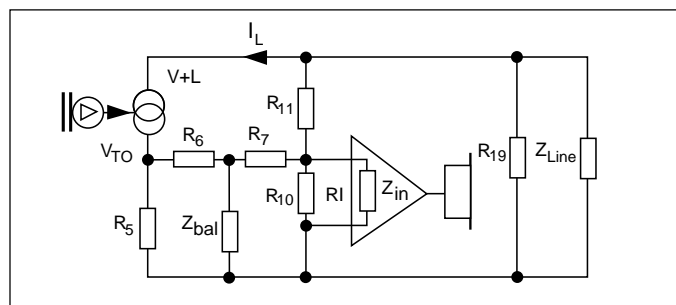


Figure 15.

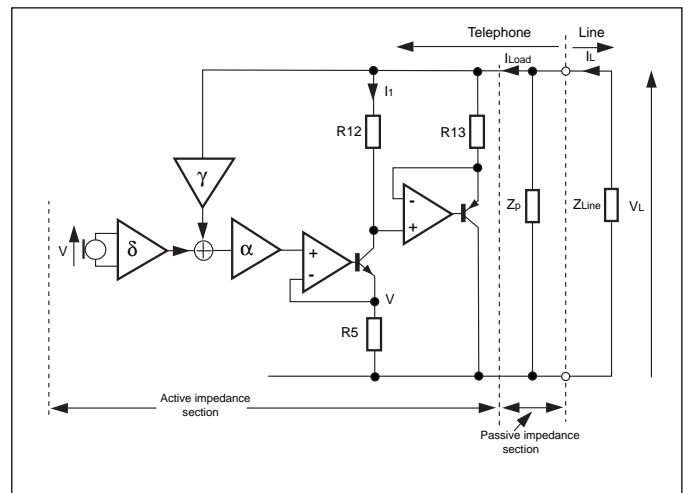


Figure 16.

MUTE and Cut-off Function

The receiver mute (pin 17) is a cut-off function. The normal receiver amplifier is cut-off but a second amplifier is activated to let the DTMF or the payphone signal injected to pin 17 get through to the earphone. The input level for cut-off:

$$V \text{ cut-off} = R_x \text{ output } (V_{DC}/2) + 2x V \text{ diode} \approx 2.2 \text{ V} + 1.3 \text{ V} \approx 3.5 \text{ V}$$

The signal for DTMF confidence tone in the earphone must be injected to pin 17 at correct DC level or via a capacitor.

The mute signal is taken from pin 18 via a resistor in series with a diode. The diode is needed in order not to disturb the DC-level at pin 17 in not muted condition.

The transmit mute (pin 10) can be taken from the same point (pin 18) with a series resistor (see figure 10).

Side Tone Suppression

(See figure 13, 14)

The side tone suppression is achieved by adding two signals $V+L$ and V_{TO} that are in opposite phase at input R1. Because of the complex line impedance Z_{line} , V_{TO} must be compensated by Z_{bal} in order to get the correct level and phase for the signal to the summing point. Maximum compensation is achieved when following conditions are fulfilled:

$$\frac{R_5}{R_6} = \frac{R_7}{R_{11}} Z_{line} // (R_1 + R_2) \left\{ \frac{1}{R_9} + \frac{1}{R_{11}} + \frac{1}{R_{bal}} \right\}$$

$$R_{11} \gg Z_{line} // (R_1 + R_2)$$

$$R_6 \gg R_5$$

$$Z_{bal} = R_8, R_9, \text{ and } C_6$$

C_{10} is omitted in the equation

$$Z_{bal} = \frac{1}{\frac{R_5 R_{11}}{R_6 R_7} \cdot \frac{1}{Z_{line} // (R_1 + R_2)} - \frac{1}{R_6} - \frac{1}{R_7}}$$

Following should be noted when designing the side tone network:

The side tone network impedance in parallel with the R_5 should not be too low. This does influence the transmitter gain and frequency response. ($Z_{bal} + R_6 \gg R_5$)

R_7 should not be low compared with Z_{bal} this will influence the receiver frequency response. ($R_7 \gg Z_{bal}$)

The side tone network impedance, parallel with the receiver input impedance Z_{in} , should not be too high compared with Z_{in} this influencing the spread in the receiver gain. $Z_{in} \gg$ side tone network impedance, $R_{11}/R_{10} // (R_7 + R_6 // Z_{bal})$.

Maximum compensation without any assumption is obtained when following condition is fulfilled:

$$\frac{R_5}{R_5 + R_6} = R_7 \frac{Z_{line} // (R_1 + R_2)}{Z_{line} // (R_1 + R_2 + R_{11})} \left\{ \frac{1}{R_6} + \frac{1}{R_7} + \frac{1}{Z_{bal}} - \frac{R_5}{R_5 + R_6} \right\}$$

In practice Z_{line} varies with the line length and the feeding system parameters. Therefore Z_{bal} should be chosen to give a satisfactory side tone suppression at an average line length. An other method is to make R_{11} complex. See figure 13. This will be advantageous in case the R_5 is low Ω (10-39 Ω) because this coupling will give +6dB more signal for the side tone balancing. Warning! At low values of R_5 the circuit will have an insufficient overcurrent protection. A over voltage protection with lower limiting level has to be used across the circuit. It also will make it possible to implement a better working volume control for the earphone. There will be some disadvantages as: More difficult to trim and need of closer tolerance components.

A Short Guidance for Understanding the Side Tone Principle

(See fig. 14)

Assume the line impedance to be 600 Ω .

Z_1 = Line impedance

Z_2 = The telephone set impedance
600 Ω

$Z_1 // Z_2 = 300\Omega$

R_5 will have a certain value 39-100 Ω to give the telephone a specified DC-characteristic and overcurrent protection.

Assuming that this DC-characteristic requires $R_5=60\Omega$, hence it will be 1/5 of the $Z_1 // Z_2$. This will also give 1/5 of the ac-signal that is on the line across R_5 .

Note that the signals at points a and b are 180 degrees off phase.

$$10 \times R_5 \approx R_6 + Z_{bal} \quad \text{Note\#1}$$

$$R_6 \approx Z_{bal} \quad \text{Note\#2}$$

The ac-signal at point c is now 1/10 of the signal on the line because it is further divided by two from point b.

$$(R_6 \approx Z_{bal})$$

Hence $10 \times R_7 \approx R_{11}$ to satisfy the balancing criteria.

R_{10} is to set the receiver gain. (can also be a volume control potentiometer.)

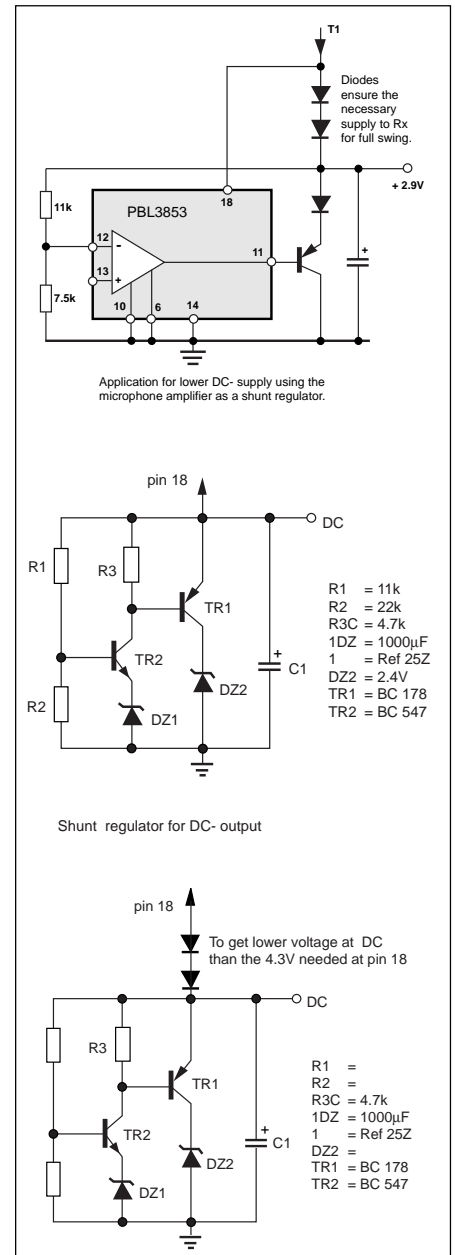


Figure 17.

Note#1 These values ensure that the frequency behaviour of the transmitter is not influenced. With the ratio 1/10 the influence is 1 dB, and with ratio 1/20 it is 0.5 dB.

Note#2 If the R_6 is made low ohmic compared with Z_{bal} , it will load the latter and result in a bad side tone performance, again if the R_6 is made high ohmic compared with Z_{bal} will result in a low signal to balance the side tone with and make the balancing difficult.

Making any of the impedances unnecessary high will make the circuit sensitive to RFI. All values given here are approximate and serve as starting entities only. The final trimming of side tone network is a cut and try proposition because a part of the balance lies in the acoustical path between the microphone and earphone.

DC-Supply

In general

The most significant feature of this speech circuit is its ability to draw current from the line, that is used to a DC-supply for auxiliary electronics and for the receiver amplifier on the chip, under maintaining the line impedance. This function is boosted by two external high β PNP transistors T1 and T2, and internal amplifier and a comparator.

In detail

The circuit is designed such that it supplies current first into the DC supply output (priority) so that the micro-computer which is controlling the auxiliary circuits and functions will have power. The rest of the line current is going to the speech function supply at pin 4 (≈0.3mA) and through the transmitter. The speech function needs 4mA min. to operate to full specification. The current to the DC supply is set by expression:

$I_{DC} = (I_1 \times R_{12} + VD)/R_{13}$ where $VD = 0.65V$, $I_1 \approx 4 - 0.3 = 3.7mA$ and controlled with an amplifier through the voltage between terminals 1 and 9. A certain increase in the voltage (ΔV_L) across the resistor R_{12} will result in an equal increase in the voltage across R_{13} which gives the expression:

$\Delta I_L = \Delta I_1 (1 + R_{12}/R_{13})$. From this can be seen that an increasing line current will also partly increase the DC-supply current.

The comparator will compare the voltage at terminal 18 plus $2 \times VD$ (≈ 1.3V), with the voltage at terminal 1, whether it is higher or lower. The line current will be distributed as follows: The comparator will route the line current to the DC-supply until the set current is reached after which the exceeding line current will be divided between the transmitter and the DC-supply according to the expression $\Delta I_L = \Delta I_1 (1 + R_{12}/R_{13})$.

Note: That the DC-supply charge current coming from the line, given by the expression VD/R_{13} , because of the constant voltage difference of VD between the pins 1 and 9, makes it possible to use the DC-supply for external electronics at low line currents even before the speech function. It can be seen in the figure 16 that the line voltage at low line currents is given by $V_L = V_{18} + 2 VD + I_1 \times R_{12} \approx 6.0V$. ($V_{18} = 4.5V; I_1$ small)

The DC-supply level is monitored by a circuit that will cut the charge current whenever the line voltage with the modulated signal reaches a value $2 \times$

diode drops below the DC level. The current will be returned via TR2 to ground thus maintaining the correct impedance towards the line and making it possible to transmit a swing to the line that has lower level than the DC. See figure 18. When the line voltage without signal reaches the TR2 monitor level the charge to the DC-supply will be cut off whereby the receiver dies because it draws its current from the DC-supply. In case that the dc-characteristic is set such that the current will come first to the minimum working level (≈ $I_{DC} + 4 mA$) then the transmitter will die first because of the I_{DC} priority.

Example:

A payphone is to be designed. According to the specification the minimum line current is 20mA at 6V inclusive the bridge for the phone to work with all its auxiliary functions. The auxiliary functions will need as much current as it is possible to draw from the line and the worst case is naturally at the longest line length. The speech section of the circuit with the earphone amplifier needs ≈ 4mA for function. In this case the highest possible IDC with the longest line will be $20 - 4 = 16mA$.

$$I_L = I_1 + I_{pin4} + I_{DC}, \text{ see figure 16}$$

$$\text{or}$$

$$I_1 = I_L - I_{pin4} - I_{DC}$$

$$\text{or}$$

$$I_{DC} = I_L - I_{pin4} - I_1$$

$$\text{and}$$

$$I_1 R_{12} + V_D = I_{DC} R_{13}$$

Values for R_{12} and R_{13} :
$$\frac{(I_1 \cdot R_{12}) + V_D}{R_{13}} = I_{DC}$$

The speech function current consists of two branches I_1 and the current to pin 4 which is ≈ 0.3mA thus the current I_1 through R_{12} will be $4 - 0.3 = 3.7mA$. V_D is taken to be 0.650V. Choose $R_{12} = 50 \Omega$ to start with. The voltage drop across this resistor is translated to voltage drop across R_{13} which in its turn will steal available voltage from V_{DC} . These values render a $R_{13} = 51.6 \Omega$.

An increasing available line current will be divided between I_1 and I_{DC} as follows:

$$\Delta I_1 R_{12} = \Delta I_{DC} R_{13}$$

$$\Delta I_L = \Delta I_1 + \Delta I_{DC}$$

$$\Delta I_1 = \Delta I_L / (1 + R_{12}/R_{13})$$

$$\text{or}$$

$$\Delta I_{DC} = \Delta I_L / (1 + R_{13}/R_{12})$$

$$\Delta I_L = \Delta I_1 (1 + R_{12}/R_{13}) = \Delta I_{DC} (1 + R_{13}/R_{12})$$

Simply, when the voltage drop across R_{13} reaches one diode drop (0.650V) then the current will be divided between I_1 and I_{DC} as

$$1 : (R_{13}/R_{12}) = 1 : (51.6/50) \approx 1 : 1.$$

Comments to the Reference Figure for PBL 3853 Test Set up (fig.4) Regarding the External DC-Supply (V_{DC}).

This schematic is a specific application of the PBL 3853, where the main objective is to optimize the usage of the line current so that of 16 mA line current 12 mA can be taken out to feed auxiliary functions. Typical line voltage 6.0 V at 20 mA line current (with a transmitter signal swing of 1.8 Vpeak).

It would be possible to save some more current ($\approx 50\text{mA}$) by instead of increasing the DC-characteristic towards the line with low ohmic R_c and R_d (the sum 41k) make these larger (68k+33k=101k) and lift the DC-char. with R_b instead (see fig.7). The gain of this is questionable because the need of an additional resistor.

It ought to be understood that these 12 mA charge current into VDC out of 16 mA line current can only be achieved in no signal condition, both transmitter and receiver. In case there is a transmitted signal above a certain (low) amplitude, it will cause breaks in the 12 mA VDC charge current during a part or the whole time of the negative half periods of the signal on the line. This means that the filtered VDC will have a lower usable current output. Is the said signal on the line large, it will cause an absence of the charge current half of the time and the useful current will sink to 6 mA. (See fig. 15). A similar thing will happen at receiving as at transmitting but on top of that, the current to the receiver which

is taken from V_{DC} will leave even less current available from V_{DC} . The speech functions and with them the V_{DC} output will die below 16 mA line current. There is an unintentional effect that might cause puzzlement. Just below 16 mA line current the VDC will come into function and will be operating when a high signal is on the line. With a high signal on the line half of the set current, in this case 6 mA, can be taken out. The possible available current out from V_{DC} will increase with increasing line current and the margin to, that a signal on the line has the effect of decreasing the available current out from V_{DC} , will increase in the same extent as the line voltage increases.

Figure 4 shows only one of the many ways to use the circuit to generate the V_{DC} supply. The conditions for dimensioning the V_{DC} are set by the available line voltage and line current, what voltage respectively current the V_{DC} supply has to deliver or actually the difference between the line voltage and V_{DC} out respectively line current contra the possible output current from V_{DC} . It is possible to minimize these differences somewhat but it requires more complex solutions, on the other hand if the requirement to keep the voltage and current difference as small as possible is not of utmost importance it is possible to create simpler solutions than what is shown in the figure 4.

In case there is a need to minimize the voltage difference between line and V_{DC} it can be influenced as follows. To start with, the level that sets which way the

current will go, either through T1 to VDC or through T2 to ground, has to be altered. It is done easiest by adding a shunt regulator between the collector of T1 and pin 18 on the circuit. The VDC is still taken at the collector of T1, see fig. 20.

In the most simple case the shunt regulator can be a diode (eventually a Schottky diode) maybe with an addition of a resistor between pin 18 and -line to keep somewhat constant voltage across the diode. Unfortunately the voltage can not be increased more than a couple of hundred millivolts before the function that is to prevent the T1 from saturating regarding the voltage across it would disappear and which in its turn would result that the negative half periods of the signal would be clipped with a massive distortion as a result. What has to be done at the same time to come further in this matter is to make the voltage drop smaller across R13 (also to use a T1 with low sat.voltage). This requires that the sense level of which voltage has to be across R13 is altered. The voltage across R13 is one diode drop plus the voltage drop across R12. What can be done is to subtract some of the diode drop, see fig. 21.

Observe that the circuitry needs a recalculation of several components especially R13. How far it is possible to come, by decreasing the gap between V_{line} and V_{DC} depends on spread figures and temperature requirement.

The difference between the line current and current out from V_{DC} is possible to alter but only to minor extent.

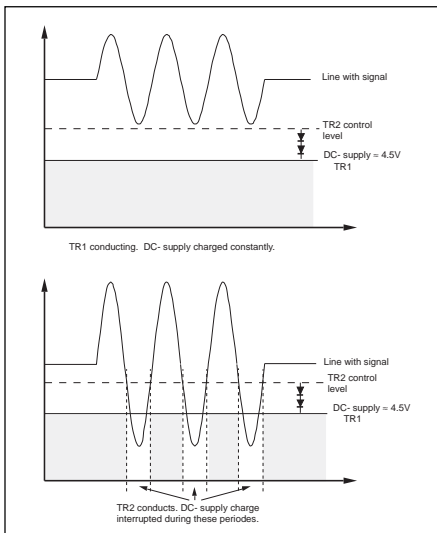


Figure 18.

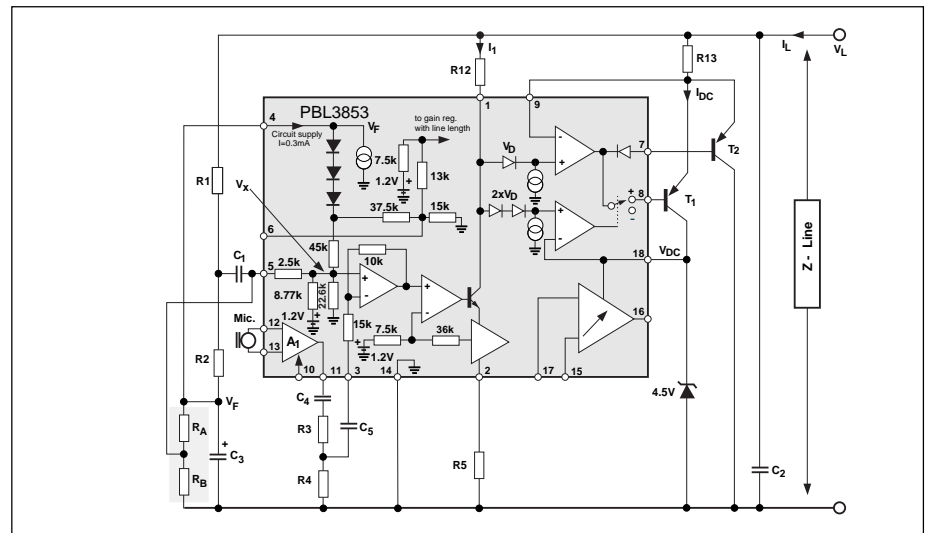


Figure 19 . Functional Diagram.

What can be done, is to use more of the transmitter output signal for charging the VDC via the T1. The transmitter output signal passes both R12 and R13. The transmit current that passes R12 is lost regarding the charging of VDC but it can not be zero because it is exactly that signal that via the amplifiers and followers T1 and T2 drives the current through R12. Caution has to be taken so that spread figures do not eat up all the won current. If the specification of DC-characteristic towards the line has to be maintained when the quota of R12/R13 is made smaller, the R5 must most probably be increased. What has to be understood is that the current consumption of the IC circuit it self (like in the given figure. 4.) can not be lowered and that it at 16 mA line current not only consists of approx. 540 mA into pin 4, but also that of the 3.2 mA into pin 1, some 600 mA is used to power up the IC. This supply current to the circuit will be multiplied by the R13/R12 ratio but it will not participate in giving any signal out. What is left of the current that also passes pin 2, to produce output signal, is 2.6 mA. Of this current 1.9 mAp is modulated. This current in its turn will be multiplied by R12 and R13 (the function being $1 + R12/R13$) and the result 4.2 mAp will give in the load of 490 Ω ($600 \Omega // 2.7 k$) a signal swing of approx. 2 Vp. This value will fulfil a typical requirement of 1.4 Vp swing with margin.

Even in a case, that the differencies in the in- and out-current respective voltage are satisfactory, there might be a need for redimensioning. In case that a lower DC mask is desirable and there is a possibility to accept a lower VDC it can be made with RA for the DC mask (RC and RD must be high ohmic) and sinking the voltage for the shunt regulator VDC (simply drawn as a diode in the fig. 4). What has to be observed is that the supply for the receiver

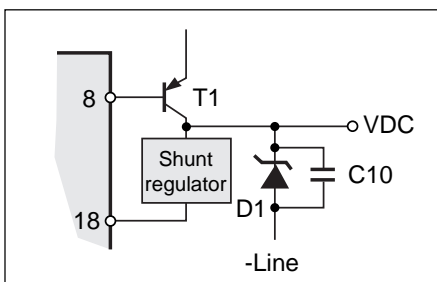


Figure 20.

amplifier is also lowered this way and hence the output swing of it. In contrary if a higher DC mask is desired and a higher line voltage is acceptable, both can thus be increased (the line voltage is increased with RB).

A higher current out of VDC can be achieved by altering R12, R13 and R5 but at cost of the lowest line current the circuit will work with, which will increase accordingly. The opposite is valid if less current is required out of VDC it will say that the circuit will work to lower line current. The lowest line current the circuit will work to full specification is (with the increased DC mask) 5.5 mA but in this case only 1 mA can be taken out of VDC (with no current out of receiver output).

The circuitry can be made simpler in case the requirement for the voltage and/or current difference between the line - and VDC is smaller. If the requirement for the smallest possible voltage between the line voltage and the voltage at VDC gets easier, which will say that the difference is that large (1-3 V dependent on spec.) so that the AC signal on the line never goes that much negative that it reaches the VDC level, the transistor T2, which is used to shunt current past the VDC in order not to disturb the line impedance, can be omitted and the pins 7 and 8 connected together. To ensure that high enough signal can be taken out from the receiver when a low VDC is used a shunt element should be connected between VDC and pin 18 (see fig. 20). If the requirement on the difference between the line current and the current

out from VDC is not that critical, it means that the circuit can dispose more current for its own function. The first thing in this case is to see if the active transmitter output impedance towards the line is necessary. (in case it is used in the first place). The active impedance towards the line is used to save current and it functions as follows. The transmitter generates at the same time as it transmits a required impedance towards the line by taking a part of the signal on the line and feeding it back to the transmitter amplifier, thus saving the current that would have been necessary to drive the signal out on the line. Because the transmitter amplifier is not current loaded by the impedance that it generates itself, the current need with active impedance will be: 600 Ω line impedance parallel with the 2.7 k (R1) which with transmit swing of 2 Vp needs 4.1 mAp transmitter current instead if the circuit would have a passive impedance of 600 Ω the current needed would be 6.7 mAp. The current need without active impedance increases with 2.6 mAp. Half of this current flows through R12 and generates no charge current for VDC, hence if the difference between the line current and VDC current increases by 1.3 mA it would result that the active impedance could be changed against a passive. This is done by omitting C₁, short R2, decrease R1 to a suitable value for the impedance towards line (R1 can be a complex network) and adjust back the DC mask to compensate for the lower voltage drop across R1. The pin 5 can be left open or connected with a small

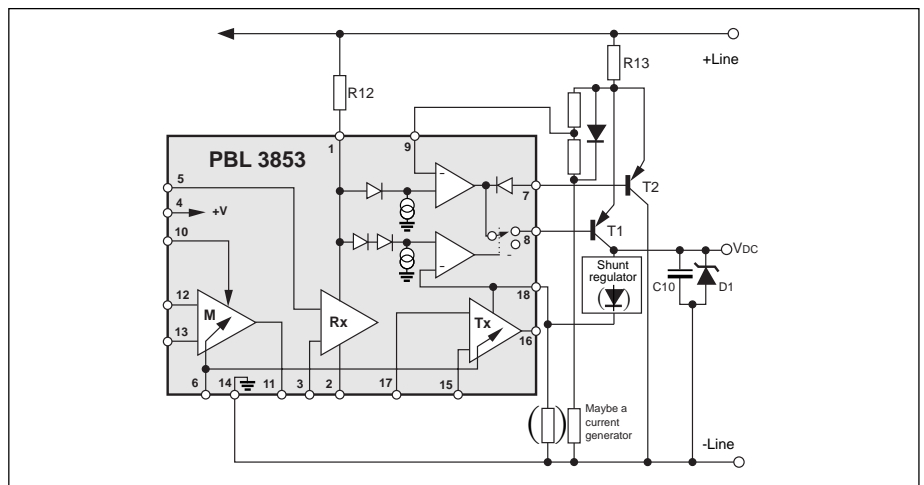


Figure 21.

capacitor to ground if the input tends to pick up noise. This input can also be used as a second input to the transmitter (in-impedance approx. 8 kΩ, gain depending on the arrangement at pin 3) used for ex. DTMF tones.

If there is with passive impedance still extra margin in current (more than 4 mA + 1.3 mA = 5.3 mA) then it is possible to increase the current through R12 versus current through R13 by making the R12 go towards 0. The whole transmitting current would go through pin 1 to the line and the charge current for VDC would be set by a diode voltage drop across R13 which would make the current that can be taken out from VDC constant, regardless the line current. (assuming that the line current is high enough) The lowest current for such a circuit is, if no more current is taken out of the VDC than what is needed for the earphone amplifier: 540 mA into pin 4 plus 600 mA supply current into pin 1 plus 6.7 mA transmitting current to give 2 Vp signal

across 300 Ω load (600 Ω line parallel with 600 Ω output impedance of the circuitry) plus 0.8 mA to VDC and pin 18 in order to get 0.6 Vp across 350 Ω earphone (according to the test circuit) the sum is 8.6 mA line current, or for 0.4 Vp across 150 Ω earphone the sum being 8.8 mA line current. The rest of the available line current can be utilized, by decreasing R13, to charge VDC. In this manner simplified circuit should be adapted to the requirement for the VDC, both in voltage and current, related to under which line conditions the circuit has to work. The circuit gets more complex the closer the operational limits are utilized. The drawback in using the active impedance towards the line, is the difficulty of dimensioning (especially in case of complex line impedance requirement) that limits its usage to only when absolutely necessary.

Softclipping

(see Fig.22)

The risk for signal clipping increases on long lines especially when using the line length regulation that doubles the gain on such a line. This clipping is highly uncomfortable for the second party but also for one self because it destroys the side tone balance and thus allows shockwise 20 dB higher levels into the receiver of ones own voice. There is a cure for this especially if an active impedance is used towards the line. A

so called "softclipping" is a methode where the line voltage is monitored and the transmitter gain is controlled in case the line voltage gets to high. This can be adapted to PBL 3853 as follows. The line length regulation function is used to control the gain down when high signals appear on the line, no matter if line length regulation is desired or not. In the case the function is used there is the high gain situation at a long line (where it is actual to regulate the gain down) but in case the function is not used the transmitter is set to have the high gain by making the RD enough low ohmic and omitting RC.

The 1k resistor is to prevent an unlinear load to the line and also to form the "on" time constant of the function with the capacitor CB. A capacitor of 0.1 μF will block the DC from the line but will also set the lower frequency characteristic for the function. The two diodes form a voltage doubler rectifier for the AC signal which is then filtered by CB, the resistor of 10M will set the "off" time constant. The transistor acts as a impedance transformer. Resistor RE sets the level at which the amplitude limiter cuts in. It is possible to connect diodes in series with the pin 6 (pointing to the pin) in order to alter the "edge" of the limiter. The value of RE is set by the values of RC and RD. A component value suggestion for the case of no gain regulation with line length:

RC = RD = 8.2k RE = 4.7k.

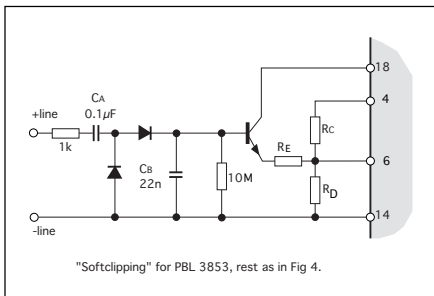


Figure 22.

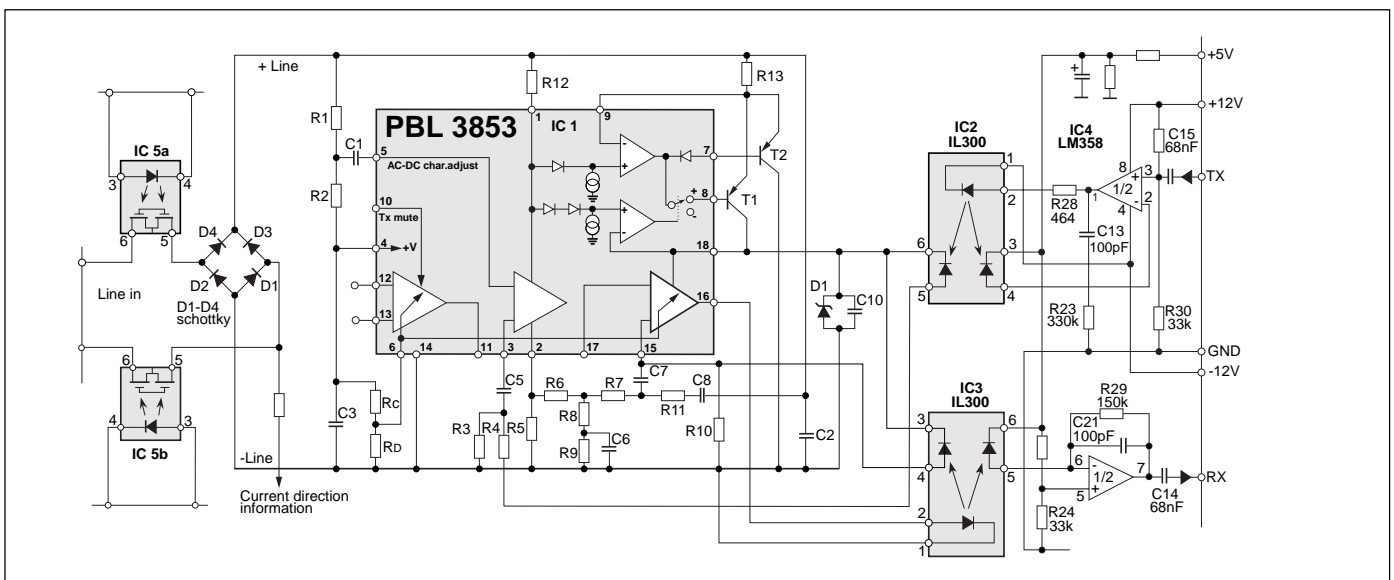


Figure 23. Analog line interface to a digital PABX with galvanic insulation by linear optocouplers (IL3000) using PBL 3853.

Ordering Information

Package	Part No
Plastic DIP	PBL 3853N
Plastic	PBL 3853SO
Plastic	PBL 3853SO-T

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