





Feb 2008



- Pletronics' SM77M Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 0.8 to 70 MHz
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable
- Disable function includes low standby power mode
- Low Jitter
- Optimized for low power applications (battery)

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.17 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V
lo Output Current	+25 mA to -25 mA

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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PART NUMBER:

SM77	45	M	Ε	W	- 50.0M	-XX		Part Marking:
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel	PLE SM77 FF.FFF M • YMDXX
							Frequency in MHz	or
							Supply Voltage V _{cc} W = 2.5V <u>+</u> 10%	PLE SM77 FF.FFF M • YYWWXX
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C E = Temp. range -40 to +85°C	or
							Series Model	7XYWWXX
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	FF.FFF M • PLE XXX
							Series Model	

Marking Legend:

PLE = Pletronics

FF.FFF M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	1	2
Year	2006	2007	2008	2009	2010	2011	2012

	Code	Α	В	С	D	E	F	G	Н	J	K	L	M
M	lonth	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
	-I		^	^	4	_	^	-	_	^		_	^

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Day	1	2	3	4	5	6	7	8	9	10	11	12
Code	D	E	F	G	Н	J	K	L	М	N	Р	R
Day	13	14	15	16	17	18	19	20	21	22	23	24
Code	Т	U	٧	W	Х	Y	Z					
Day	25	26	27	28	29	30	31					



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Electrical Specification for 2.50V $\pm 10\%$ over the specified temperature range

Item	Min	Max	Unit	Condition
Frequency Range	0.8	69.9	MHz	
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for 1
" 44 "	-25	+25		year, shock, vibration and temperatures
" 20 "	-20	+20		
Output Waveform		CMOS		
Output High Level	90	-	%	of V _{CC} (See load circuit)
Output Low Level	-	10	%	
Output Symmetry	45	55	%	at 50% point of V _{CC} (See load circuit)
Jitter	-	0.6	pS RMS	12 KHz to 20 MHz from the output frequency
	-	2.5	pS RMS	10 Hz to 1 MHz from the output frequency
E/D Internal Pull-up	50	-	Kohm	to V _{CC}
V disable	-	30	%	of V_{CC} applied to pad 1
V enable	70	1	%	
Output leakage V _{OUT} = V _{CC}	-10	+10	uA	Pin 1 low, device disabled
V _{OUT} = 0V	-10	+10	uA	
Standby Current I _{cc}	-	3	uA	
Enable time	1	100	nS	Time for output to reach a logic state
Disable time		100	nS	Time for output to reach a high Z state
Start up time	-	3	mS	Time for output to reach specified frequency
Operating Temperature	-10	+70	°C	Standard Temperature Range
Range	-40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	



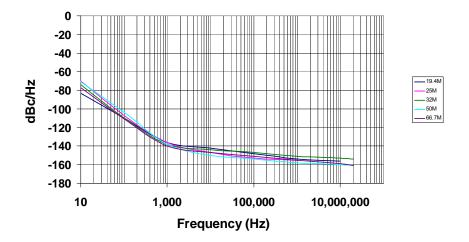
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Electrical Specification for 2.50V ±10% over the specified temperature range

Item	Тур	Max	Unit	Condition	
Output T _{RISE} and T _{FALL}	2.8	5.5	nS	< 35 MHz	$C_{LOAD} = 15 \text{ pF}$
	3	6	nS	≥ 35 MHz	- 10% to 90% of V _{cc} See Load Circuit
	4.5	9	nS	< 35 MHz	C _{LOAD} =30 pF 10% to 90% of V _{CC}
	6	12	nS	≥ 35 MHz	See Load Circuit
V _{CC} Supply Current (I _{CC})	1.5	2.5	mA	< 8 MHz	C _{LOAD} = 15 pF
	1.8	3	mA	≥ 8 MHz and < 16 MHz	
	2.4	4	mA	≥ 16 MHz and < 35 MHz	
	7	9	mA	≥ 35 MHz	
	2	3	mA	< 8 MHz	C _{LOAD} = 30 pF
	2.2	3.5	mA	≥ 8 MHz and < 16 MHz	
	2.5	4.5	mA	<u>></u> 16 MHz and < 35 MHz	
	10	13	mA	<u>></u> 35 MHz	

Specifications with Pad 1 E/D open circuit

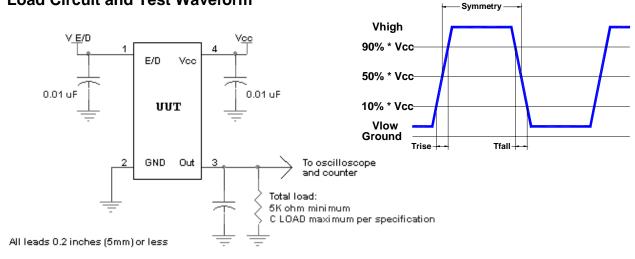
Typical phase noise plot for 5 oscillators at different output frequencies.





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Load Circuit and Test Waveform



Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	1500	MIL-STD-883 Method 3115		
Charged Device Model	1000	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

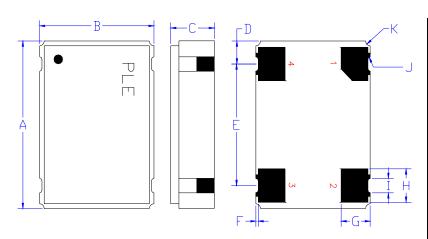
Category=e4

Max Safe Temp=260C for 10s 2X Max



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Mechanical:



Inches mm 0.276 +0.006 7.00 +0.15 В 0.197 + 0.0065.00 <u>+</u>0.15 С 0.068 <u>+</u>0.018 1.73 <u>+</u>0.44 D^1 0.038 0.96 E^1 0.200 5.08 F^1 0.004 0.10 G^1 0.050 1.27 H^1 0.055 1.40 0.024 0.60 J^1 0.004 0.10R K^1 0.008 0.020R

Not to Scale

¹ Typical dimensions

Contacts:

Gold 11.8 $\mu inches$ 0.3 μm minimum over Nickel 50 to 350 $\mu inches$ 1.27 to 8.89 μm

Pad	Function	Note
1	Output Enable/Disable	When this pin is not connected the oscillator shall operate. When this pin is logic low the output will be inhibited (high impedance state.) Recommend connecting this pin to $V_{\rm CC}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.



Layout and application information

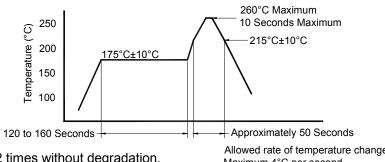
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

Allowed rate of temperature change Maximum 4°C per second

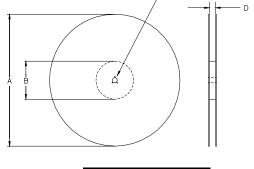
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

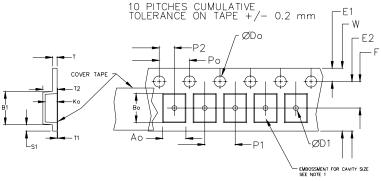
	Constant Dimensions Table 1											
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05							
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1				
24mm		1.5			<u>+</u> 0.1							

Variable Dimensions Table 2										
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko			
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1			

Note 1: Embossed cavity to conform to EIA-481-B

Not to scale





USER DIRECTION OF UNREELING -

			REEL DIMENSIONS			
A	Ą	inches	7.0	10.0	13.0	
		mm	177.8	254.0	330.2	
E	В	inches	2.50	4.00	3.75	
		mm	63.5	101.6	95.3	Tape Width
(С	mm	13.0 +0.5 / -0.2			vvidili
[O	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0

Reel dimensions may vary from the above



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