

# **Complementary Silicon Power Transistors**

The MJ15011 and MJ15012 are PowerBase power transistors designed for high-power audio, disk head positioners, and other linear applications. These devices can also be used in power switching circuits such as relay or solenoid drivers, dc-to-dc converters or inverters.

- High Safe Operating Area (100% Tested) 1.2 A @ 100 V
- Completely Characterized for Linear Operation
- High DC Current Gain and Low Saturation Voltage

 $h_{FE} = 20 \text{ (Min)} @ 2 \text{ A}, 2 \text{ V}$ 

 $V_{CE(sat)} = 2.5 \text{ V (Max)} @ I_C = 4 \text{ A}, I_B = 0.4 \text{ A}$ 

• For Low Distortion Complementary Designs

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	250	Vdc
Collector–Emitter Voltage	V <sub>CEX</sub>	250	Vdc
Emitter–Base Voltage	V <sub>EB</sub>	5	Vdc
Collector Current — Continuous — Peak (1)	I <sub>С</sub> I <sub>СМ</sub>	10 15	Adc
Base Current — Continuous — Peak (1)	I <sub>B</sub>	2 5	Adc
Emitter Current — Continuous — Peak (1)	I <sub>E</sub> I <sub>EM</sub>	12 20	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	200 1.14	Watts W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	0.875	°C/W
Maximum Lead Temperature for Soldering Purposes	T <sub>L</sub>	265	°C

(1) Pulse Test: Pulse Width = 5 ms, Duty Cycle  $\leq$  10%.

## MJ15011\* PNP MJ15012\*

\*ON Semiconductor Preferred Device

10 AMPERE COMPLEMENTARY POWER TRANSISTORS 250 VOLTS 200 WATTS



CASE 1-07 TO-204AA (TO-3)

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

#### MJ15011 MJ15012

### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				•
Collector–Emitter Breakdown Voltage (1) (I <sub>C</sub> = 100 mA)	V <sub>(BR)CEO</sub>	250	_	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 200 Vdc)	I <sub>CEO</sub>	_	1	mAdc
Collector Cutoff Current (V <sub>CE</sub> = 250 Vdc, V <sub>BE(off)</sub> = 15 Vdc)	I <sub>CEX</sub>	_	500	μAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5 Vdc)	I <sub>EBO</sub>	_	500	μAdc
ON CHARACTERISTICS (1)				
DC Current Gain $(I_C = 2 \text{ Adc}, V_{CE} = 2 \text{ Vdc})$ $(I_C = 4 \text{ Adc}, V_{CE} = 2 \text{ Vdc})$	h <sub>FE</sub>	20 5	100 —	_
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 2 Adc, I <sub>B</sub> = 0.2 Adc) (I <sub>C</sub> = 4 Adc, I <sub>B</sub> = 0.4 Adc)	V <sub>CE(sat)</sub>	_	0.8 2.5	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 4 Adc, V <sub>CE</sub> = 2 Vdc)	V <sub>BE(on)</sub>	_	2	Vdc
DYNAMIC CHARACTERISTICS	•			
Output Capacitance (V <sub>CB</sub> = 10 Vdc, f = 1 MHz)	C <sub>ob</sub>	_	750	pF
SECOND BREAKDOWN				
Second Breakdown Collector Current with Base Forward Biased ( $V_{CE} = 40 \text{ Vdc}, t = 0.5 \text{ s}$ ) ( $V_{CE} = 100 \text{ Vdc}, t = 0.5 \text{ s}$ )	I <sub>S/b</sub>	5 1.4		Adc

<sup>(1)</sup> Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$  2%.

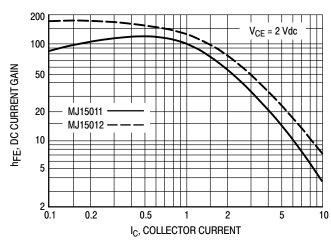


Figure 1. DC Current Gain

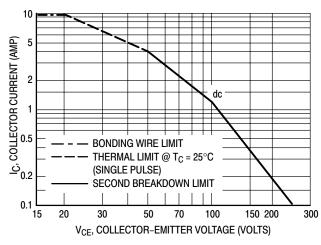
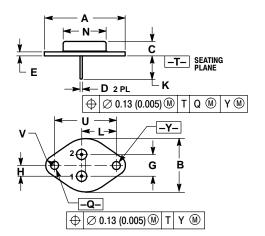


Figure 2. Active Region Safe Operating Area

### MJ15011 MJ15012

#### **PACKAGE DIMENSIONS**

#### **CASE 1-07** TO-204AA (TO-3) ISSUE Z



- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: INCH.

  3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	1.550 REF		39.37 REF		
В		1.050		26.67	
С	0.250	0.335	6.35	8.51	
D	0.038	0.043	0.97	1.09	
Е	0.055	0.070	1.40	1.77	
G	0.430 BSC		10.92 BSC		
Н	0.215 BSC		5.46 BSC		
K	0.440	0.480	11.18	12.19	
L	0.665	BSC	16.89 BSC		
N		0.830		21.08	
Q	0.151	0.165	3.84	4.19	
υ	1.187 BSC		30.15 BSC		
٧	0.131	0.188	3.33	4.77	

STYLE 1: PIN 1. BASE 2. EMITTER CASE: COLLECTOR

#### MJ15011 MJ15012

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

#### **PUBLICATION ORDERING INFORMATION**

#### NORTH AMERICA Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

**Phone**: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

EUROPE: LDC for ON Semiconductor - European Support

German Phone: (+1) 303–308–7140 (Mon–Fri 2:30pm to 7:00pm CET)

Email: ONlit-german@hibbertco.com

French Phone: (+1) 303–308–7141 (Mon–Fri 2:00pm to 7:00pm CET)

Email: ONlit-french@hibbertco.com

English Phone: (+1) 303–308–7142 (Mon–Fri 12:00pm to 5:00pm GMT)

Email: ONlit@hibbertco.com

EUROPEAN TOLL-FREE ACCESS\*: 00-800-4422-3781

\*Available from Germany, France, Italy, UK, Ireland

#### CENTRAL/SOUTH AMERICA:

Spanish Phone: 303-308-7143 (Mon-Fri 8:00am to 5:00pm MST)

Email: ONlit-spanish@hibbertco.com

Toll–Free from Mexico: Dial 01–800–288–2872 for Access –

then Dial 866-297-9322

ASIA/PACIFIC: LDC for ON Semiconductor – Asia Support

Phone: 1-303-675-2121 (Tue-Fri 9:00am to 1:00pm, Hong Kong Time)

Toll Free from Hong Kong & Singapore:

001-800-4422-3781 Email: ONlit-asia@hibbertco.com

JAPAN: ON Semiconductor, Japan Customer Focus Center

4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031

Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local

Sales Representative.