MBRA210LT3

Surface Mount Schottky Power Rectifier

SMA Power Surface Mount Package

. . . employing the Schottky Barrier principle in a metal–to–silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes. Typical applications are ac/dc and dc–dc converters, reverse battery protection, and "Oring" of multiple supply voltages and any other application where performance and size are critical.

- Ultra Low V_F
- 1st in the Market Place with a 10 V_R Schottky Rectifier
- Compact Package with J–Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over–Voltage Protection
- Optimized for Low Forward Voltage

Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL94, V_O at 1/8″
- Weight: 70 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Polarity Band Indicates Cathode Lead
- ESD Ratings: Machine Model = C
  Human Body Model = 3A
- Available in 12 mm Tape, 5000 Units per 13 inch Reel
- Marking: B2L1

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	10	V
Average Rectified Forward Current (At Rated V_R, T_L = 110°C)	I_O	2.0	A
Non–Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	230	A
Storage/Operating Case Temperature Operating Junction Temperature	T_{stg}, T_C T_J	–55 to +125	°C
Voltage Rate of Change (Rated V_R, T_J = 25°C)	dv/dt	10,000	V/μs

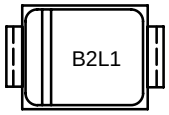
ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 2 AMPERES 10 VOLTS

SMA
CASE 403D
PLASTIC

MARKING DIAGRAM



B2L1 = Device Code

ORDERING INFORMATION

Device	Package	Shipping
MBRA210LT3	SMA	5000/Tape & Reel

THERMAL CHARACTERISTICS

Characteristic	Symbol	Min Pad	1 Inch Pad	Unit
Thermal Resistance – Junction–to–Lead Thermal Resistance – Junction–to–Ambient	$R_{\theta JL}$ $R_{\theta JA}$	22 150	15 81	°C/W

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 1)	V_F	**T_J = 25°C**	**T_J = 100°C**	V
(I_F = 0.1 A) (I_F = 1.0 A) (I_F = 2.0 A)		0.260 0.325 0.350	0.15 0.23 0.26	
Maximum Instantaneous Reverse Current	I_R	**T_J = 25°C**	**T_J = 100°C**	mA
(V_R = 5.0 V) (V_R = 10 V)		0.25 0.70	40 60	

1. Pulse Test: Pulse Width ≤ 250 μs, Duty Cycle ≤ 2%.

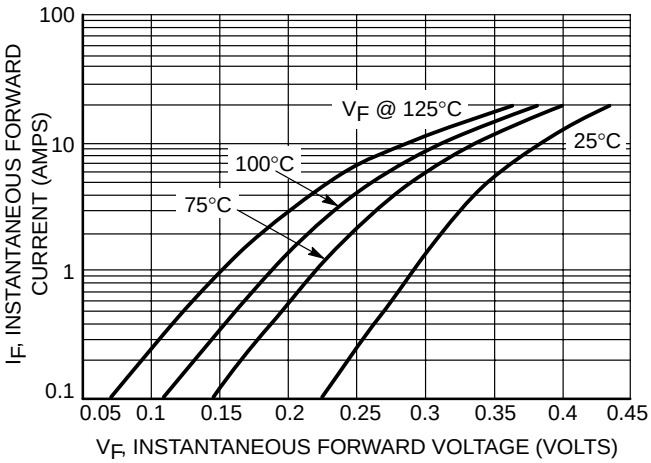


Figure 1. Typical Forward Voltage

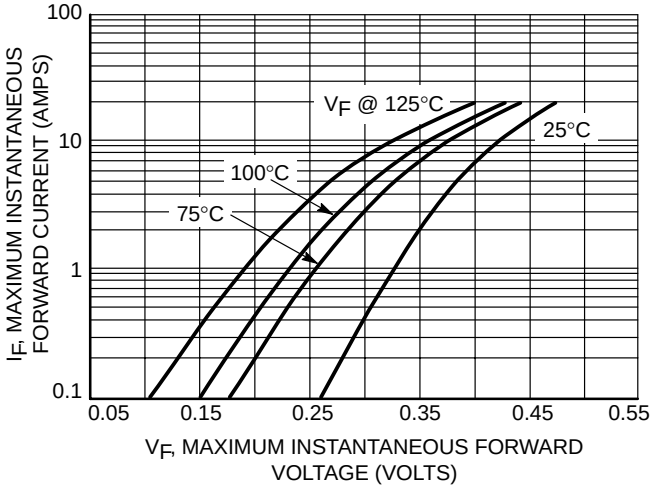


Figure 2. Maximum Forward Voltage

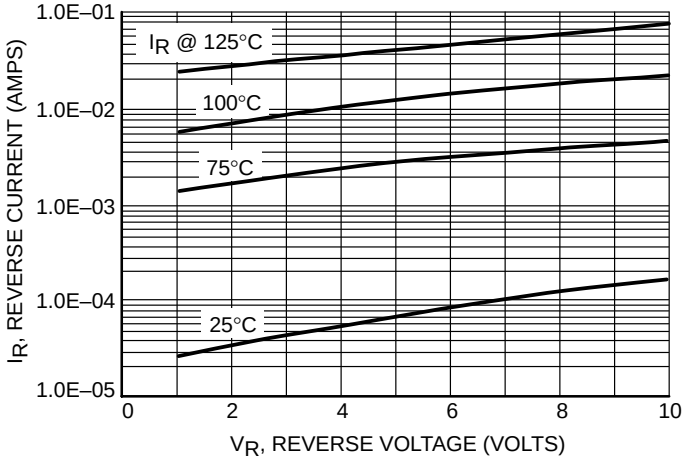


Figure 3. Typical Reverse Current

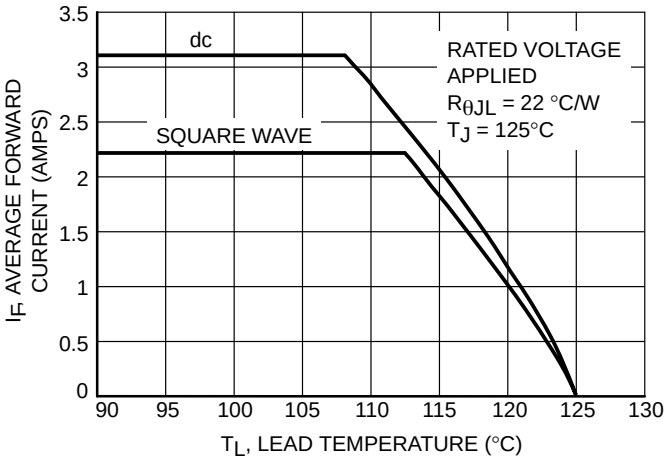


Figure 4. Current Derating – Junction to Lead

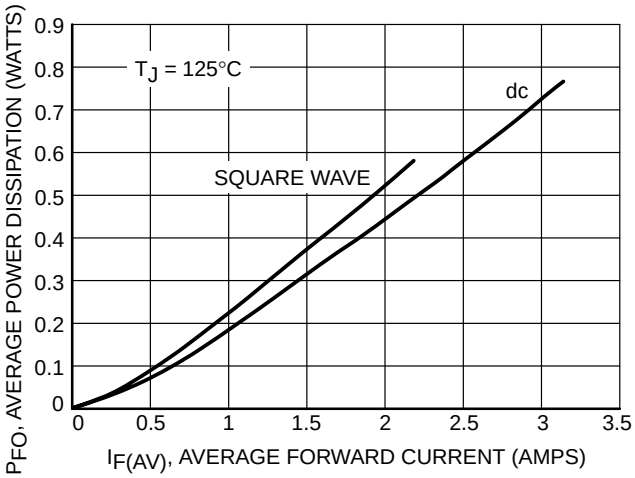


Figure 5. Forward Power Dissipation

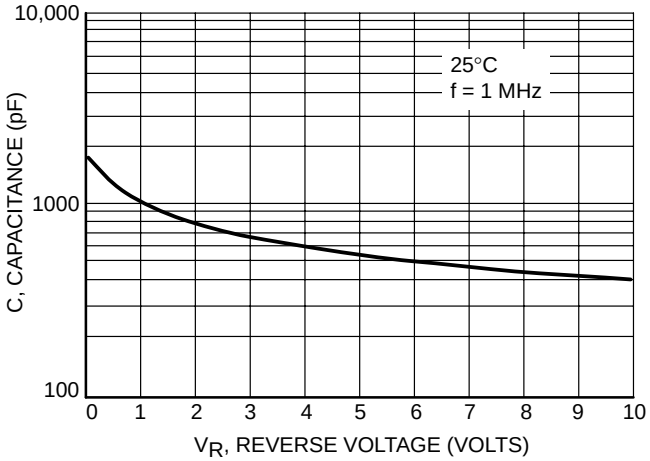


Figure 6. Typical Capacitance

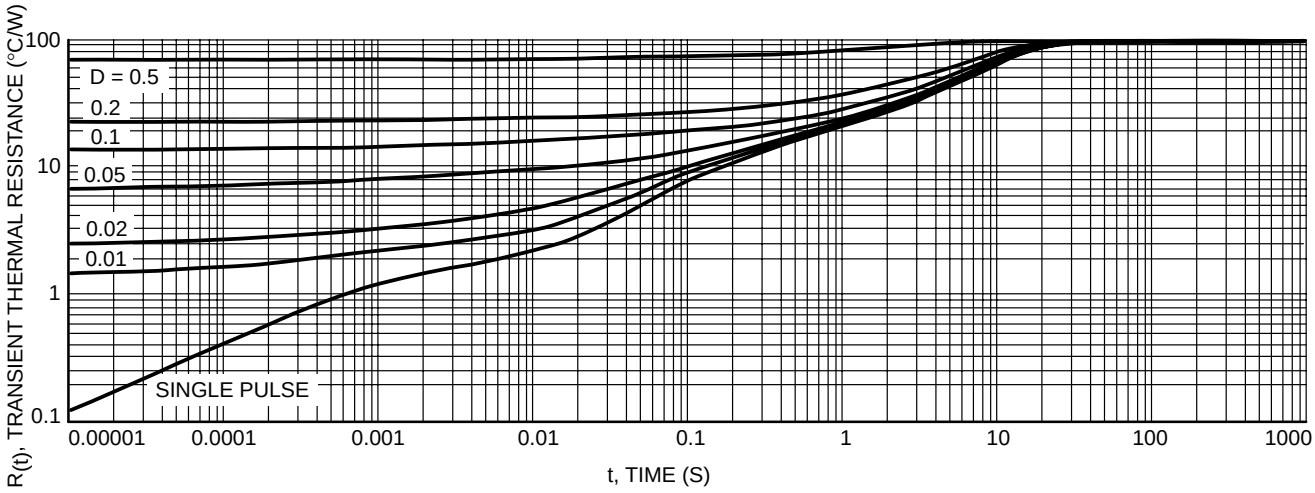


Figure 7. Thermal Response, Junction to Ambient (min pad)

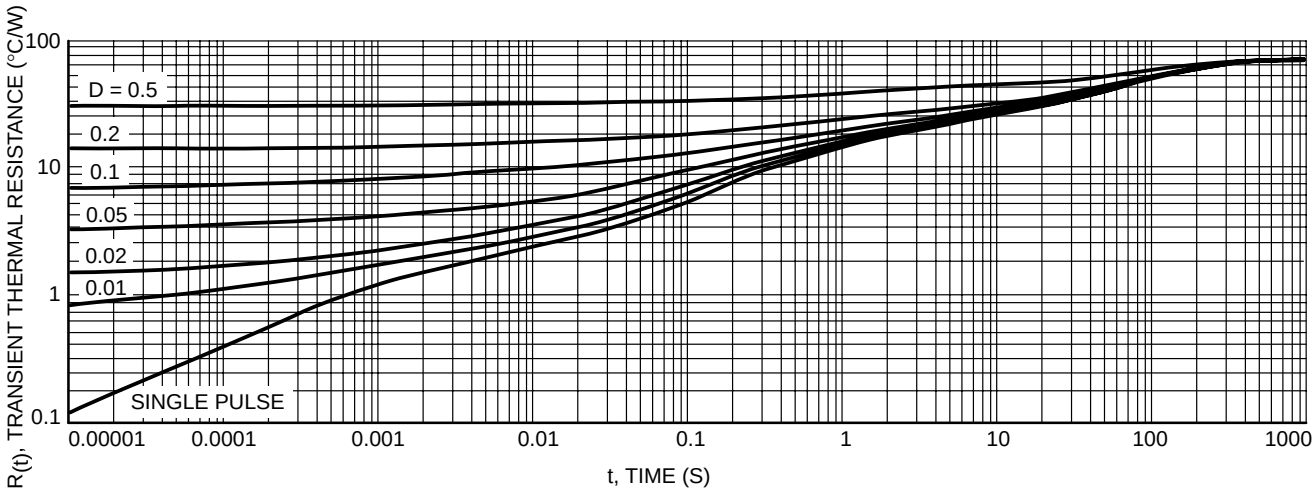
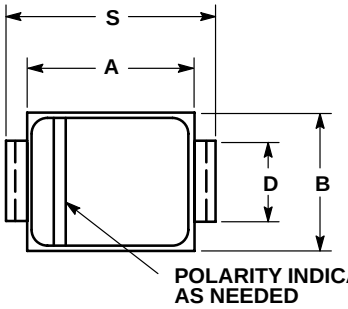


Figure 8. Thermal Response, Junction to Ambient (1 inch pad)

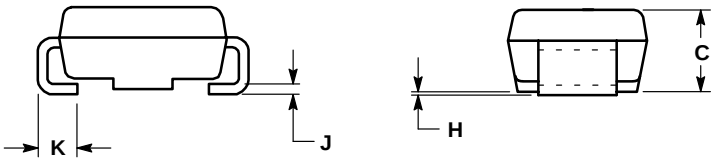
PACKAGE DIMENSIONS

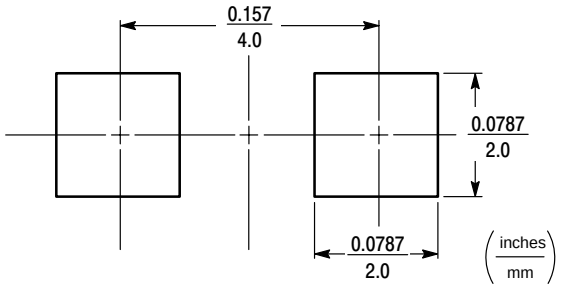
SMA
CASE 403D–02
ISSUE A



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 403D–01 OBSOLETE, NEW STANDARD IS 403D–02.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.160	0.180	4.06	4.57
B	0.090	0.115	2.29	2.92
C	0.075	0.095	1.91	2.41
D	0.050	0.064	1.27	1.63
H	0.002	0.006	0.05	0.15
J	0.006	0.016	0.15	0.41
K	0.030	0.060	0.76	1.52
S	0.190	0.220	4.83	5.59





SMA FOOTPRINT

Notes

Notes

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:
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