

**PNP 1 GHz switching transistor****PMBTH81****FEATURES**

- Low cost
- High transition frequency.

**DESCRIPTION**

The PMBTH81 is a general purpose silicon pnp transistor, encapsulated in a SOT23 plastic envelope. Its complement is the PMBTH10.

**PINNING**

PIN	DESCRIPTION
Code: V31	
1	base
2	emitter
3	collector

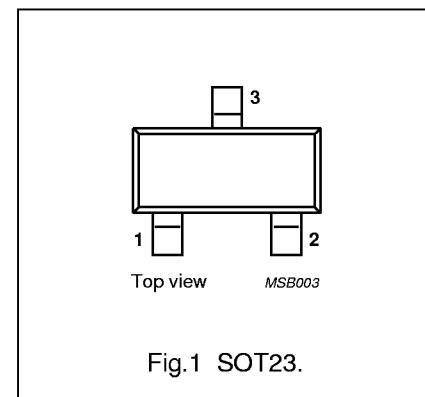


Fig.1 SOT23.

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	20	V
$V_{CEO}$	collector-emitter voltage	open base	–	20	V
$P_{tot}$	total power dissipation	$T_s = 45^\circ\text{C}$ (note 1)	–	400	mW
$C_{ce}$	collector-emitter capacitance	$V_{CB} = 10 \text{ V}; I_B = 0; f = 1 \text{ MHz}$	–	0.65	pF
$C_{cb}$	collector-base capacitance	$V_{CB} = 10 \text{ V}; I_E = 0; f = 1 \text{ MHz}$	–	0.85	pF
$f_T$	transition frequency	$V_{CE} = 10 \text{ V}; I_C = 5 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25^\circ\text{C}$	600	–	MHz

**Note**

1.  $T_s$  is the temperature at the soldering point of the collector tab.

## PNP 1 GHz switching transistor

PMBTH81

**LIMITING VALUES**

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	20	V
$V_{CEO}$	collector-emitter voltage	open base	–	20	V
$V_{EBO}$	emitter-base voltage	open collector	–	3	V
$I_C$	collector current		–	40	mA
$P_{tot}$	total power dissipation	$T_s = 45^\circ\text{C}$ (note 1)	–	400	mW
$T_{stg}$	storage temperature		–65	150	°C
$T_j$	junction temperature		–	150	°C

**THERMAL RESISTANCE**

SYMBOL	PARAMETER	THERMAL RESISTANCE
$R_{th,j-s}$	from junction to soldering point (note 1)	260 K/W

**Note**

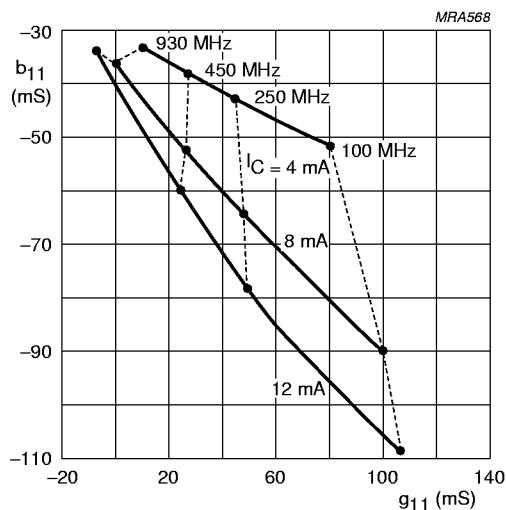
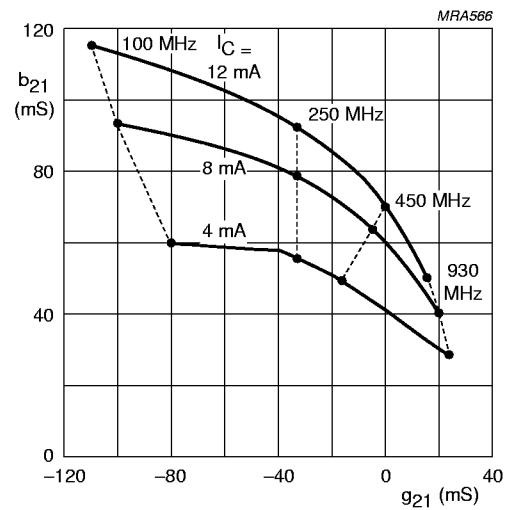
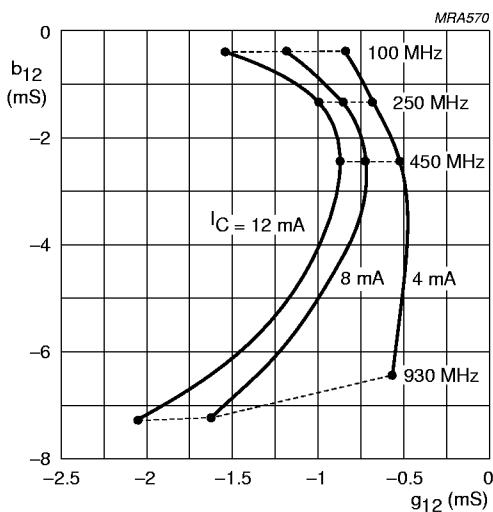
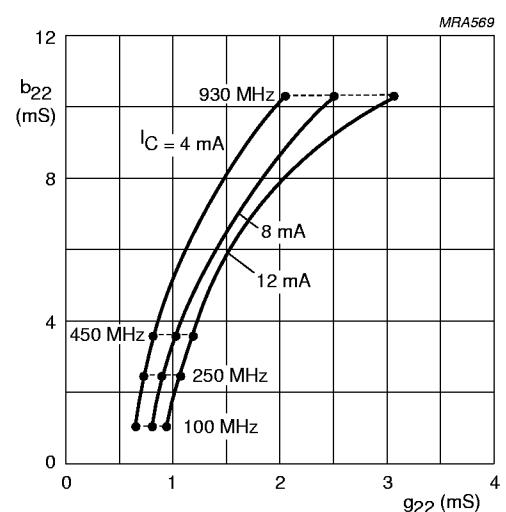
- $T_s$  is the temperature at the soldering point of the collector tab.

**CHARACTERISTICS** $T_j = 25^\circ\text{C}$ 

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{(BR)CBO}$	collector-base breakdown voltage	open emitter; $I_C = 10 \mu\text{A}$ ; $I_E = 0$	20	–	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	open base; $I_C = 1 \text{ mA}$ ; $I_B = 0$	20	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	open collector; $I_E = 10 \mu\text{A}$ ; $I_C = 0$	3	–	V
$V_{CE\text{ sat}}$	collector-emitter saturation voltage	$I_C = 5 \text{ mA}$ ; $I_B = 0.5 \text{ mA}$	–	0.5	V
$V_{BE\text{ on}}$	base-emitter ON voltage	$V_{CE} = 10 \text{ V}$ ; $I_C = 5 \text{ mA}$	–	0.9	V
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 10 \text{ V}$ ; $I_E = 0$	–	100	nA
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 2 \text{ V}$ ; $I_C = 0$	–	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 10 \text{ V}$ ; $I_C = 5 \text{ mA}$	60	–	
$C_{ce}$	collector-emitter capacitance	$V_{CB} = 10 \text{ V}$ ; $I_B = 0$ ; $f = 1 \text{ MHz}$	–	0.65	pF
$C_{cb}$	collector-base capacitance	$V_{CB} = 10 \text{ V}$ ; $I_E = 0$ ; $f = 1 \text{ MHz}$	–	0.85	pF
$f_T$	transition frequency	$V_{CE} = 10 \text{ V}$ ; $I_C = 5 \text{ mA}$ ; $f = 100 \text{ MHz}$ ; $T_{amb} = 25^\circ\text{C}$	600	–	MHz

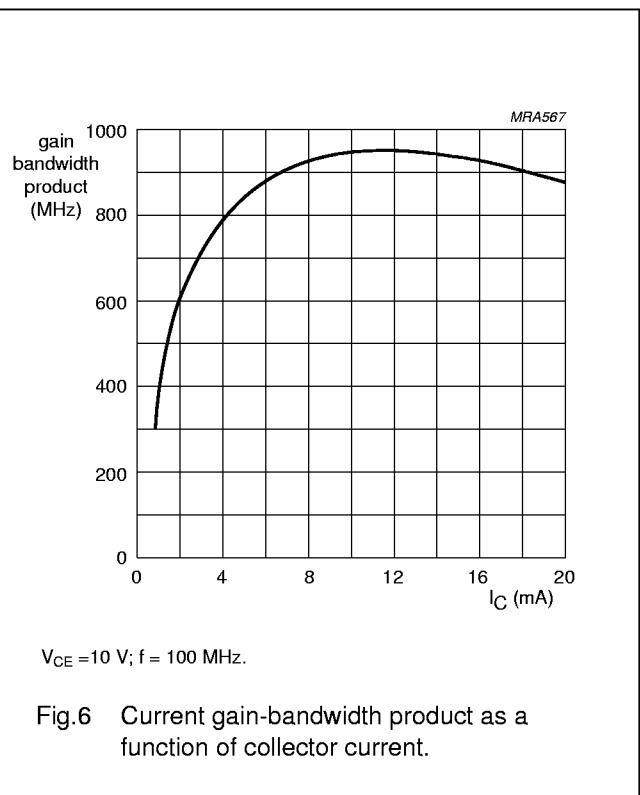
## PNP 1 GHz switching transistor

PMBTH81

 $V_{CB} = 10 \text{ V}; T_{amb} = 25^\circ\text{C}$ .Fig.2 Common base input admittance ( $Y_{11}$ ). $V_{CB} = 10 \text{ V}; T_{amb} = 25^\circ\text{C}$ .Fig.3 Forward transfer admittance ( $Y_{21}$ ). $V_{CB} = 10 \text{ V}; T_{amb} = 25^\circ\text{C}$ .Fig.4 Reverse transfer admittance ( $Y_{12}$ ). $V_{CB} = 10 \text{ V}; T_{amb} = 25^\circ\text{C}$ .Fig.5 Common base output admittance ( $Y_{22}$ ).

## PNP 1 GHz switching transistor

PMBTH81



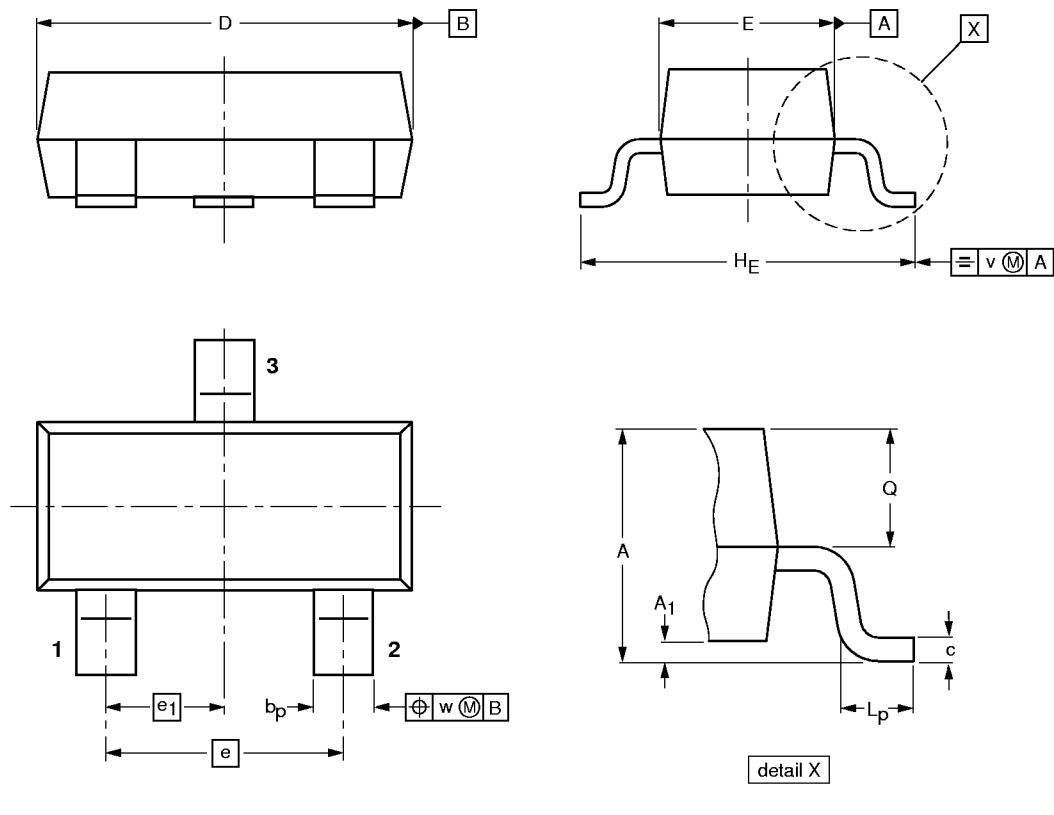
## PNP 1 GHz switching transistor

PMBTH81

## PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



0      1      2 mm  
scale

## DIMENSIONS (mm are the original dimensions)

UNIT	A	$A_1$ max.	$b_p$	c	D	E	e	$e_1$	$H_E$	$L_p$	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23						97-02-28