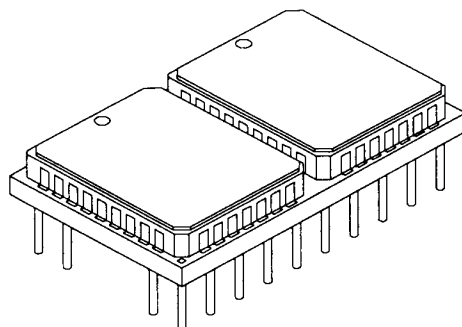


PRELIMINARY

DESCRIPTION:

The DPS8X16A is a 40-pin Pin Grid Array (PGA) consisting of two 8K X 8 SRAM devices in ceramic LCC packages surface mounted on a co-fired ceramic substrate with matched thermal coefficients.

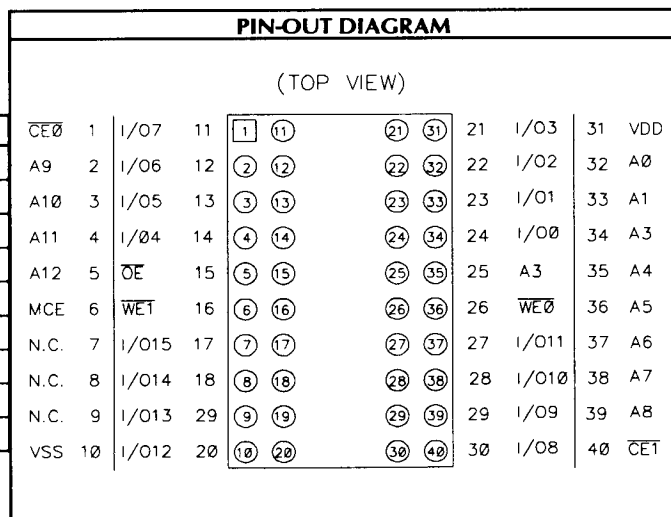
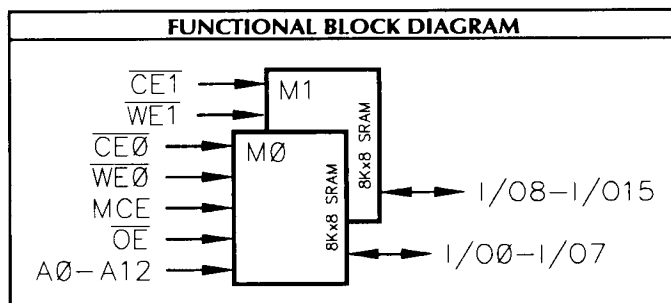
The pins have been arranged around a central 0.3" gap which can accommodate a heat rail, if desired. In this central gap is a cavity containing two 0.1 μ f decoupling capacitors.



4

FEATURES:

- Organization Available: 8K X 16
- Access Times:
25, 35, 45, 55, 70, 85,
100, 120, 150ns (max.)
- Low Data Retention: 2.0V min.
- Fully Static Operation - No clock or refresh required
- Three-State Outputs
- TTL-compatible Inputs and Outputs
- 40-Pin PGA (Pin Grid Array) Package
- Module can be built with devices compliant to MIL-STD 883C



PIN NAMES	
A0 - A12	Address Inputs
I/O0 - I/O15	Data In/Out
CE0, CE1	Chip Enables
MCE	Master Chip Enable
WE0, WE1	Write Enables
OE	Output Enable
VDD	Power (+5V)
VSS	Ground
N.C.	No Connect

FOR FURTHER INFORMATION
SEE CHAPTER 10
FOR COMPLETE DATA SHEET