

PMEG2020EJ

20 V, 2 A very low V_{F} MEGA Schottky barrier rectifier in SOD323F package

Rev. 02 — 31 January 2005

Product data sheet



1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection encapsulated in a SOD323F (SC-90) very small and flat SMD plastic package.

1.2 Features

- Forward current: 2 A
- Reverse voltage: 20 V
- Very low forward voltage
- Very small and flat SMD package.

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications.

1.4 Quick reference data

Table 1: Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _F	continuous forward current	T _s ≤ 55 °C	-	-	2	Α
V _R	continuous reverse voltage		-	-	20	V
V _F	forward voltage	I _F = 2 A; see <u>Figure 1</u>	<u>[1]</u> -	450	525	mV

[1] Pulse test: $t_0 \le 300 \,\mu\text{s}$; $\delta \le 0.02$.



2. Pinning information

Table 2: Discrete pinning

Pin	Description	Simplified outline Symbol
1	cathode	<u>[1]</u>
2	anode	1 1 2 sym001

^[1] The marking bar indicates the cathode.

3. Ordering information

Table 3: Ordering information

Type number	Package		
	Name	Description	Version
PMEG2020EJ	SC-90	plastic surface mounted package; 2 leads	SOD323F

4. Marking

Table 4: Marking codes

Type number	Marking code
PMEG2020EJ	CA

5. Limiting values

Table 5: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{R}	continuous reverse voltage		-	20	V
I _F	continuous forward current	$T_s \le 55$ °C	-	2	А
I _{FRM}	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.5$	-	7	Α
I _{FSM}	non-repetitive peak forward current	t = 8 ms; square wave	-	10	Α
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T_{stg}	storage temperature		-65	+150	°C

6. Thermal characteristics

Table 6: Thermal characteristics

Symbol	Parameter	Conditions	ı	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1][2]	-	-	350	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		[2] [3] .	-	-	55	K/W

^[1] Refer to standard mounting conditions; see Figure 5.

7. Characteristics

Table 7: Characteristics

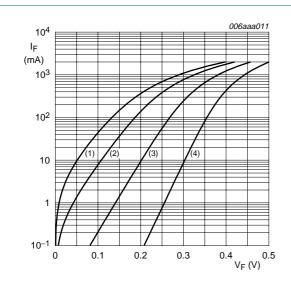
 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{F}	forward voltage	see Figure 1	<u>[1]</u>			
		$I_F = 0.01 A$	-	200	220	mV
		$I_F = 0.1 A$	-	260	290	mV
		I _F = 1 A	-	370	430	mV
		I _F = 2 A	-	450	525	mV
I _R	reverse current	see Figure 2				
		$V_R = 5 V$	-	15	50	μΑ
		V _R = 10 V	-	20	80	μΑ
		$V_R = 20 \text{ V}$	-	45	200	μΑ
C _d	diode capacitance	$V_R = 5 \text{ V}; f = 1 \text{ MHz};$ see Figure 3	-	50	60	pF

^[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02.$

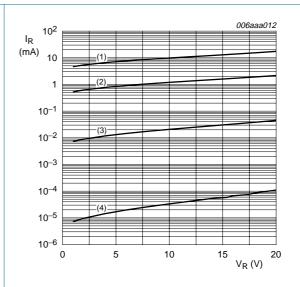
^[2] For Schottky barrier rectifier thermal run-away has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and I_{F(AV)} rating will be available on request.

^[3] Soldering point of cathode tab.



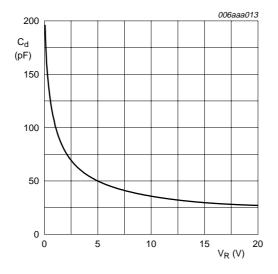
- (1) $T_{amb} = 125 \, ^{\circ}C$
- (2) $T_{amb} = 85 \, ^{\circ}C$
- (3) $T_{amb} = 25 \,^{\circ}C$
- (4) $T_{amb} = -40 \, ^{\circ}C$

Fig 1. Forward current as a function of forward voltage; typical values



- (1) $T_{amb} = 125 \,^{\circ}C$
- (2) $T_{amb} = 85 \, ^{\circ}C$
- (3) $T_{amb} = 25 \, ^{\circ}C$
- (4) $T_{amb} = -40 \, ^{\circ}C$

Fig 2. Reverse current as a function of reverse voltage; typical values



 T_{amb} = 25 °C; f = 1 MHz.

Fig 3. Diode capacitance as a function of reverse voltage; typical values

8. Package outline

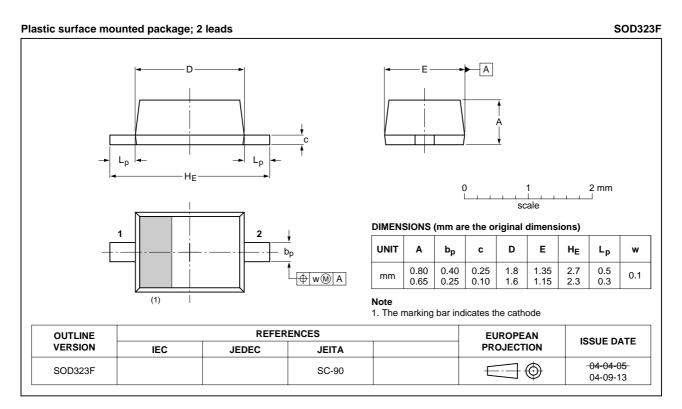


Fig 4. Package outline SOD323F (SC-90)

9. Packing information

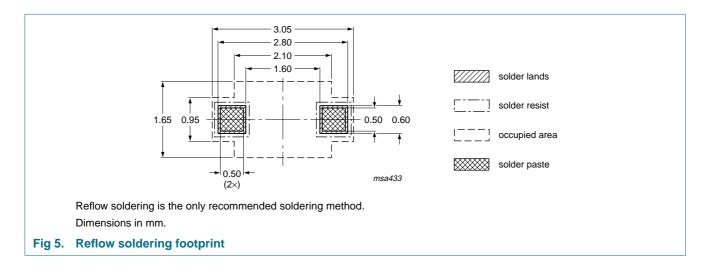
Table 8: Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code. [1]

Type number	Package	Description	Packing quantity	
			3000	10000
PMEG2020EJ	SOD323F	4 mm pitch, 8 mm tape and reel	-115	-135

^[1] For further information and the availability of packing methods, see Section 15.

10. Soldering





11. Revision history

Table 9: Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
PMEG2020EJ_2	20050131	Product data sheet	-	9397 750 14314	PMEG2020EJ_1
Modifications: • This data sheet is an enhancement of data sheet PMEG2020EJ_1					
	 Data shee 	t status changed to Produ	ıct data sheet		
PMEG2020EJ_1	20040830	Preliminary data sheet	-	9397 750 13536	-

12. Data sheet status

Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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- [1] Please consult the most recently issued data sheet before initiating or completing a design.
- [2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
- [3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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