N-channel LFPAK 40 V 3.3 mΩ standard level MOSFETRev. 04 — 25 October 2010Product data

Product data sheet

#### **Product profile** 1.

### 1.1 General description

Standard level N-channel MOSFET in LFPAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

#### 1.2 Features and benefits

- Advanced TrenchMOS provides low RDSon and low gate charge
- High efficiency gains in switching power converters

### 1.3 Applications

- DC-to-DC convertors
- Lithium-ion battery protection
- Load switching

### 1.4 Quick reference data

- Improved mechanical and thermal characteristics
- LFPAK provides maximum power density in a Power SO8 package
- Motor control
- Server power supplies

| Table 1.         | Quick reference data             |   |     |     |     |      |
|------------------|----------------------------------|---|-----|-----|-----|------|
| Symbol           | Parameter                        | Conditions  | Min | Тур | Max | Unit |
| V <sub>DS</sub>  | drain-source voltage             | T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C   | -   | -   | 40  | V    |
| I <sub>D</sub>   | drain current                    | $T_{mb} = 25 \text{ °C}; V_{GS} = 10 \text{ V};$<br>see <u>Figure 1</u>                         | -   | -   | 100 | A    |
| P <sub>tot</sub> | total power dissipation          | T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>  | -   | -   | 117 | W    |
| Tj               | junction temperature             |   | -55 | -   | 175 | °C   |
| Static cha       | aracteristics                    |   |     |     |     |      |
| $R_{DSon}$       | drain-source on-state resistance | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A;<br>T <sub>j</sub> = 100 °C; see <u>Figure 12</u> | -   | -   | 4.5 | mΩ   |
|                  |                                  | $V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$<br>T <sub>j</sub> = 25 °C; see <u>Figure 13</u>    | -   | 2.6 | 3.3 | mΩ   |



# PSMN3R3-40YS

### N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET

| Table 1.             | Quick reference data co                            | ontinued  |     |      |     |      |  |
|----------------------|--|---|-----|------|-----|------|--|
| Symbol               | Parameter  | Conditions  | Min | Тур  | Max | Unit |  |
| Dynamic              | characteristics                                    |   |     |      |     |      |  |
| $Q_{GD}$             | gate-drain charge                                  | $V_{GS}$ = 10 V; $I_{D}$ = 25 A;                                      | -   | 11.2 | -   | nC   |  |
| Q <sub>G(tot)</sub>  | total gate charge                                  | V <sub>DS</sub> = 20 V; see <u>Figure 14;</u><br>see <u>Figure 15</u> | -   | 49   | -   | nC   |  |
| Avalanch             | Avalanche ruggedness                               |   |     |      |     |      |  |
| E <sub>DS(AL)S</sub> | non-repetitive<br>drain-source avalanche<br>energy |   | -   | -    | 162 | mJ   |  |

### 2. Pinning information

| Table 2. | Pinning | j information                     |   |                |
|----------|---------|-----------------------------------|---|----------------|
| Pin      | Symbol  | Description                       | Simplified outline                                    | Graphic symbol |
| 1        | S       | source                            |   | -              |
| 2        | S       | source                            | mb  |                |
| 3        | S       | source                            |   |                |
| 4        | G       | gate                              |   |                |
| mb       | D       | mounting base; connected to drain | $ \begin{array}{cccccccccccccccccccccccccccccccccccc$ | mbb076 S       |
|          |         |                                   | SOT669 (LFPAK)  |                |

### 3. Ordering information

| Table 3. | Ordering in | formation |   |         |
|----------|-------------|-----------|---|---------|
| Type num | ber         | Package   |   |         |
|          |             | Name      | Description   | Version |
| PSMN3R3  | -40YS       | LFPAK     | plastic single-ended surface-mounted package (LFPAK); 4 leads | SOT669  |

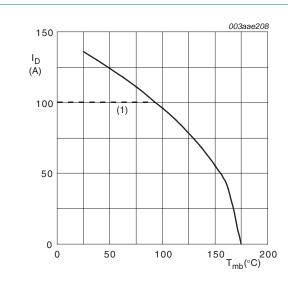
N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET

### 4. Limiting values

#### Table 4. Limiting values

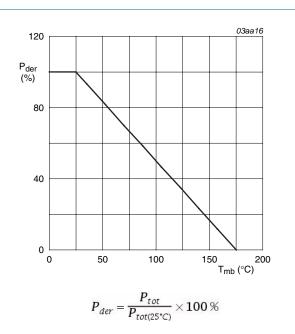
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol               | Parameter                                       | Conditions  | Min | Мах | Unit |
|----------------------|---|---|-----|-----|------|
| V <sub>DS</sub>      | drain-source voltage                            | T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C   | -   | 40  | V    |
| V <sub>DGR</sub>     | drain-gate voltage                              | $T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$                                    | -   | 40  | V    |
| V <sub>GS</sub>      | gate-source voltage                             |   | -20 | 20  | V    |
| I <sub>D</sub>       | drain current                                   | $V_{GS}$ = 10 V; $T_{mb}$ = 100 °C; see <u>Figure 1</u>   | -   | 97  | А    |
|                      |   | $V_{GS}$ = 10 V; $T_{mb}$ = 25 °C; see <u>Figure 1</u>  | -   | 100 | А    |
| I <sub>DM</sub>      | peak drain current                              | pulsed; t <sub>p</sub> ≤ 10 µs; T <sub>mb</sub> = 25 °C;<br>see <u>Figure 3</u>                                 | -   | 546 | A    |
| P <sub>tot</sub>     | total power dissipation                         | T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>  | -   | 117 | W    |
| T <sub>stg</sub>     | storage temperature                             |   | -55 | 175 | °C   |
| Tj                   | junction temperature                            |   | -55 | 175 | °C   |
| T <sub>sld(M)</sub>  | peak soldering temperature                      |   | -   | 260 | °C   |
| Source-drai          | n diode   |   |     |     |      |
| I <sub>S</sub>       | source current                                  | T <sub>mb</sub> = 25 °C   | -   | 100 | А    |
| I <sub>SM</sub>      | peak source current                             | pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^{\circ}C$  | -   | 546 | А    |
| Avalanche r          | uggedness                                       |   |     |     |      |
| E <sub>DS(AL)S</sub> | non-repetitive drain-source<br>avalanche energy | $V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 100 A;<br>$V_{sup} \le 40$ V; unclamped; $R_{GS}$ = 50 $\Omega$ | -   | 162 | mJ   |



 $V_{GS} \ge 10$  V; (1) Capped at 100 A due to package.

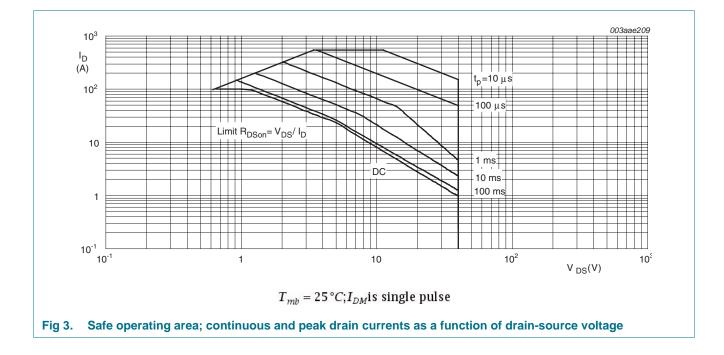
Fig 1. Continuous drain current as a function of mounting base temperature





## PSMN3R3-40YS

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#### **Thermal characteristics** 5.

Thermal characteristics

| Symbol                | Parameter   | Conditions   | Min | Тур  | Max       | Unit |
|-----------------------|---|--------------|-----|------|-----------|------|
| R <sub>th(j-mb)</sub> | thermal resistance from junction to mounting base | see Figure 4 | -   | 0.54 | 1.28      | K/W  |
|                       |   |              |     |      | 003aae210 |      |

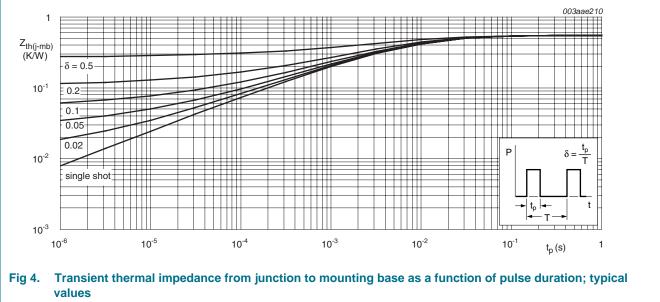


Table 5.

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### 6. Characteristics

| Symbol                 | Parameter                         | Conditions   | Min | Тур  | Max  | Unit |
|------------------------|-----------------------------------|--|-----|------|------|------|
| Static chara           | cteristics                        |  |     |      |      |      |
| V <sub>(BR)DSS</sub>   | drain-source breakdown            | I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C  | 36  | -    | -    | V    |
|                        | voltage                           | $I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^{\circ}C$  | 40  | -    | -    | V    |
| V <sub>GS(th)</sub>    | gate-source threshold voltage     | I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = -55 °C;<br>see <u>Figure 10</u>        | -   | -    | 4.6  | V    |
|                        |                                   | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$<br>see <u>Figure 10</u>                               | 1   | -    | -    | V    |
|                        |                                   | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$<br>see <u>Figure 11</u> ; see <u>Figure 10</u>         | 2   | 3    | 4    | V    |
| I <sub>DSS</sub>       | drain leakage current             | $V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$   | -   | 0.02 | 1    | μΑ   |
|                        |                                   | $V_{DS}$ = 40 V; $V_{GS}$ = 0 V; $T_j$ = 125 °C  | -   | 10   | 100  | μΑ   |
| I <sub>GSS</sub>       | gate leakage current              | $V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$   | -   | 10   | 100  | nA   |
|                        |                                   | $V_{GS} = -20 \text{ V};  V_{DS} = 0 \text{ V};  T_j = 25 ^{\circ}\text{C}$  | -   | 10   | 100  | nA   |
| R <sub>DSon</sub>      | drain-source on-state resistance  | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 100 °C;<br>see <u>Figure 12</u>                    | -   | -    | 4.5  | mΩ   |
|                        |                                   | $V_{GS}$ = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C;<br>see <u>Figure 12</u>                           | -   | 4.7  | 5.94 | mΩ   |
|                        |                                   | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C;<br>see <u>Figure 13</u>                     | -   | 2.6  | 3.3  | mΩ   |
| R <sub>G</sub>         | internal gate resistance (AC)     | f = 1 MHz  | -   | 0.67 | -    | Ω    |
| Dynamic ch             | aracteristics                     |  |     |      |      |      |
| Q <sub>G(tot)</sub>    | total gate charge                 | $I_D = 0 \text{ A}; \text{ V}_{DS} = 0 \text{ V}; \text{ V}_{GS} = 10 \text{ V}$                                   | -   | 39   | -    | nC   |
|                        |                                   | $I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 10 \text{ V};$<br>see <u>Figure 14</u> ; see <u>Figure 15</u> | -   | 49   | -    | nC   |
| Q <sub>GS</sub>        | gate-source charge                | $I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 10 \text{ V};$  | -   | 13.8 | -    | nC   |
| Q <sub>GS(th)</sub>    | pre-threshold gate-source charge  | see <u>Figure 14</u>   | -   | 8.3  | -    | nC   |
| Q <sub>GS(th-pl)</sub> | post-threshold gate-source charge |  | -   | 5.5  | -    | nC   |
| Q <sub>GD</sub>        | gate-drain charge                 | $I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 10 \text{ V};$<br>see <u>Figure 14</u> ; see <u>Figure 15</u> | -   | 11.2 | -    | nC   |
| V <sub>GS(pl)</sub>    | gate-source plateau voltage       | $I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; \text{ see } \frac{\text{Figure } 14}{\text{Figure } 15}$              | -   | 4.9  | -    | V    |
| C <sub>iss</sub>       | input capacitance                 | $V_{DS} = 20 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$  | -   | 2754 | -    | pF   |
| C <sub>oss</sub>       | output capacitance                | T <sub>j</sub> = 25 °C; see <u>Figure 16</u>   | -   | 600  | -    | pF   |
| C <sub>rss</sub>       | reverse transfer capacitance      |  | -   | 316  | -    | pF   |
| d(on)                  | turn-on delay time                | $V_{DS} = 20 \text{ V}; \text{ R}_{L} = 0.8 \Omega; \text{ V}_{GS} = 10 \text{ V};$                                | -   | 21   | -    | ns   |
| t <sub>r</sub>         | rise time                         | $R_{G(ext)} = 4.7 \Omega$  | -   | 21   | -    | ns   |
| t <sub>d(off)</sub>    | turn-off delay time               |  | -   | 38   | -    | ns   |
| t <sub>f</sub>         | fall time                         |  | -   | 14   | -    | ns   |

Symbol

Source-drain diode

# **PSMN3R3-40YS**

Тур

Max

Unit

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Min

| / <sub>SD</sub>   | source-drain voltage  | I <sub>S</sub> = 25 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C;<br>see <u>Figure 17</u>                   | -  | 0.82                   | 1.2                         | V     |
|---|---|---|--|------------------------|-----------------------------|-------|
| rr  | reverse recovery time   | I <sub>S</sub> = 40 A; dI <sub>S</sub> /dt = -100 A/µs;   | -  | 44                     | -                           | ns    |
| Q <sub>r</sub>  | recovered charge  | $V_{GS} = 0 \text{ V};  V_{DS} = 20 \text{ V}$  | -  | 48                     | -                           | nC    |
| 100<br>I <sub>D</sub><br>(A)<br>80                          |   | 003aae211 80 I <sub>D</sub> (A) 60 60   |  |                        | 003aae212                   |       |
| 60<br>40<br>20  |   | 40  | T <sub>i</sub> = 175 °                                   |                        | = 25 °C                     |       |
| 0   |   | $(V) = 4.5$ $V_{DS}(V)^{1}$   | 2  |                        | 6<br>3S(V)                  |       |
|   |   |   |  |                        |                             |       |
|   | $T_j = 25 ^{\circ}C$<br>Dutput characteristics: drain cu<br>unction of drain-source voltage |   | $V_{DS} > I_D \times R$<br>haracteristics<br>gate-source | : drain c              |                             |       |
|   | Output characteristics: drain cu<br>unction of drain-source voltage                         |   | haracteristics   | : drain c<br>voltage;  |                             |       |
| 100<br>g <sub>fs</sub><br>(S)                               | Output characteristics: drain cu<br>unction of drain-source voltage                         | b; typical values     function of       003aae213     6000       C     C  | haracteristics   | : drain c<br>voltage;  | ; typical                   |       |
| 100<br>g <sub>fs</sub><br>(S)<br>80                         | Output characteristics: drain cu<br>unction of drain-source voltage                         | 203aae213<br>C<br>(pF)  | haracteristics   | : drain c<br>voltage;  | typical           003aae214 |       |
| ft<br>100<br>g <sub>fs</sub><br>(S)<br>80<br>60<br>40<br>20 | Dutput characteristics: drain cu<br>unction of drain-source voltage                         | bit     function of       003aae213     6000       C     C       (pF)     4000                                  | haracteristics<br>gate-source                            | c drain c<br>voltage;  | ; typical                   | value |
| ft<br>100<br>g <sub>fs</sub><br>(S)<br>80<br>60<br>40<br>20 | Dutput characteristics: drain cu<br>unction of drain-source voltage                         | a; typical values     function of       203aae213     6000       C     (pF)       4000     2000       ID     80 | haracteristics<br>gate-source                            | e: drain c<br>voltage; | 2003aae214                  | value |

Conditions

#### Characteristics ... continued Table 6.

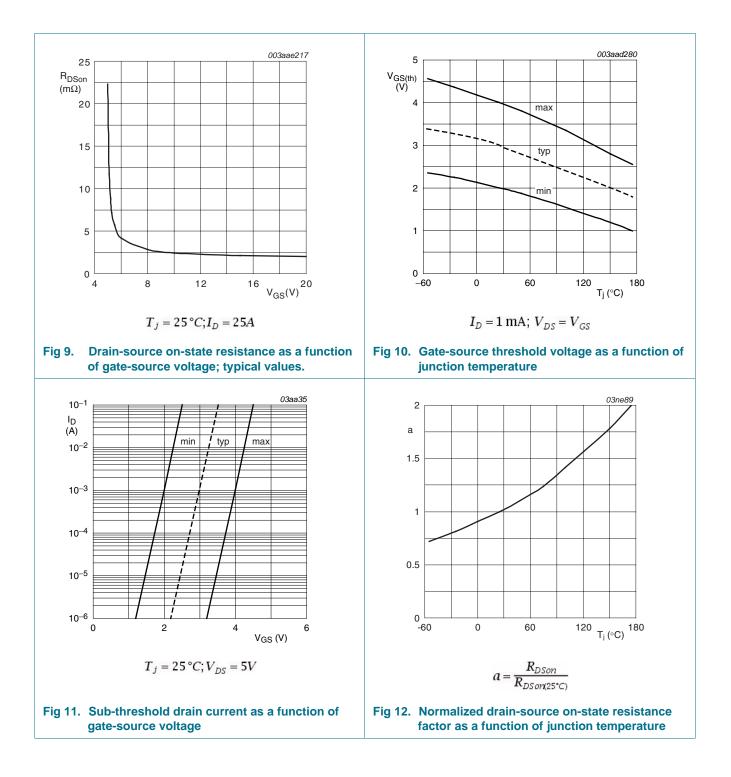
Parameter

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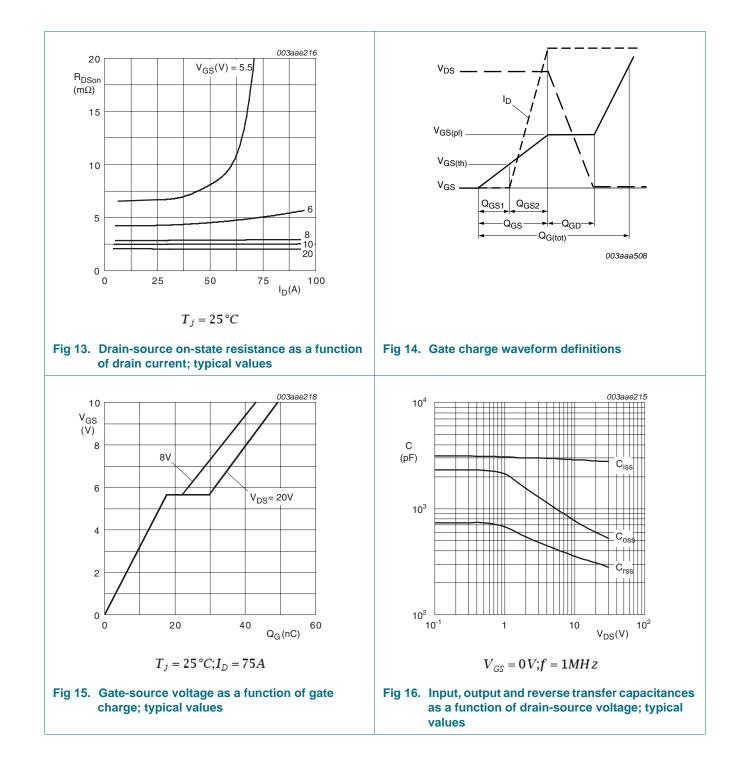
## PSMN3R3-40YS

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## PSMN3R3-40YS

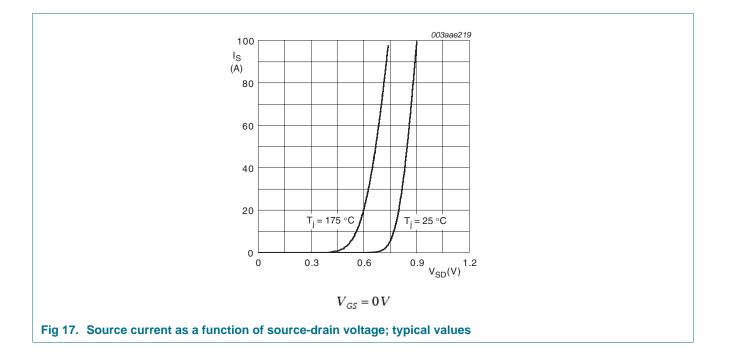
#### N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET



PSMN3R3-40YS

# PSMN3R3-40YS

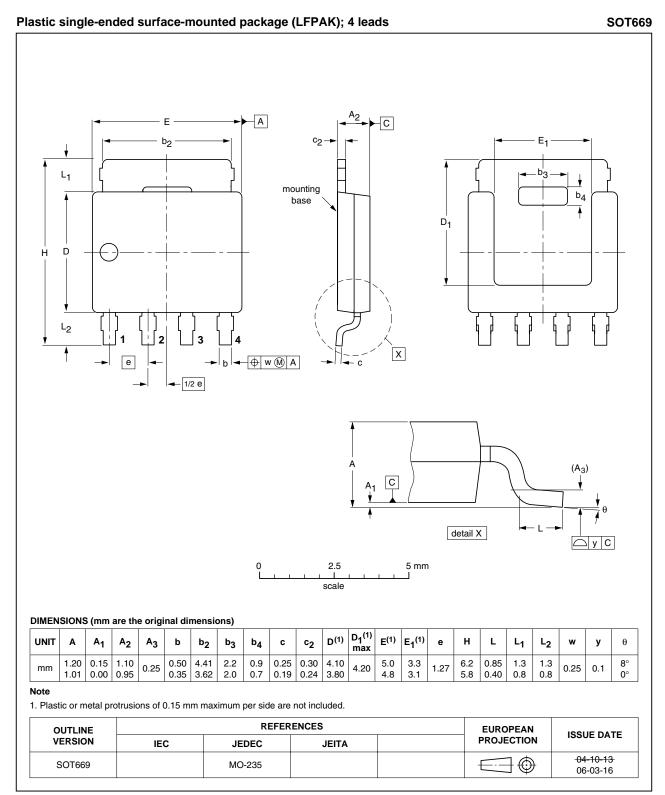
N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET



### PSMN3R3-40YS

N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET

### 7. Package outline



#### Fig 18. Package outline SOT669 (LFPAK)

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### 8. Revision history

| Table 7. | <b>Revision history</b> |  |
|----------|-------------------------|--|
|----------|-------------------------|--|

| Document ID      | Release date                        | Data sheet status  | Change notice | Supersedes       |
|------------------|-------------------------------------|--------------------|---------------|------------------|
| PSMN3R3-40YS v.4 | 20101025                            | Product data sheet | -             | PSMN3R3-40YS v.3 |
| Modifications:   | <ul> <li>Various changes</li> </ul> | to content.        |               |                  |
| PSMN3R3-40YS v.3 | 20100930                            | Product data sheet | -             | PSMN3R3-40YS v.2 |

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### 9. Legal information

#### 9.1 Data sheet status

| Document status[1][2]          | Product status <sup>[3]</sup> | Definition  |
|--------------------------------|-------------------------------|---|
| Objective [short] data sheet   | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nxp.com</u>.

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N-channel LFPAK 40 V 3.3 mΩ standard level MOSFET

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Date of release: 25 October 2010 Document identifier: PSMN3R3-40YS