50Ω 684 to 800 MHz

The Big Deal

- · Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

Product Overview

The KSN-800A-119+ is a Frequency Synthesizer, designed to operate from 684 to 800 MHz for W-CDMA application. The KSN-800A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -104 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -96 dBc typ. • Reference Spurious: -116 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-800A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-800A-119+ to be used in compact designs.







 50Ω 684 to 800 MHz

Features

- Integrated VCO + PLL
- Low phase noise and spurious
- · Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3.3V)
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042 PRICE: \$29.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

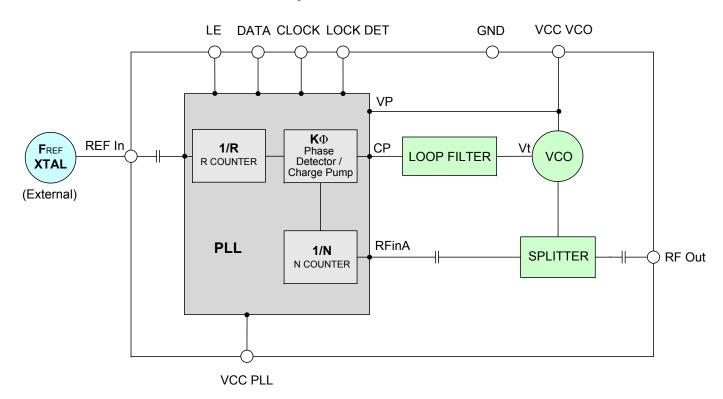
Applications

W-CDMA

General Description

The KSN-800A-119+ is a Frequency Synthesizer, designed to operate from 684 to 800 MHz for W-CDMA application. The KSN-800A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-800A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic





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HEV. OH M126669 EDR-8530/2F1 KSN-800A-119+ Category-A1 RAV 100321

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Notes: 1. Performance and quality attributes and conditions not expressly stated in this specification sheet are intended to be excluded and do not form a part of this specification sheet. 2. Electrical specifications and performance data contained herein are based on Mini-Circuits applicable established test performance criteria and measurement instructions. 3. The parts covered by this specification sheet are subject to Mini-Circuit standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this rat en entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp.

Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units			
Frequency Range	-	684	-	800	MHz				
Step Size	-	-	200	-	kHz				
Settling Time		Within ± 500 Hz	-	15	-	mSec			
Output Power		-	0	+3	+6	dBm			
		@ 100 Hz offset	-	-80	-				
		@ 1 kHz offset	-	-76	-70	-			
SSB Phase Noise		@ 10 kHz offset	-	-104	-98	dBc/Hz			
		@ 100 kHz offset	-	-128	-121				
		@ 1 MHz offset	-	-148	-141				
Reference Spurious Suppre	ession	Ref. Freq. 10 MHz	-	-116	-86				
Comparison Spurious Supp	pression	Step Size 200 kHz	-	-96	-76	dD.			
Non - Harmonic Spurious S	Suppression	-	-	-90	-	dBc			
Harmonic Suppression		-	-	-30	-25				
VCO Supply Voltage		5.00	+4.75	5.00	+5.25	V			
PLL Supply Voltage		3.30	3.15	3.30	3.45	V			
VCO Supply Current		-	-	36	43	mA			
PLL Supply Current		-	-	6	13	IIIA			
	Frequency	10 (sine wave)	-	10	-	MHz			
Reference Input	Amplitude	1	-	1	-	V _{P-P}			
(External)	Input impedance	-	-	100	-	ΚΩ			
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz			
RF Output port Impedance		-	-	50	-	Ω			
Input Logic Level	Input high voltage	-	2.80	-	-	V			
input Logic Level	Input low voltage	-	-	-	0.60	V			
Digital Lock Detect	Locked	-	2.75	-	3.45	V			
Digital Lock Detect	Unlocked	-	-	-	0.40	V			
Frequency Synthesizer PLI	_	-	ADF4118						
PLL Programming		-	3-wire serial 3.3V CMOS						
	F_Register	-	(MSB) X0XXX00000X0010010010 (LSB)						
Register Map @ 800 MHz	N_Register	-	(MSB) 100000011111010000001 (LSB)						
	R_Register	-	(MSB) 1XX	(MSB) 1XXXX0000000011001000 (LSB)					

Absolute Maximum Ratings

Parameters	Ratings					
VCO Supply Voltage	6.3V					
PLL Supply Voltage	6.3V					
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.5V					
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax					
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax					
Operating Temperature	-40°C to +85°C					
Storage Temperature	-55°C to +100°C					

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	PO	POWER OUTPUT			VCO CURRENT			PLL CURENT		
(MHz)		(dBm)			(mA)			(mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
684	4.94	4.58	4.15	34.18	36.36	37.47	5.24	6.22	7.15	
685	4.93	4.57	4.14	34.18	36.37	37.48	5.20	6.19	7.13	
700	4.76	4.43	3.97	34.18	36.37	37.49	5.21	6.22	7.15	
715	4.46	4.26	3.77	34.21	36.38	37.50	5.22	6.24	7.17	
730	4.16	4.05	3.55	34.25	36.41	37.52	5.19	6.22	7.15	
745	4.01	3.81	3.31	34.31	36.44	37.54	5.21	6.24	7.18	
760	3.91	3.52	3.03	34.31	36.45	37.54	5.22	6.26	7.19	
775	3.64	3.22	2.73	34.28	36.43	37.54	5.20	6.24	7.17	
790	3.20	2.91	2.41	34.24	36.41	37.54	5.21	6.26	7.19	
800	2.90	2.71	2.20	34.21	36.41	37.54	5.20	6.25	7.18	

FREQUENCY		HARMONICS (dBc)					
(MHz)		F2			F3		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
684	-29.68	-32.70	-35.76	-52.50	-51.42	-57.83	
685	-29.82	-32.70	-35.82	-52.86	-51.30	-57.92	
700	-34.31	-36.25	-38.40	-55.24	-55.19	-63.99	
715	-38.02	-38.40	-38.62	-58.97	-60.78	-78.94	
730	-34.42	-36.56	-36.84	-54.99	-58.14	-62.40	
745	-33.75	-36.41	-37.64	-55.34	-58.67	-63.17	
760	-34.91	-37.81	-39.66	-58.43	-59.26	-62.54	
775	-38.88	-39.79	-42.65	-63.99	-60.88	-62.52	
790	-40.99	-43.74	-42.58	-60.77	-59.63	-61.77	
800	-39.99	-43.40	-41.63	-57.43	-59.57	-58.73	



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FREQUENCY	PH	ASE NOIS	E (dBc/Hz) @OFFSE	TS
(MHz)			+25°C		
	100Hz	1kHz	10kHz	100kHz	1MHz
684	-83.72	-75.87	-105.32	-129.64	-149.75
685	-82.65	-77.02	-106.58	-129.66	-149.71
700	-83.80	-76.59	-106.22	-129.50	-149.63
715	-84.52	-77.07	-105.40	-129.16	-149.39
730	-83.48	-77.68	-104.93	-129.19	-149.32
745	-83.30	-77.95	-104.39	-128.89	-149.01
760	-86.68	-77.97	-104.09	-127.57	-147.82
775	-85.07	-75.38	-102.92	-126.73	-146.83
790	-82.84	-75.34	-103.08	-125.89	-146.04
800	-81.00	-75.60	-101.97	-125.42	-145.70

EDECHENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
FREQUENCY (MHz)		-45°C						
	100Hz	1kHz	10kHz	100kHz	1MHz			
684	-82.15	-77.63	-106.15	-130.60	-150.79			
685	-84.92	-79.28	-106.01	-130.46	-151.00			
700	-79.64	-78.02	-105.98	-130.21	-150.62			
715	-83.32	-77.26	-105.23	-130.08	-150.36			
730	-81.79	-79.22	-105.50	-130.36	-150.56			
745	-83.75	-77.86	-105.25	-130.34	-150.94			
760	-85.18	-79.17	-104.60	-129.57	-149.90			
775	-84.55	-78.69	-102.69	-127.64	-147.88			
790	-82.63	-77.72	-102.55	-126.32	-146.64			
800	-82.44	-77.27	-101.80	-125.77	-145.96			

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS									
(MHz)	+85°C									
	100Hz	1kHz	10kHz	100kHz	1MHz					
684	-83.68	-77.13	-105.27	-128.26	-148.36					
685	-84.25	-76.14	-105.06	-128.19	-148.40					
700	-83.04	-76.38	-104.94	-128.22	-148.34					
715	-82.46	-76.69	-104.34	-128.03	-148.27					
730	-83.01	-77.37	-104.38	-127.84	-148.02					
745	-82.50	-77.73	-103.05	-127.36	-147.54					
760	-83.42	-76.23	-102.69	-126.50	-146.82					
775	-82.79	-74.51	-102.31	-125.73	-145.96					
790	-80.15	-75.36	-101.87	-125.17	-145.32					
800	-79.95	-74.73	-101.36	-124.69	-144.87					





COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @ Fcarrier 684MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 742MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 800MHz+(n*Fcomparison) (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-116.76	-115.98	-117.51	-115.80	-117.11	-117.67	-112.12	-116.15	-115.96
-4	-115.45	-114.30	-114.79	-117.41	-117.72	-112.22	-115.11	-116.47	-114.27
-3	-115.31	-102.15	-102.90	-112.77	-102.14	-103.13	-109.50	-101.43	-101.78
-2	-110.45	-108.20	-107.42	-105.14	-108.88	-105.36	-103.22	-108.17	-107.68
-1	-93.25	-97.76	-96.89	-96.63	-98.09	-96.50	-97.62	-93.92	-98.26
o ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-93.38	-94.57	-95.52	-95.06	-96.66	-95.10	-96.91	-98.99	-94.47
+2	-104.48	-106.33	-107.70	-105.13	-107.90	-104.59	-109.23	-105.67	-105.89
+3	-115.27	-105.99	-101.17	-110.78	-100.49	-103.78	-110.57	-101.62	-101.85
+4	-114.56	-117.23	-116.41	-114.51	-117.65	-118.40	-112.31	-112.53	-116.32
+5	-115.45	-115.69	-116.18	-116.13	-118.26	-117.39	-113.80	-116.58	-115.04

Note 1: Comparison frequency 200 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @ Fcarrier 684MHz+(n*Freference) (dBc) note 3		@Fcarrier @Fcarrier dz+(n*Freference) 742MHz+(n*Freference)			REFERENCE SPURIOUS @ Fcarrier 800MHz+(n*Freference) (dBc) note 3			
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-122.11	-112.98	-112.04	-122.17	-114.45	-111.43	-123.59	-112.42	-112.99
-4	-122.53	-123.88	-126.61	-124.90	-124.77	-123.39	-107.01	-124.21	-111.58
-3	-121.66	-120.74	-121.14	-119.18	-121.82	-121.28	-117.61	-115.79	-113.03
-2	-125.88	-129.55	-126.23	-129.85	-125.86	-125.13	-103.21	-122.99	-106.63
-1	-116.36	-118.10	-118.44	-115.58	-119.37	-114.23	-110.15	-108.22	-110.47
0 ^{note 4}	-	-	-	-	-	-	-	-	-
+1	-119.80	-121.59	-123.12	-116.73	-115.93	-114.90	-108.77	-109.33	-111.57
+2	-128.73	-126.16	-124.59	-124.96	-127.50	-124.01	-106.67	-126.34	-110.07
+3	-119.49	-121.03	-123.46	-115.02	-118.67	-118.57	-115.49	-115.58	-121.02
+4	-127.34	-125.02	-124.69	-124.89	-126.07	-124.95	-111.02	-122.88	-114.70
+5	-119.94	-115.81	-112.72	-117.31	-115.99	-110.95	-112.51	-111.76	-111.63

Note 3: Reference frequency 10 MHz

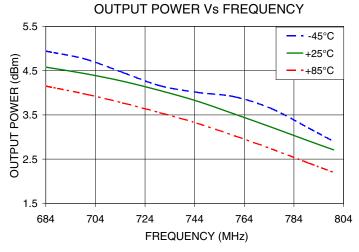
Note 4: All spurs are referenced to carrier signal (n=0).

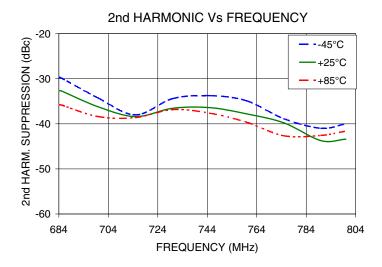


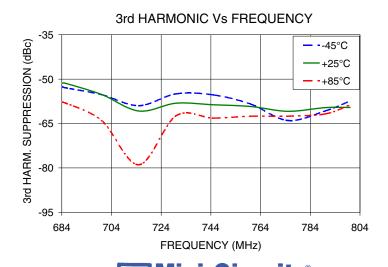
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Typical Performance Curves

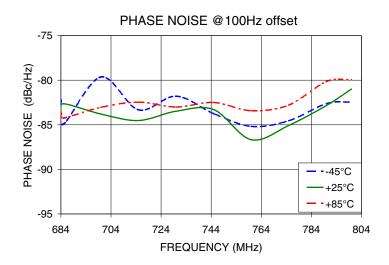


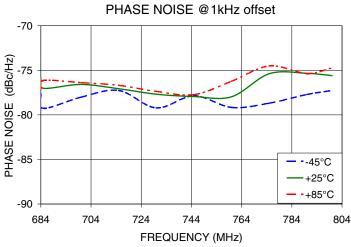


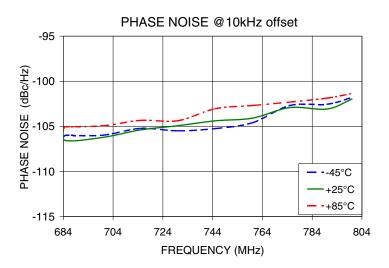


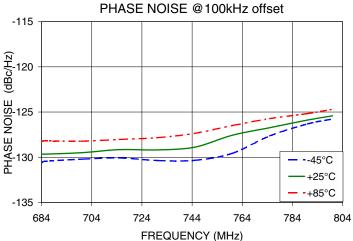
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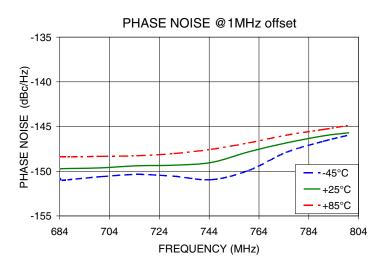










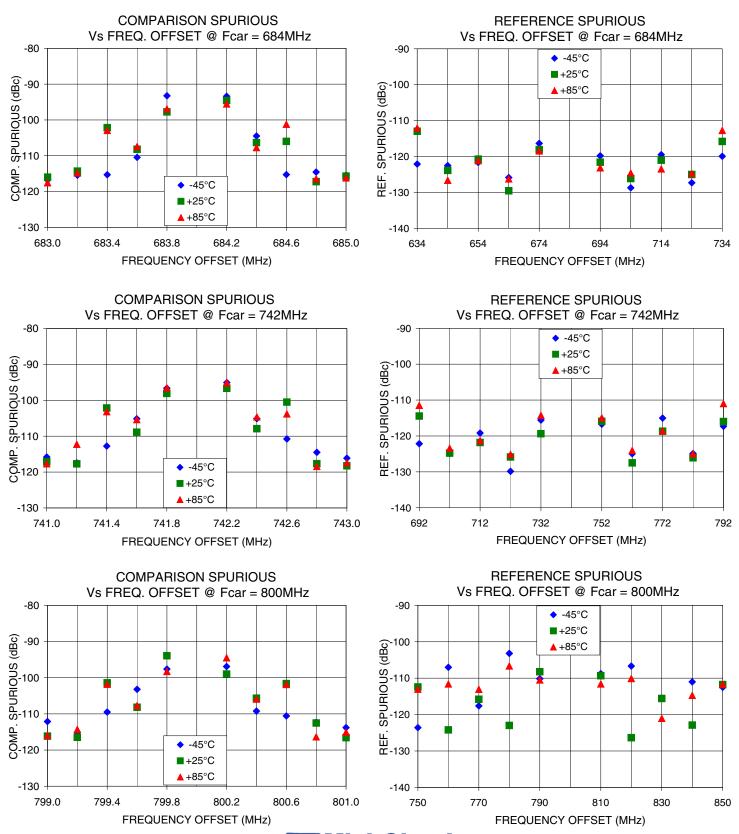


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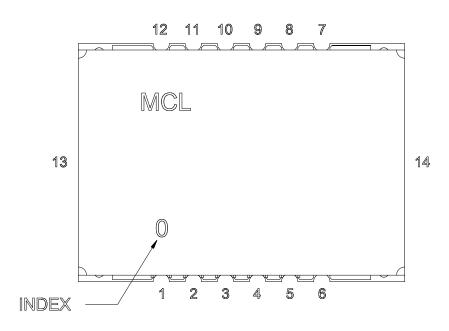
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Pin Configuration

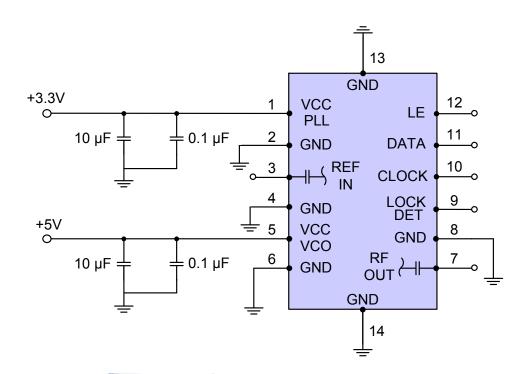


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

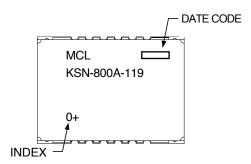




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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-1+

Environment Ratings: ENV03T2



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