

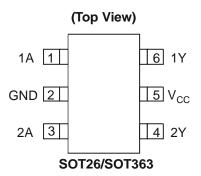
#### **Description**

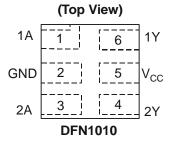
The 74LVC2G04 is a dual inverter gate with standard push-pull outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output preventing damaging current backflow when the device is powered down.

The gate performs the positive Boolean function:

$$Y=\overline{\boldsymbol{A}}$$

#### **Pin Assignments**





#### **Features**

- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.0V
- CMOS low power consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115-A)
  - o Exceeds 2000-V Human Body Model (A114-A)
  - o Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- Range of Package Options
- SOT26, SOT363, and DFN1010 Available in "Green" Molding Compound (no Br, Sb)
- Lead Free Finish/ RoHS Compliant (Note 1)

### **Applications**

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - o PCs, networking, notebooks, netbooks, PDAs
  - o Computer peripherals, hard drives, CD/DVD ROM
  - o TV, DVD, DVR, set top box
  - o Cell Phones, Personal Navigation / GPS
  - o MP3 players ,Cameras, Video Recorders

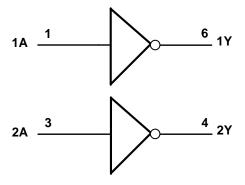
Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead\_free.html.



## **Pin Descriptions**

Pin Name	Pin NO.	Description
1A	1	Data Input
GND	2	Ground
2A	3	Data Input
2Y	4	Data Output
V <sub>CC</sub>	5	Supply Voltage
1Y	6	Data Output

## **Logic Diagram**



### **Function Table**

Inputs	Output
Α	Υ
Н	L
L	Н



### **Absolute Maximum Ratings (Note 2)**

Symbol	Description	Rating	Unit
ESD HBM	ESD Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high impedance or I <sub>OFF</sub> state	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state.	-0.3 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> <0	-50	mA
lok	Output Clamp Current	-50	mA
Io	Continuous output current	±50	mA
	Continuous current through Vdd or GND	±100	mA
$T_J$	Operating Junction Temperature	-40 to 150	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



## **Recommended Operating Conditions (Note 3)**

Symbol	Pai	rameter	Min	Max	Unit	
M	On a vertice at Maltana	Operating	1.65	5.5	V	
V <sub>CC</sub>	Operating Voltage	Data retention only	1.5		V	
		V <sub>CC</sub> = 1.65V to 1.95V	0.65 X V <sub>CC</sub>			
\/	High-level Input Voltage	$V_{CC} = 2.3V \text{ to } 2.7V$	1.7		V	
$V_{IH}$	High-level Input Voltage	$V_{CC} = 3V$ to 3.6V	2		V	
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 X V <sub>CC</sub>			
		V <sub>CC</sub> = 1.65V to 1.95V		0.35 X V <sub>CC</sub>		
\	Lavy laval innut valta sa	V <sub>CC</sub> = 2.3V to 2.7V		0.7	V	
$V_{IL}$	/ <sub>IL</sub> Low-level input voltage	V <sub>CC</sub> = 3V to 3.6V		0.8	V	
		V <sub>CC</sub> = 4.5V to 5.5V		0.3 X V <sub>CC</sub>		
VI	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	V <sub>CC</sub>	V	
		V <sub>CC</sub> = 1.65V		-4		
		V <sub>CC</sub> = 2.3V		-8		
$I_{OH}$	High-level output current			-16	mA	
		$V_{CC} = 3V$		-24		
		V <sub>CC</sub> = 4.5V		-32		
		V <sub>CC</sub> = 1.65V		4		
		V <sub>CC</sub> = 2.3V		8		
$I_{OL}$	Low-level output current	V 2V		16	mA	
		$V_{CC} = 3V$		24		
		V <sub>CC</sub> = 4.5V		32		
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$		20		
$\Delta t/\Delta V$	Input transition rise or fall rate	$V_{CC} = 3.3V \pm 0.3V$		10	ns/V	
		$V_{CC} = 5V \pm 0.5V$		5		
T <sub>A</sub>	Operating free-air temperature		-40	125	°C	

Notes: 3. Unused inputs should be held at V<sub>CC</sub> or Ground.



### **Electrical Characteristics**

		T	.,	40°C to	o 85ºC	-40°C to	125°C	
Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
		$I_{OH} = -100 \mu A$	1.65V to 5.5V	V <sub>CC</sub> – 0.1		V <sub>CC</sub> – 0.1		
		$I_{OH} = -4mA$	1.65V	1.2		0.95		
\ \/	High Level Output	$I_{OH} = -8mA$	2.3V	1.9		1.7		V
V <sub>OH</sub>	Voltage	I <sub>OH</sub> = -16mA	2)./	2.4		1.9		V
		$I_{OH} = -24mA$	3V	2.3		2.0		
		$I_{OH} = -32mA$	4.5V	3.8		3.4		
		$I_{OL} = 100 \mu A$	1.65V to 5.5V		0.1		0.1	
		I <sub>OL</sub> = 4mA	1.65V		0.45		0.70	
\/	Low Level Output	I <sub>OL</sub> = 8mA	2.3V		0.3		0.45	V
V <sub>OL</sub>	Voltage	I <sub>OL</sub> = 16mA	2)./		0.4		0.60	V
		I <sub>OL</sub> = 24mA	3V		0.55		0.80	
		$I_{OL} = 32mA$	4.5V		0.55		0.80	
II	Input Current	$V_I = 5.5 \text{ V or GND}$	0 to 5.5V		± 5		± 20	μΑ
I <sub>OFF</sub>	Power Down Leakage Current	$V_I$ or $V_O = 5.5V$	0		± 10		± 20	μA
Icc	Supply Current	$V_I = 5.5V$ of GND $I_O=0$	1.65V to 5.5V		10		40	μA
ΔI <sub>CC</sub>	Additional Supply Current	Input at V <sub>CC</sub> -0.6V	3 V to 5.5V		500		5000	μΑ

## Package Characteristics (All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = 25°C)

Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Тур.	Max	Unit
C <sub>I</sub>	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	3.3		4		pF
	The man of Designations	SOT26			204		
$\theta_{JA}$	Thermal Resistance Junction-to-Ambient	SOT363	(Note 4)		371		°C/W
	Junction-to-Ambient	DFN1010			430		
	The man of Designations	SOT26			52		
$\theta_{JC}$	Thermal Resistance Junction-to-Case  SOT363 (Note 4)		143		°C/W		
	Junction-to-Case	DFN1010			190		

Notes: 4. Test condition for SOT26, SOT363 and DFN1010: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



## **Switching Characteristics**

 $T_A$ = -40°C to 85°C, CL = 30 or 50pF (see Figure 1)

Parameter	From (Input)	TO (OUTPUT)	V <sub>CC</sub> = ± 0.	1.8 V 15V		= 2.5V ).2V	V <sub>CC</sub> = ± 0	: 3.3V ).3V	V <sub>CC</sub>	= 5V .5V	Unit
	(iliput)	(001101)	Min	Max	Min	Max	Min	Max	Min	Max	
t <sub>pd</sub>	A	Y	0.5	8.0	1.0	4.4	0.5	4.1	0.5	3.2	ns

**T<sub>A</sub>= -40°C to 125°C**, CL = 30 or 50pF (see Figure 1)

Parameter	From (Input)	TO (OUTPUT)		V <sub>CC</sub> = 1.8V ± 0.15V		= 2.5V ).2V	V <sub>CC</sub> =	= 3.3V ).3V	V <sub>CC</sub>	= 5V ).5V	Unit
	(iliput)	(OUTPUT)	Min	Max	Min	Max	Min	Max	Min	Max	
t <sub>pd</sub>	A	Y	0.5	9.5	0.5	5.4	0.5	5.5	0.5	3.8	ns

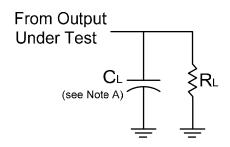
## **Operating Characteristics**

#### T<sub>△</sub> = 25 °C

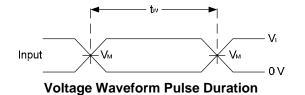
	Parameter	Test Conditions	V <sub>CC</sub> = 1.8V Typ.	V <sub>CC</sub> = 2.5V Typ.	V <sub>CC</sub> = 3.3V Typ.	V <sub>CC</sub> = 5V Typ.	Unit
C <sub>pd</sub>	Power dissipation capacitance	f = 10 MHz	17	19	20	21	pF

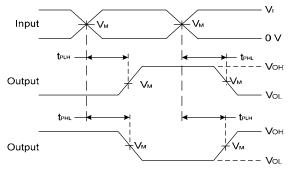


#### **Parameter Measurement Information**



V	Inp	uts	V		
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	C <sub>L</sub>	$R_L$
1.8V±0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30 pF	1 ΚΩ
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30 pF	500 Ω
3.3V±0.3V	3V	≤2.5ns	1.5 V	50 pF	500 Ω
5V±0.5V	V <sub>CC</sub>	≤2.5ns	V <sub>CC</sub> /2	50 pF	500 Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes:

- A. Includes test lead and test apparatus capacitance.
- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t<sub>PLH</sub> and t<sub>PHL</sub> are the same as t<sub>PD</sub>.



## **Ordering Information**

74LVC2G 04 XX - 7

Function Package Packing

74 : Logic Prefix

04 : Inverter / Driver

W6: SOT26 DW: SOT363 7 : Tape & Reel

LVC: 1.65 to 5.5V Family

Logic Device

FW4: DFN1010

2G: Two gates

	Device	Package	Packaging	7" Tape	and Reel
	Device	Code	(Note 5)	Quantity	Part Number Suffix
<b>Pb</b> ,	74LVC2G04W6-7	W6	SOT26	3000/Tape & Reel	-7
<b>Pb</b> ,	74LVC2G04DW-7	DW	SOT363	3000/Tape & Reel	-7
<b>Pb</b> ,	74LVC2G04FW4-7	FW4	DFN1010	5000/Tape & Reel	-7

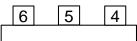
Notes:

- 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <a href="http://www.diodes.com/datasheets/ap02001.pdf">http://www.diodes.com/datasheets/ap02001.pdf</a>.
- 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf



### **Marking Information**

(1) SOT26, SOT363



XXYWX

2 3 XX: Identification code

Y: Year 0~9

W: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week X: A~Z: Internal Code

Part Number	Package	Identification Code
74LVC2G04W6	SOT26	Z2
74LVC2G04DW	SOT363	Z2

(2) DFN1010

(Top View)

XX  XX: Identification Code

: Year : 0~9

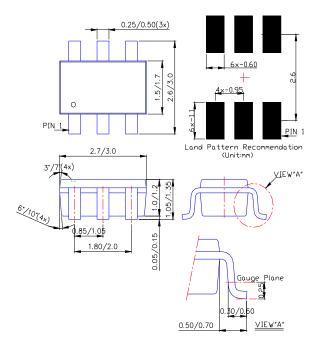
\overline{\text{\Week}}: A~Z : 1~26 week; a~z : 27~52 week; z represents

52 and 53 week X: A~Z: Internal code

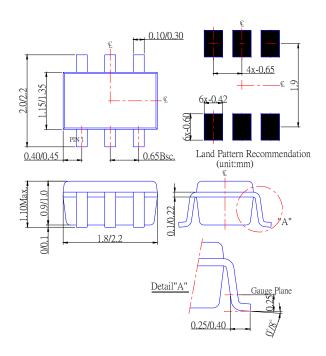
Part Number	Package	Identification Code
74LVC2G04FW4	DFN1010	<b>Z</b> 2



#### (1) Package Type: SOT26



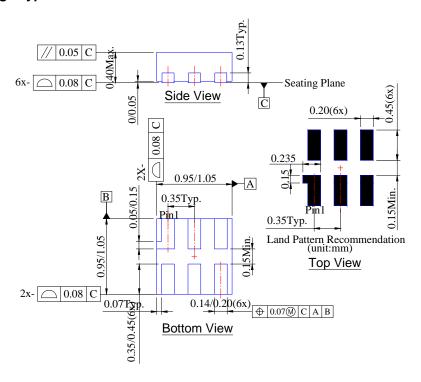
#### (2) Package Type: SOT363





### Package Outline Dimensions (All Dimensions in mm)

#### (3) Package Type: DFN1010





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