

Size		C0G Dielectric						X7R Dielectric					
Rated voltage d.c.		0805		1206		1210		0805		1206		1210	
Cap. range	Code	Minimum and Maximum capacitance values available - Chip thickness (T)											
0.47pF	0p47		0.45		0.45		0.45		0.45		0.45		
0.56	0p56		0.45		0.45		0.45		0.45		0.45		
0.68	0p68		0.45		0.45		0.45		0.45		0.45		
0.82	0p82		0.45		0.45		0.45		0.45		0.45		
1.0	1p0		0.45		0.45		0.45		0.45		0.45		
1.2	1p2		0.45		0.45		0.45		0.45		0.45		
1.5	1p5		0.45		0.45		0.45		0.45		0.45		
1.8	1p8		0.45		0.45		0.45		0.45		0.45		
2.2	2p2		0.45		0.45		0.45		0.45		0.45		
2.7	2p7		0.45		0.45		0.45		0.45		0.45		
3.3	3p3		0.45		0.45		0.45		0.45		0.45		
3.9	3p9		0.45		0.45		0.45		0.45		0.45		
4.7	4p7		0.45		0.45		0.45		0.45		0.45		
5.6	5p6		0.45		0.45		0.45		0.45		0.45		
6.8	6p8		0.45		0.45		0.45		0.45		0.45		
8.2	8p2		0.45		0.45		0.45		0.45		0.45		
10	100		0.45		0.45		0.45		0.45		0.45		
12	120		0.45		0.45		0.45		0.45		0.45		
15	150		0.45		0.45		0.45		0.45		0.45		
18	180		0.45		0.45		0.45		0.45		0.45		
22	220		0.45		0.45		0.45		0.45		0.45		
27	270		0.45		0.45		0.45		0.45		0.45		
33	330		0.45		0.45		0.45		0.45		0.45		
39	390		0.45		0.45		0.45		0.45		0.45		
47	470		0.45		0.45		0.45		0.45		0.45		
56	560		0.45		0.45		0.45		0.45		0.45		
68	680		0.45		0.45		0.45		0.45		0.45		
82	820		0.45		0.45		0.45		0.45		0.45		
100	101		0.45		0.45		0.45		0.45		0.45		
120	121		0.45		0.45		0.45		0.45		0.45		
150	151		0.45		0.45		0.45		0.45		0.45		
180	181		0.45		0.45		0.45		0.45		0.45		
220	221		0.45		0.45		0.45		0.45		0.45		
270	271		0.45		0.45		0.45		0.45		0.45		
330	331		0.45		0.45		0.45		0.45		0.45		
390	391		0.45		0.45		0.45		0.45		0.45		
470	471		0.50		0.45		0.45		0.45		0.45		
560	561		0.50		0.45		0.45		0.45		0.45		
680	681		0.60		0.45		0.45		0.45		0.45		
820	821	0.45	0.65		0.45		0.45		0.45		0.45		
1.0nF	102	0.50			0.45		0.45		0.45		0.45		
1.2	122	0.60			0.45		0.45		0.45		0.45		
1.5	152	0.65			0.50		0.45		0.45		0.45		
1.8	182		0.45	0.60			0.45		0.45		0.45		
2.2	222		0.50				0.50		0.45		0.45		
2.7	272		0.60		0.45	0.60		0.45		0.45		0.45	
3.3	332		0.65		0.50	0.65		0.45		0.45		0.45	
3.9	392				0.60				0.45		0.45		
4.7	472				0.60				0.45		0.45		
5.6	562				0.65				0.45		0.45		
6.8	682								0.45		0.45		
8.2	822								0.45		0.45		
10	103								0.45		0.45		
12	123								0.45		0.45		
15	153							0.45	0.45		0.45		
18	183							0.45	0.50		0.45		
22	223							0.45	0.60		0.45		
27	273							0.45	0.60		0.45		
33	333							0.45	0.65		0.45		
39	393							0.50		0.45	0.45		0.45
47	473							0.60		0.45	0.50		0.45
56	563							0.65		0.45	0.60		0.45
68	683								0.45	0.65	0.45	0.50	
82	823									0.50		0.45	0.60
100	104									0.60		0.45	0.60
120	124										0.65	0.45	0.65
150	154										0.50		0.60
180	184										0.60		0.65
220	224										0.65		

Application

Available in four maximum thicknesses of 0.45mm, 0.50mm, 0.60mm and 0.65mm. The 0.45mm type is ideal for use in smart cards and sensors, where an extremely low thickness is required. The 0.65mm Z5U capacitors are designed to be surface mounted beneath a plastic leaded chip carrier. This method minimises circuit inductance and allows higher packaging densities to be achieved. They are ideal for decoupling logic circuits and memories up to 1 megabyte. All types are available with either silver/palladium or nickel barrier terminations.

Ordering information
Example: 0805 J 025 0102 J C B A52

Type No/Size ref _____

Termination _____

J = Nickel Barrier

F = Palladium/Silver

Voltage d.c. _____

025 = 25V; 050 = 50V

Capacitance IEC Code _____

Suffix code

A51 = 0.45mm; A52 = 0.50mm

A53 = 0.60mm; A54 = 0.65mm

Packaging

B = Bulk; T = Taped

Dielectric code

C = C0G; X = X7R

Tolerance IEC Code