New Product



Reivision: 19-Apr-11

Vishay General Semiconductor

Surface Mount Schottky Barrier Rectifier



DO-214AC (SMA)

FEATURES

- · Low profile package
- · Ideal for automated placement
- · Guardring for overvoltage protection
- Low power losses, high efficiency
- · Very low switching losses
- High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in high frequency inverters, switching power supplies, freewheeling diodes, oring diode, DC/DC converters and reverse battery protection.

MECHANICAL DATA

Case: DO-214AC (SMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes the cathode end

| MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted) | | | | | |
|--|--------|-----------------------------------|---------------|------|--|
| PARAMETER | | SYMBOL | BYS11-90 | UNIT | |
| Device marking code | | | BYS109 | | |
| Maximum repetitive peak reverse voltage | | V _{RRM} | 90 | V | |
| Maximum average forward rectified current | | I _{F(AV)} | 1.5 | А | |
| Peak forward surge current single half sine-wave superimposed on rated load | 8.3 ms | I _{FSM} | 40 | ^ | |
| | 10 ms | | 30 | - A | |
| Voltage rate of change (rated V _R) | | dV/dt | 10 000 | V/µs | |
| Junction and storage temperature range | | T _J , T _{STG} | - 55 to + 150 | °C | |

Document Number: 89409 For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

RoHS COMPLIANT HALOGEN FREE

| PRIMARY CHARACTERISTICS | | | | |
|-------------------------|--------|--|--|--|
| I _{F(AV)} | 1.5 A | | | |
| V _{RRM} | 90 V | | | |
| I _{FSM} | 40 A | | | |
| V _F | 0.75 V | | | |
| T _J max. | 150 °C | | | |

BYS11-90





| ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | | | | |
|--|------------------|-------------------------|-------------------------------|-----|-------------------------------|----------|------|
| PARAMETER | TEST CONDITIONS | | TEST CONDITIONS | | SYMBOL | BYS11-90 | UNIT |
| Maximum instantaneous forward voltage | 1.0 A | | forward voltage 1.0 A | | V _F ⁽¹⁾ | 750 | mV |
| Maximum DC reverse current | M | T _J = 25 °C | I _B ⁽¹⁾ | 100 | μA | | |
| | V _{RRM} | T _J = 100 °C | IR | 1 | mA | | |

Note

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

| THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted) | | | | |
|--|---------------------------------|----------|------|--|
| PARAMETER | | BYS11-90 | UNIT | |
| Maximum thermal resistance, junction to lead | | 25 | °C/W | |
| | R _{0JA} ⁽¹⁾ | 150 | | |
| Maximum thermal resistance, junction to ambient | R _{0JA} ⁽²⁾ | 125 | °C/W | |
| | R _{0JA} ⁽³⁾ | 100 | | |

Notes

⁽¹⁾ Mounted on epoxy-glass hard tissue

 $^{(2)}\,$ Mounted on epoxy-glass hard tissue, 50 mm^2 35 μm Cu

⁽³⁾ Mounted on Al-oxide-ceramic (Al₂O₃), 50 mm² 35 µm Cu

| ORDERING INFORMATION (Example) | | | | | | |
|--------------------------------|-----------------|------------------------|---------------|------------------------------------|--|--|
| PREFERRED P/N | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE | | |
| BYS11-90-M3/TR | 0.064 | TR | 1800 | 7" diameter plastic tape and reel | | |
| BYS11-90-M3/TR3 | 0.064 | TR3 | 7500 | 13" diameter plastic tape and reel | | |

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

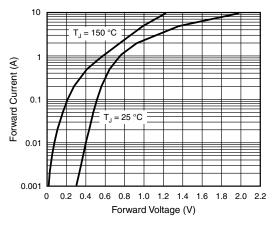


Fig. 1 - Forward Current vs. Forward Voltage

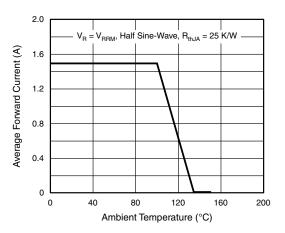


Fig. 2 - Max. Average Forward Current vs. Ambient Temperature

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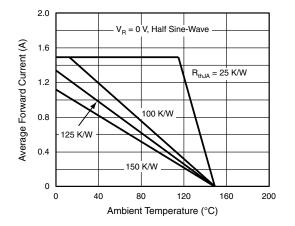


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

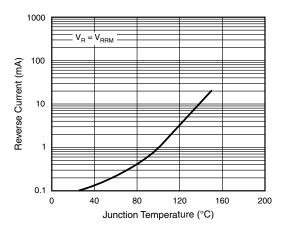


Fig. 4 - Reverse Current vs. Junction Temperature

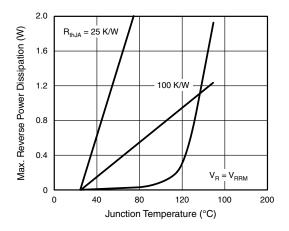


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

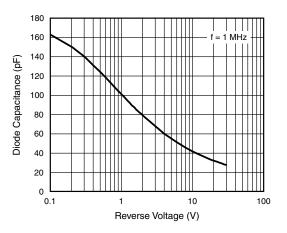
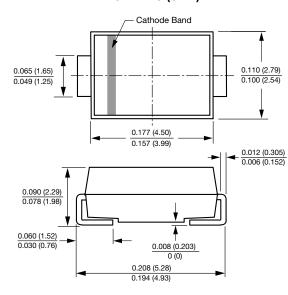


Fig. 6 - Diode Capacitance vs. Reverse Voltage

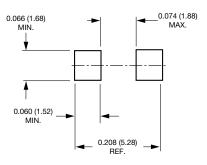


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PACKAGE OUTLINE DIMENSIONS in inches (millimeters) DO-214AC (SMA)



Mounting Pad Layout





Vishay

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