600 WATT MULTI-LINE ULTRA LOW CAPACITANCE TVS ARRAY



DESCRIPTION

The SLVU2.8-8 is an ultra low capacitance TVS array that provides four line pairs of protection. This device protects high-frequency applications such as voice, video and data related systems and is designed to minimize the effects of high overshoot voltage experienced during and ESD event. This device has an in-line design, which reduces lead inductance thus providing lower overshoot voltage.

The SLVU2.8-8 meets IEC 61000-4-2, IEC 61000-4-4 and IEC 61000-4-5 requirements. Packaged in an SO-8 configuration, this device is rated for 600 Watts Peak Pulse Power, for an $8/20\mu s$ waveform.

FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 24A, 8/20μs Level 2(Line-Gnd) & Level 3(Line-Line)
- 600 Watts Peak Pulse Power per Line (tp = 8/20µs)
- Protects up to Four Line Pairs
- Low Leakage Current < 1.0μA
- Ultra Low Capacitance: 6pF Typical
- · RoHS Compliant
- REACH Compliant

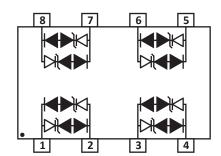
MECHANICAL CHARACTERISTICS

- Molded JEDEC SO-8 Package
- Approximate Weight: 70 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
 - Pure-Tin Sn, 100: 260-270°C
- 12mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

APPLICATIONS

- Ethernet 10/100/1000 Base T
- SMART Phones
- Audio/Video Inputs
- · Portable Electronics

PIN CONFIGURATION



TYPICAL DEVICE CHARACTERISTICS

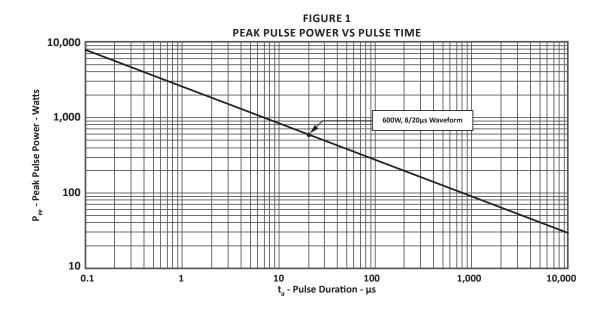
| MAXIMUM RATINGS @ 25°C Unless Otherwise Specified | | | | | | | | |
|---|------------------|------------|-------|--|--|--|--|--|
| PARAMETER | SYMBOL | VALUE | UNITS | | | | | |
| Peak Pulse Power (tp = 8/20μs) - See Figure 1 | P _{pp} | 600 | Watts | | | | | |
| Peak Pulse Current (tp = 8/20μs) | I _{pp} | 30 | Amps | | | | | |
| Lead Soldering Temperature | I _{FRM} | 260 | °C | | | | | |
| Operating Temperature | T _L | -55 to 150 | °C | | | | | |
| Storage Temperature | T _{stg} | -55 to 150 | °C | | | | | |

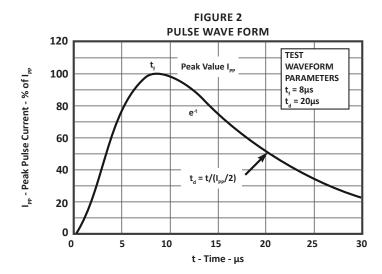
| ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified | | | | | | | | | | |
|---|-------------------|---|--|---|--|--|--|---|---|------------------------------------|
| PART NUMBER (Note 1) | DEVICE MARKING | RATED STAND-OFF VOLTAGE (Note 1) | MINIMUM BREAK- DOWN VOLTAGE (Note 1) | MINIMUM SNAPBACK VOLTAGE (Note 1) | MAXIMUM CLAMPING VOLTAGE (Note 1) (Fig. 2) | | | | MAXIMUM LEAKAGE CURRENT (Note 1) | TYPICAL CAPACITANCE (Note 1) |
| | | V _{wм} VOLTS | @1mA V _(BR) VOLTS | @I _{SB} = 50mA V _{SB} VOLTS | @I _{PP} = 2A V _C VOLTS | @I _{PP} = 5A V _C VOLTS | @I _{PP} =24A V _C VOLTS | @I _{PP} = 30A V _C VOLTS | @V _{wм} Ι _D μΑ | @0V, 1MHz C pF |
| SLVU2.8-8 | SL8 | 2.8 | 3.0 | 2.8 | 5.5 | 8.5 | 15 | 17 | 1.0 | 6 |

NOTES

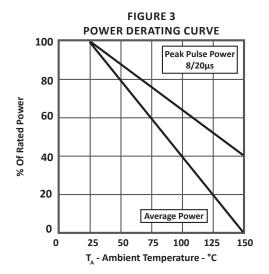
^{1.} Device measured between pin 1 to pin 2, pin 3 to pin 4, pin 5 to pin 6 and pin 7 to pin 8.

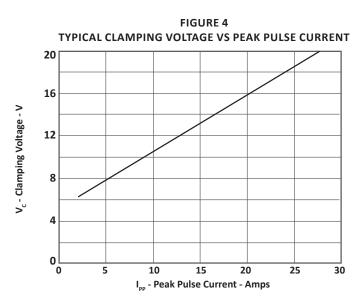
TYPICAL DEVICE CHARACTERISTICS





TYPICAL DEVICE CHARACTERISTICS





APPLICATION INFORMATION

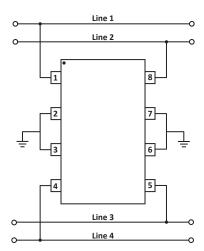


FIGURE 1 - BIDIRECTIONAL COMMON-MODE PROTECTION

The SLVU2.8-8 provides 4 lines of protection in a common mode configuration. Circuit connectivity is as follows:

- Line 1 connected to Pin 1
- Line 2 connected to Pin 8
- Line 3 connected to Pin 5
- Line 4 connected to Pin 4
- Pins 2, 3, 6, 7 are connected to ground

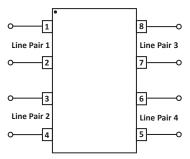


FIGURE 2 - BIDIRECTIONAL DIFFERENTIAL-MODE PROTECTION

The SLVU2.8-8 provides four line pairs in a differential mode configuration. Circuit connectivity is as follows:

- Line Pair 1 connected to Pins 1 & 2
- Line Pair 2 connected to Pins 3 & 4
- Line Pair 3 connected to Pins 5 & 6
- Line Pair 4 connected to Pins 7 & 8

CIRCUIT BOARD RECOMMENDATIONS

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.



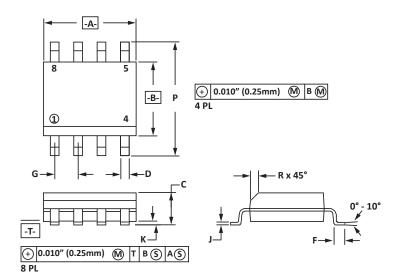


SO-8 PACKAGE INFORMATION

| OUTLINE DIMENSIONS | | | | | | | | | |
|--------------------|----------|--------|--------|-------|--|--|--|--|--|
| DIM | MILLIN | IETERS | INCHES | | | | | | |
| | MIN | MAX | MIN | MAX | | | | | |
| Α | 4.80 | 5.00 | 0.189 | 0.196 | | | | | |
| В | 3.80 | 4.00 | 0.150 | 0.157 | | | | | |
| С | 1.35 | 1.75 | 0.054 | 0.068 | | | | | |
| D | 0.35 | 0.49 | 0.014 | 0.019 | | | | | |
| F | 0.40 | 1.25 | 0.016 | 0.049 | | | | | |
| G | 1.27 BSC | | 0.05 | BSC | | | | | |
| J | 0.18 | 0.25 | 0.007 | 0.009 | | | | | |
| К | 0.10 | 0.25 | 0.004 | 0.008 | | | | | |
| Р | 5.80 | 6.20 | 0.229 | 0.244 | | | | | |
| R | 0.25 | 0.50 | 0.010 | 0.019 | | | | | |

NOTES

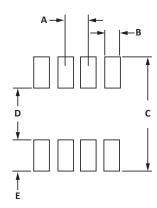
- 1. -T- = Seating plane and datum surface.
- 2. Dimensions "A" and "B" are datum.
- 3. Dimensions "A" and "B" do not include mold protrusion.
- 4. Maximum mold protrusion is 0.015" (0.380mm) per side.
- 5. Dimensioning and tolerances per ANSI Y14.5M, 1982.
- 6. Dimensions are exclusive of mold flash and metal burrs.



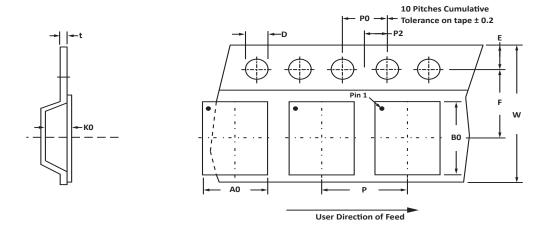
| PAD LAYOUT DIMENSIONS | | | | | | | | | |
|-----------------------|--------|--------|--------|-------|--|--|--|--|--|
| DIM | MILLIN | IETERS | INCHES | | | | | | |
| | MIN | MAX | MIN | MAX | | | | | |
| А | 1.14 | 1.40 | 0.045 | 0.055 | | | | | |
| В | 0.64 | 0.89 | 0.025 | 0.035 | | | | | |
| С | 6.22 | - | 0.245 | - | | | | | |
| D | 3.94 | 4.17 | 0.155 | 0.165 | | | | | |
| E | 1.02 | 1.27 | 0.040 | 0.050 | | | | | |

NOTES

1. Controlling dimension: inches.



TAPE AND REEL



| SPECIFICATIONS | | | | | | | | | | | | |
|----------------|---------------|-------------|-------------|-------------|-------------|-------------|-------------|--------------|-------------|-------------|-------------|------|
| REEL DIA. | TAPE WIDTH | A0 | В0 | ко | D | E | F | w | P0 | P2 | Р | tmax |
| 178mm (7") | 12mm | 6.50 ± 0.10 | 5.40 ± 0.10 | 2.00 ± 0.10 | 1.50 ± 0.10 | 1.75 ± 0.10 | 5.50 ± 0.05 | 12.00 ± 0.30 | 4.00 ± 0.12 | 2.00 ± 0.10 | 4.00 ± 0.10 | 0.25 |

NOTES

- 1. Dimensions are in millimeters.
- 2. Surface mount product is taped and reeled in accordance with EIA-481.
- 3. Suffix T7 = 7" Reel 1,000 pieces per 12mm tape.
- 4. Suffix T13 = 13" Reel 2,500 pieces per 12mm tape.
- 5. Bulk product shipped in tubes of 98 pieces per tube.
- 6. Marking on Part marking code (see page 2), date code, logo and pin one defined by dot on top of package.

Package outline, pad layout and tape specifications per document number 06011.R4 8/10.

| ORDERING INFORMATION | | | | | | | | | |
|----------------------|-----------------|-------------|----------|-----------|----------|--|--|--|--|
| BASE PART NUMBER | LEADFREE SUFFIX | TAPE SUFFIX | QTY/REEL | REEL SIZE | TUBE QTY | | | | |
| SLVU2.8-8 | -LF | -T7 | 1,000 | 7" | 98 | | | | |
| SLVU2.8-8 | -LF | -T13 | 2,500 | 13" | 98 | | | | |

COMPANY INFORMATION

COMPANY PROFILE

ProTek Devices, based in Tempe, Arizona USA, is a manufacturer of Transient Voltage Suppression (TVS) products designed specifically for the protection of electronic systems from the effects of lightning, Electrostatic Discharge (ESD), Nuclear Electromagnetic Pulse (NEMP), inductive switching and EMI/RFI. With over 25 years of engineering and manufacturing experience, ProTek designs TVS devices that provide application specific protection solutions for all electronic equipment/systems.

ProTek Devices Analog Products Division, also manufactures analog interface, control, RF and power management products.

CONTACT US

Corporate Headquarters

2929 South Fair Lane Tempe, Arizona 85282 USA

By Telephone

General: 602-431-8101 Sales: 602-414-5109

Customer Service: 602-414-5114

By Fax

General: 602-431-2288

By E-mail:

Sales: sales@protekdevices.com

Customer Service: service@protekdevices.com
Technical Support: support@protekdevices.com

Web

www.protekdevices.com www.protekanalog.com

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