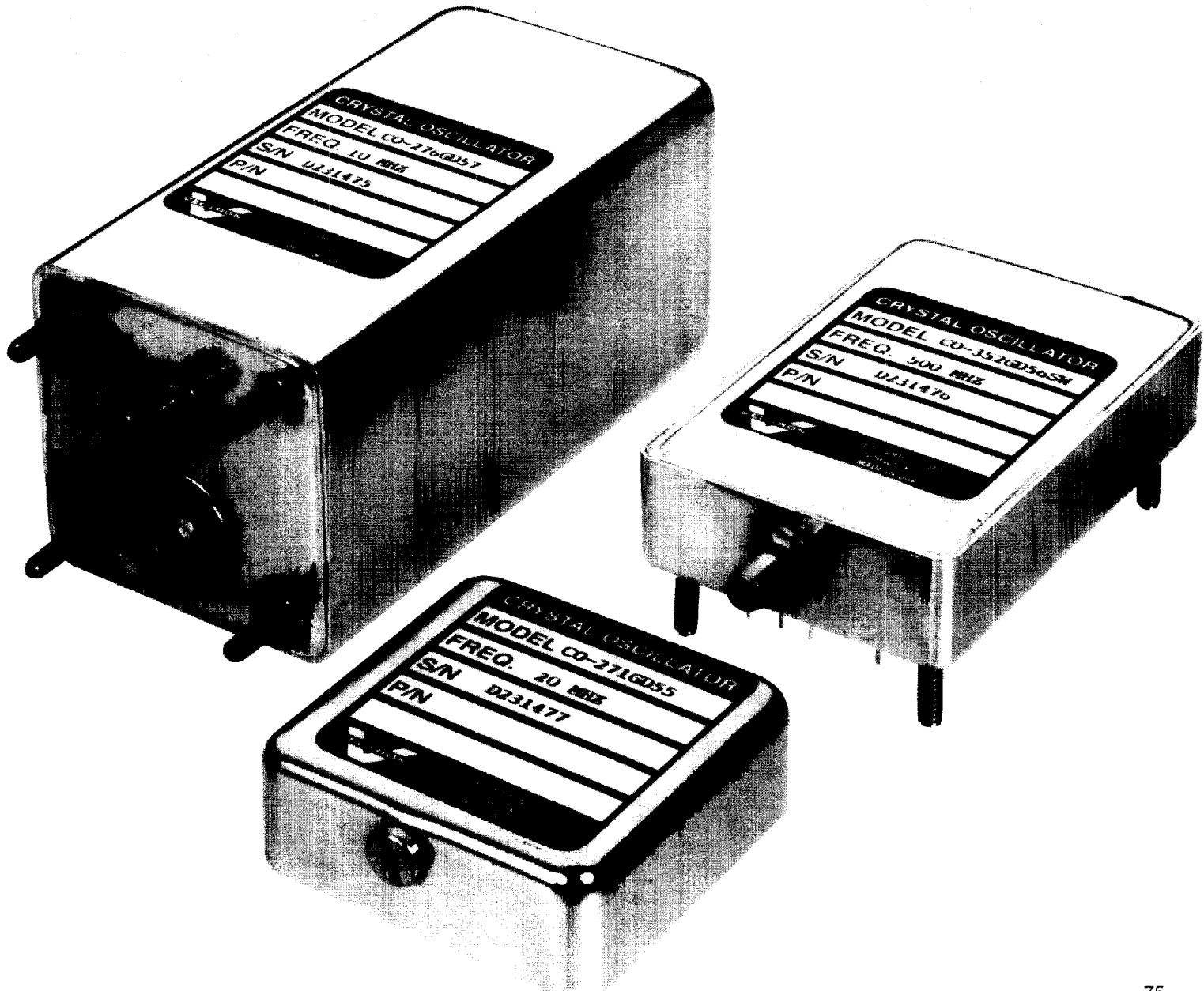


Linear **VCXOs** Voltage Controlled Crystal Oscillators

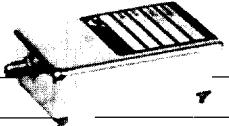
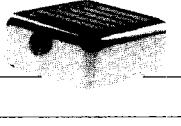
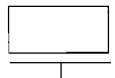
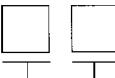
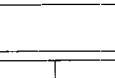
(See pages 71-74 for narrow deviation phase locking models and pages 79-82 for very wide deviation VCOs)

FEATURES

- Center Frequency to 600 MHz
- Wide Frequency Deviation (to ± 2500 PPM)
- High Degree of Linearity (to $\pm 1\%$)
- Temperature Compensated Models (TC/VCXOs)
- Oven Controlled Models (OC/VCXOs)



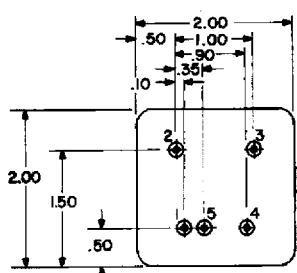
VCXOs, TC/VCXOs...to 600 MHz

	BASIC VCXOs		TEMPERATURE COMPENSATED (TC/VCXOs)				
CENTER FREQUENCY	CO-271	CO-275	CO-351	CO-352			
MODULATION CHARACTERISTICS	± .001% to ± .25%. Model numbers for ± .01%, ± .03%, ± .1%, and ± .25% are shown in the stability section below (for deviation exceeding ± .25%, consider a Vectron non-crystal VCO or custom VCXO).						
Deviation							
Control Voltage		± 5 volts (higher or lower sensitivity, or unipolar control, available)					
Linearity			"N": ± 5% (smooth monotonic) "A": ± 3% characteristic with "B": ± 1% no discontinuities				
Modulation Input Z			>50 kΩ				
Transfer Function	Negative (increasing control voltage decreases frequency)						
Modulation Rate	0 to 10 kHz, demodulated signal is flat to ± 1 dB. (Modulation input network can be broadened to provide a 100 kHz 3 dB response, but as a result of the crystal characteristic, the demodulated signal may exhibit amplitude variation of 5 to 15 dB above 20 kHz).						
STABILITY (at fo)	(Temperature Range B) 0°C to +50°C	(Temperature Range D) -20°C to +70°C	(Temperature Range F) -55°C to +85°C	(Temperature Range B) 0°C to +50°C	(Temperature Range D) -20°C to +70°C	(Temperature Range F) -55°C to +85°C	
± .01% deviation (G)	Code GB35 GB15	Stability ± .003% ± .001%	Code GD55 GD35	Stability ± .005% ± .003%	Code GF55	Stability ± .005%	
± .03% deviation (H)	HB55	± .005% ± .002%	HD14	± .01% ± .005%	HF14	± .01%	
± .1% deviation (L)	LB14	± .01% ± .003%	LD24	± .02% ± .01%	LF34	± .03%	
± .25% deviation (K)	KB34	± .03% ± .01%	KD54	± .05% ± .03%	KF13	± .1%	
	KB14			KB14	± .01% ± .003%	KD24	± .02% ± .01%
				KB35		KD14	± .01% ± .02%
OUTPUT / SUPPLY	Standard Level		>1 Vrms into 1000Ω	>0.5 Vrms into 50 Ω (+7 dBm)	>1 Vrms into 1000Ω	>0.5 Vrms into 50Ω (+7 dBm)	
Standard Supply	15 Vdc ± 5%		15 Vdc ± 5%	15 Vdc ± 5%	15 Vdc ± 5%	15 Vdc ± 5%	
Optional	OUTPUT LEVEL Option "R": +13 dBm into 50Ω Other sine options: 0 dBm to +13 dBm/50Ω Options "J": TTL Other logic options: TTL ECL CMOS HCMOS		SUPPLY ± 5%	Available Frequency Range 15 Vdc 12-28 Vdc 15 Vdc and 5 Vdc 12-28 Vdc and 5 Vdc 12-28 Vdc and -5.2 Vdc 12-15 Vdc 12-28 Vdc and 5 Vdc			
Harmonics and Sub-harmonics (Sinewave)	>20 dB below output. If internal multiplier is used (generally applies to CO-275 and CO-352 Series), subharmonics are also -20 dBc. Improved harmonic/ sub-harmonic attenuation available.				* 3.5 MHz to 500 MHz (600 MHz for CO-275H) * 3.5 MHz to 500 MHz (600 MHz for CO-275H) * 3.5 MHz to 100 MHz * 3.5 MHz to 100 MHz 20 MHz to 200 MHz On special order On special order		
Harmonics and Sub-harmonics (Sinewave)	>20 dB below output. If internal multiplier is used (generally applies to CO-275 and CO-352 Series), subharmonics are also -20 dBc. Improved harmonic/ sub-harmonic attenuation available.				* Lower frequencies on special order		
Current	<30 mA		30-70 mA depending upon frequency	<30 mA	30-70 mA depending upon frequency		
FREQUENCY ADJUST	Screwdriver adjustment permits setting center frequency; range is approximately 10% of deviation, but no less than ± .001%						
MECHANICAL	Size (See drawings on page 78)	2" x 2" x 3/4" (51 x 51 x 19 mm) 2" x 2 1/4" x 3/4" for SW option	2" x 3" x 3/4" (51 x 77 x 19 mm) (reduced size available)	2" x 2" x 3/4" (51 x 51 x 19 mm) 2" x 2 1/4" x 3/4" for SW option	2" x 3" x 3/4" (51 x 77 x 19 mm)		
Base—standard		pins for pcb mount	pins for pcb mount	pins for pcb mount	pins for pcb mount and studs		
—optional	"W": SMA, pins, and mounting studs on base "SW": SMA on side, pins on base (studs optional) (SMC connector available)	"W": SMA, pins, and mounting studs on base "SW": SMA on side, pins and studs on base (SMC connector available)	"W": SMA, pins, and mounting studs on base "SW": SMA on side, pins on base (studs optional) (SMC connector available)	"W": SMA, pins, and mounting studs on base "SW": SMA on side, pins and studs on base (SMC connector available)	(if output connector option recommended above 200 MHz) "SW": SMA on side, pins and studs on base (SMC connector available)		
HOW TO SPECIFY	 Basic Series (e.g. CO-271) _____  Deviation/Stability Code (e.g. GB15) _____  Linearity (N, A or B) _____				at	 Frequency _____	
					* Any lettered option (R, J, W, SW, U, X)		

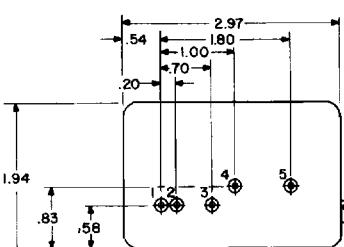
	OVEN CONTROLLED (OC/VCXOs)																	
	CO-272	CO-276																
CENTER FREQUENCY	to 23 MHz		23.1-500 MHz															
MODULATION CHARACTERISTICS	<p>Deviation $\pm .001\%$ to $\pm .25\%$. Model numbers for $\pm .01\%$, $\pm .03\%$, $\pm .1\%$, and $\pm .25\%$ are shown in the stability section below (for deviation exceeding $\pm .25\%$, consider a Vectron non-crystal VCO or custom VCXO).</p>																	
Control Voltage	± 5 volts (higher or lower sensitivity, or unipolar control, available)																	
Linearity	<p>"N": $\pm 5\%$ (smooth monotonic) "A": $\pm 3\%$ characteristic with "B": $\pm 1\%$ no discontinuities</p>																	
Modulation Input Z	$>50 \text{ k}\Omega$																	
Transfer Function	Negative (increasing control voltage decreases frequency)																	
Modulation Rate	0 to 10 kHz, demodulated signal is flat to ± 1 dB (Modulation input network can be broadened to provide a 100 kHz 3 dB response, but as a result of the crystal characteristic, the demodulated signal may exhibit amplitude variation of 5 to 15 dB above 20 kHz).																	
STABILITY (at f_0)	(Temperature Range B) 0°C to $+50^\circ\text{C}$ <table border="1"><tr><th>Code</th><th>Stability</th></tr><tr><td>GB37</td><td>$\pm .00003\%$</td></tr></table>	Code	Stability	GB37	$\pm .00003\%$	(Temperature Range D) -20°C to $+70^\circ\text{C}$ <table border="1"><tr><th>Code</th><th>Stability</th></tr><tr><td>GD57</td><td>$\pm .00005\%$</td></tr></table>	Code	Stability	GD57	$\pm .00005\%$	(Temperature Range F) -55°C to $+85^\circ\text{C}$ <table border="1"><tr><th>Code</th><th>Stability</th></tr><tr><td>GF16</td><td>$\pm .0001\%$</td></tr></table>	Code	Stability	GF16	$\pm .0001\%$			
Code	Stability																	
GB37	$\pm .00003\%$																	
Code	Stability																	
GD57	$\pm .00005\%$																	
Code	Stability																	
GF16	$\pm .0001\%$																	
$\pm .01\%$ deviation (G)																		
$\pm .03\%$ deviation (H)	HB16	$\pm .0001\%$	HF56	$\pm .0005\%$														
$\pm .1\%$ deviation (L)	LB36	$\pm .0003\%$	LF15	$\pm .001\%$														
$\pm .25\%$ deviation (K)	KB15	$\pm .001\%$	KF55	$\pm .005\%$														
OUTPUT / SUPPLY	<p>Standard Level >1 Vrms into 1000Ω</p> <p>Standard Supply $24 \text{ Vdc} \pm 5\%$</p>																	
Optional	<table border="1"> <tr> <th>OUTPUT LEVEL</th> <th>SUPPLY $\pm 5\%$</th> <th>Available Frequency Range</th> </tr> <tr> <td>Option "R": +13 dBm into 50Ω</td> <td>24 Vdc</td> <td>*3.5 MHz to 500 MHz</td> </tr> <tr> <td>Other sine options: 0 dBm to +13 dBm/50Ω</td> <td>12-28 Vdc</td> <td>*3.5 MHz to 500 MHz</td> </tr> <tr> <td>Options "J": TTL</td> <td>24 Vdc and 5 Vdc</td> <td>*700 kHz to 100 MHz</td> </tr> <tr> <td>Other logic options: TTL ECL CMOS HCMOS</td> <td>12-28 Vdc and -5.2 Vdc 12-15 Vdc 12-28 Vdc and 5 Vdc</td> <td>*700 kHz to 100 MHz 20 MHz to 200 MHz On special order On special order</td> </tr> </table> <p>*Lower frequencies on special order</p>			OUTPUT LEVEL	SUPPLY $\pm 5\%$	Available Frequency Range	Option "R": +13 dBm into 50Ω	24 Vdc	*3.5 MHz to 500 MHz	Other sine options: 0 dBm to +13 dBm/ 50Ω	12-28 Vdc	*3.5 MHz to 500 MHz	Options "J": TTL	24 Vdc and 5 Vdc	*700 kHz to 100 MHz	Other logic options: TTL ECL CMOS HCMOS	12-28 Vdc and -5.2 Vdc 12-15 Vdc 12-28 Vdc and 5 Vdc	*700 kHz to 100 MHz 20 MHz to 200 MHz On special order On special order
OUTPUT LEVEL	SUPPLY $\pm 5\%$	Available Frequency Range																
Option "R": +13 dBm into 50Ω	24 Vdc	*3.5 MHz to 500 MHz																
Other sine options: 0 dBm to +13 dBm/ 50Ω	12-28 Vdc	*3.5 MHz to 500 MHz																
Options "J": TTL	24 Vdc and 5 Vdc	*700 kHz to 100 MHz																
Other logic options: TTL ECL CMOS HCMOS	12-28 Vdc and -5.2 Vdc 12-15 Vdc 12-28 Vdc and 5 Vdc	*700 kHz to 100 MHz 20 MHz to 200 MHz On special order On special order																
Harmonics and Sub-harmonics (Sinewave)	>20 dB below output. If internal multiplier is used (generally applies to CO-276 Series), subharmonics are also -20 dBc. Improved harmonic/ sub-harmonic attenuation available.																	
Current	<6 watts at turn-on (8 watts below -20°C) <3 watts stabilized at 25°C																	
FREQUENCY ADJUST	Screwdriver adjustment permits setting center frequency, range is approximately 10% of deviation, but no less than $\pm .001\%$																	
MECHANICAL	Size	$2'' \times 2'' \times 4''$ (51 x 51 x 102 mm) Contact factory for outline and pin connection drawings	$2'' \times 2'' \times 4''$ (51 x 51 x 102 mm)															
Base—standard	octal plug-in, studs																	
—optional	<p>"W": SMA, solder header, studs "U": SMC, solder header, studs "X": BNC, solder header, studs (contact factory for pin functions)</p>																	
HOW TO SPECIFY	<p>Basic Series (e.g. CO-272) _____</p> <p>Deviation/Stability Code (e.g. GB37) _____</p> <p>Linearity _____</p> <p>at _____</p> <p>Frequency _____</p> <p>*Any lettered option (R, J, W, U, X)</p>																	
	<p>*Leave blank if not desired</p>																	

Linear VCXOs

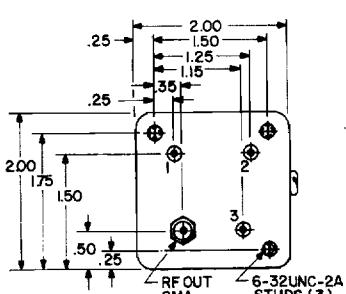
OUTLINE/INSTALLATION DRAWINGS



Pin	Function
1	RF Output
2	Supply (+)
3	OC VCXO
4	Return, Case
VCXO Input	
*5	RF Return
* -5V for TTL Output	

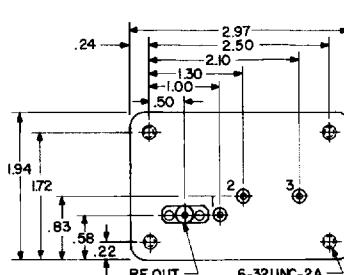


CO-275H	
(Sine >100 MHz, Logic >23 MHz)	
Pin	Function
*1	RF Return, Case
2	RF Output
3	Supply (+)
4	0 Volts, Case
5	VCXO Input
* -5 V for TTL Output	
-5.2 V for ECL Output	

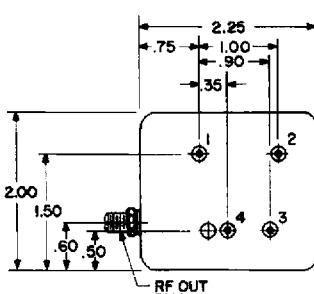


Pin	Function
1	Supply (+)
2	0 Volts, Case
3	VCXO input

Sine to 23 MHz



Pin	Function
1	Supply (+)
2	0 Volts, Case
3	VCO Input



**CO-271SW
CO-351SW**

(Sine or Logic to 23 MHz)

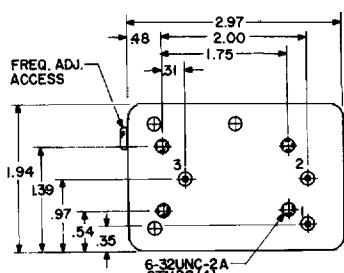
Pin	Function
1	Supply (+)
2	0 Volts, Case
3	VCXO Input
4	Case

+ 5V for TTL Output

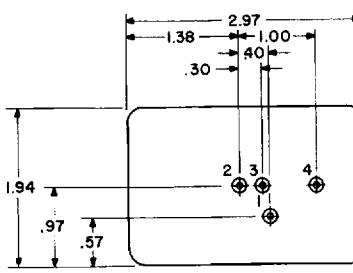
.030

.75

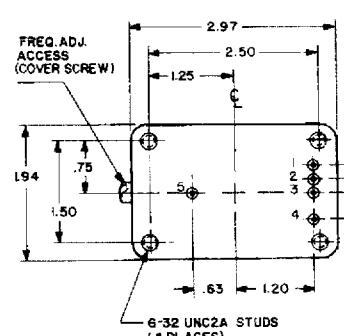
FREQ. ADJ.



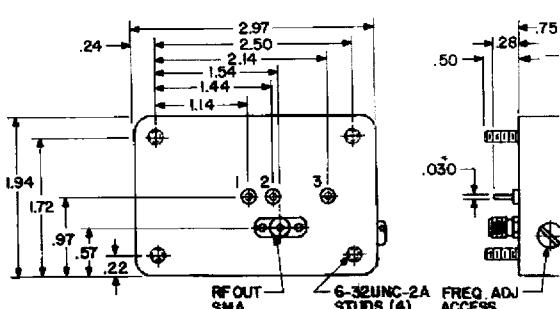
Pin	Function
1	Supply (+)
2	0 Volts, Case
3	VCO Input



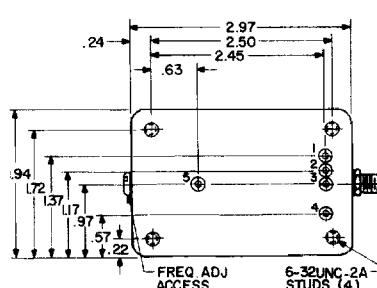
Pin	Function
1	RF Output
2	Supply (+)
3	0 Volts, Case
4	VCXO Input



CO-352	
(Sine or Logic >20 MHz)	
Pin	Function
1	Supply (-)
*2	Case
3	RF Output
4	0 Volts, Case
*5	VCXO Input
+ 5 V for TTL Output	
- 5.2 V for ECL Output	



CO-275W
(Sine, 23.1-100 MHz)



CO-352SW	
(Sine or Logic >20 MHz)	
Pin	Function
1	Supply (+)
*2	Case
3	N/C
4	0 Volts, Case
5	VXCO Input

* + 5 V for TTL Output
* 2 V for ECL Output