

LOW JITTER 3.3VDC PECL FULL-SIZE DIP
CRYSTAL CLOCK OSCILLATORS
ACPEL



FEATURES:

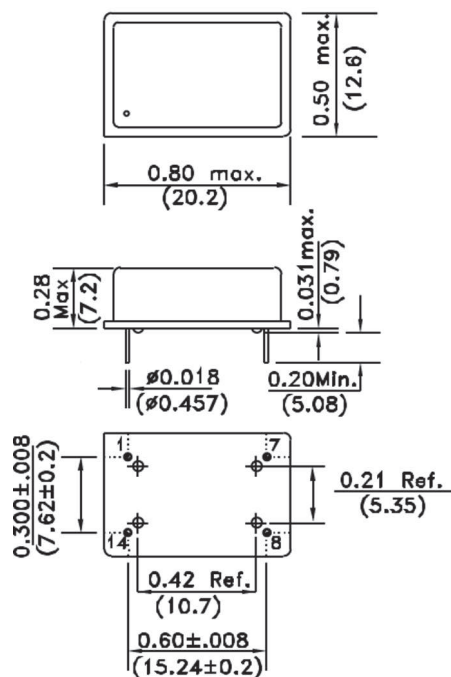
- Low jitter PECL logic, 3.3VDC supply.
- Tight overall frequency stability.
- Fast Rise and Fall Times.
- Complementary output.
- Wide frequency range.

APPLICATIONS:

- Gigabit Ethernet.
- Data telecommunications and networking.
- xDSL, Stratum IV.
- SONET/SDH

STANDARD SPECIFICATIONS

| PARAMETERS | SPECIFICATIONS |
|---|--|
| Frequency Range (F ₀) | 10.00 MHz - 200 MHz |
| Operating Temperature (T _{OPR}) | 0°C to + 70°C (see options) |
| Storage Temperature (T _{STO}) | -40°C to +85°C |
| Frequency Stability (DF/ F ₀) | ± 100 ppm max. (See Options) |
| Supply Voltage (V _{dd}) | +3.3 Vdc ± 5% |
| Supply Current (I _{dd}) | 120mA max. |
| Duty cycle or Symmetry | 40/60% max. measured at 3.67VDC |
| Rise and Fall times (Tr/Tf) | 5ns max. measured at 20% to 80% level |
| Output | PECL square wave |
| Output and E/D options (pin 1) | No connection or Complementary output or E/D |
| Output load | 270Ω terminated to GND |
| Output voltage | VOH = 2.275V min. VOL = 1.68V max. |
| Jitter | 10ps typical, 25ps RMS max. |
| Enable/Disable option (pin 1) | VIL= 1.68V max., VIH = 2.2V min. (Output is in "0" state when disabled) |
| Start-up time | 10ms max. |



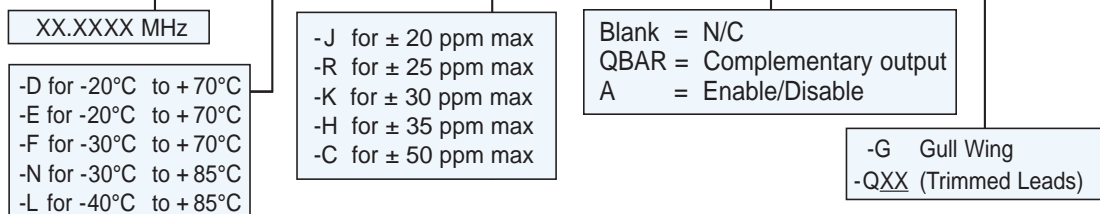
Dimensions: Inches (mm)

| PIN NUMBER | FUNCTION |
|------------|------------------------|
| 1 | NC or \bar{Q} or E/D |
| 7 | GND / Case |
| 8 | Output |
| 14 | V _{dd} |

(*) Output must be terminated into 270Ω into GND
Environmental and mechanical specifications, Group 1.

ORDERING OPTIONS

ACPEL - Frequency - Temperature - Overall Frequency Stability - Pin 1 option - Value Added



NOTE: Left blank if standard • All specifications and markings subject to change without notice

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