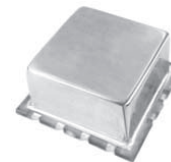
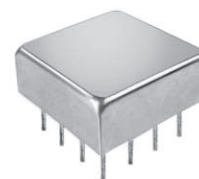


# PHASE COMPARATORS



## SURFACE-MOUNT MODELS

FREQUENCY RANGE (MHz)	IMPEDANCE (Ohm)		LO POWER LEVEL (dBm)	RF POWER LEVEL (dBm)	PHASE ERROR MAX	PHASE RANGE NOM.	DC OUTPUT (mV p-p) TYP	DC OFFSET (mV) TYP/MAX	PACKAGE	PIN-OUT (See Below)	MODEL
	LO/RF	OUTPUT									
10-20	50	500	+10	0	+/-2°	360°	300	0.3/1.0	124S	1	PCS-901
20-40	50	500	+10	0	+/-2°	360°	300	0.4/1.0	124S	1	PCS-902
55-90	50	500	+10	0	+/-2°	360°	300	0.5/2.0	124S	1	PCS-904
80-160	50	500	+10	0	+/-3°	360°	300	1.0/2.0	124S	1	PCS-908
100-200	50	500	+10	0	+/-3°	360°	300	1.0/3.0	124S	1	PCS-910
160-320	50	500	+10	0	+/-4°	360°	300	1.0/3.0	124S	1	PCS-916
225-400	50	500	+10	0	+/-4°	360°	300	1.0/4.0	124S	1	PCS-922
300-500	50	500	+10	0	+/-4°	360°	300	2.0/5.0	124S	1	PCS-930
400-600	50	500	+10	0	+/-5°	360°	300	2.0/6.0	124S	1	PCS-940
700-900	50	500	+10	0	+/-5°	360°	300	2.0/8.0	124S	1	PCS-970
800-1000	50	500	+10	0	+/-5°	360°	300	3.0/10.0	124S	1	PCS-980



## THROUGH HOLE MODELS

FREQUENCY RANGE (MHz)	IMPEDANCE (Ohm)		LO POWER LEVEL (dBm)	RF POWER LEVEL (dBm)	PHASE ERROR MAX	PHASE RANGE NOM.	DC OUTPUT (mV p-p) TYP	DC OFFSET (mV) TYP/MAX	PACKAGE	PIN-OUT (See Below)	MODEL
	LO/RF	OUTPUT									
10-20	50	500	+10	0	+/-2°	360°	300	0.3/1.0	136	2	PCP-901
20-40	50	500	+10	0	+/-2°	360°	300	0.4/1.0	136	2	PCP-902
55-90	50	500	+10	0	+/-2°	360°	300	0.5/2.0	136	2	PCP-904
80-160	50	500	+10	0	+/-3°	360°	300	1.0/2.0	136	2	PCP-908
100-200	50	500	+10	0	+/-3°	360°	300	1.0/3.0	136	2	PCP-910
160-320	50	500	+10	0	+/-4°	360°	300	1.0/3.0	136	2	PCP-916
225-400	50	500	+10	0	+/-4°	360°	300	1.0/4.0	136	2	PCP-922
300-500	50	500	+10	0	+/-4°	360°	300	2.0/5.0	136	2	PCP-930
400-600	50	500	+10	0	+/-5°	360°	300	2.0/6.0	136	2	PCP-940
700-900	50	500	+10	0	+/-5°	360°	300	2.0/8.0	136	2	PCP-970
800-1000	50	500	+10	0	+/-5°	360°	300	3.0/10.0	136	2	PCP-980

### NOTE:

- Output polarity is negative on all models.
- Maximum RF input power, 100 mW. Peak IF current, 40mA.

PIN-OUT TABLE

	LO	RF	SINE	COSINE	CASE GND
#1	16	1	13	4	All Other
#2	1	7	8	3	All Other

For pin location and package outline drawings, see back pages.

# PHASE COMPARATORS



## FLAT PACK MODELS

FREQUENCY RANGE (MHz)	IMPEDANCE (Ohm)		LO POWER LEVEL (dBm)	RF POWER LEVEL (dBm)	PHASE ERROR MAX	PHASE RANGE NOM.	DC OUTPUT (mV p-p) TYP	DC OFFSET (mV) TYP/MAX	PACKAGE	PIN-OUT (See Below)	MODEL
	LO/RF	OUTPUT									
10-20	50	500	+10	0	+/-2°	360°	300	0.3/1.0	115	1	PCF-101
20-40	50	500	+10	0	+/-2°	360°	300	0.4/1.0	115	1	PCF-102
55-90	50	500	+10	0	+/-2°	360°	300	0.5/2.0	115	1	PCF-104
80-160	50	500	+10	0	+/-3°	360°	300	1.0/2.0	115	1	PCF-108
100-200	50	500	+10	0	+/-3°	360°	300	1.0/3.0	115	1	PCF-110
160-320	50	500	+10	0	+/-4°	360°	300	1.0/3.0	115	1	PCF-116
225-400	50	500	+10	0	+/-4°	360°	300	1.0/4.0	115	1	PCF-122
300-500	50	500	+10	0	+/-4°	360°	300	2.0/5.0	115	1	PCF-130
400-600	50	500	+10	0	+/-5°	360°	300	2.0/6.0	115	1	PCF-140
700-900	50	500	+10	0	+/-5°	360°	300	2.0/8.0	115	1	PCF-170
800-1000	50	500	+10	0	+/-5°	360°	300	3.0/10.0	115	1	PCF-180



## COAXIAL (SMA-F) CONNECTOR MODELS

FREQUENCY RANGE (MHz)	IMPEDANCE (Ohm)		LO POWER LEVEL (dBm)	RF POWER LEVEL (dBm)	PHASE ERROR MAX	PHASE RANGE NOM.	DC OUTPUT (mV p-p) TYP	DC OFFSET (mV) TYP/MAX	PACKAGE	PIN-OUT (See Below)	MODEL
	LO/RF	OUTPUT									
10-20	50	500	+10	0	+/-2°	360°	300	0.3/1.0	113	2	PCK-701S
20-40	50	500	+10	0	+/-2°	360°	300	0.4/1.0	113	2	PCK-702S
55-90	50	500	+10	0	+/-2°	360°	300	0.5/2.0	113	2	PCK-704S
80-160	50	500	+10	0	+/-3°	360°	300	1.0/3.0	113	2	PCK-708S
100-200	50	500	+10	0	+/-3°	360°	300	1.0/3.0	113	2	PCK-710S
160-320	50	500	+10	0	+/-4°	360°	300	1.0/4.0	113	2	PCK-716S
225-400	50	500	+10	0	+/-4°	360°	300	2.0/5.0	113	2	PCK-722S
300-500	50	500	+10	0	+/-4°	360°	300	2.0/6.0	113	2	PCK-730S
400-600	50	500	+10	0	+/-5°	360°	300	2.0/6.0	113	2	PCK-740S
700-900	50	500	+10	0	+/-5°	360°	300	2.0/8.0	113	2	PCK-770S
800-1000	50	500	+10	0	+/-5°	360°	300	3.0/10.0	113	2	PCK-780S

**NOTE:**

1. Output polarity is negative on all models.
2. Maximum RF input power, 100 mW. Peak IF current, 40mA.

PIN-OUT TABLE

	LO	RF	SINE	COSINE	CASE GND
# 1	14	1	10	3	All Other
# 2	3	1	2	4	All Other

For pin location and package outline drawings, see back pages.