CFPP-600, -601 PROGRAMMABLE OSCILLATORS

ISSUE 4; 1 NOVEMBER 2008 - RoHS 2002/95/EC

Description

- Surface mount oscillator with complimentary PECL/LVPECL outputs

Package Outline

- 12.4 x 9.6mm

Frequency Range

- 1 to 133MHz

Output Compatibility & Load

- PECL or LVPECL
- Output Load 50Ω terminated to Vs –2.0V

Standard Frequency Stabilities

- ±50ppm, ±100ppm

Operating Temperature Ranges

- 0 to 70°C CFPP-600, -601
- –40 to 85°C CFPP-600I, -601I

Storage Temperature Range

- –55 to 125°C

Tri-state Operation (CMOS Levels)

- Logic '1' to pad 1 enables oscillator output
- Logic '0' to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state
- No connection to pad 1 enables oscillator output

Supply Voltages

- 5.0V CFPP-600
- 3.3V CFPP-601

Ageing

- ±5ppm typical in 1st year @ 25°C, Vs = 3.3V

Marking Includes

- Frequency + Model Number

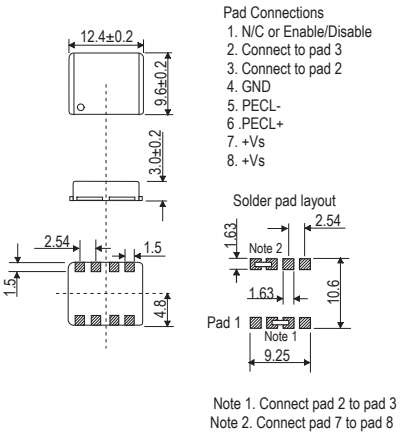
Packaging

- Bulk or Tape & Reel

Minimum Order Information Required

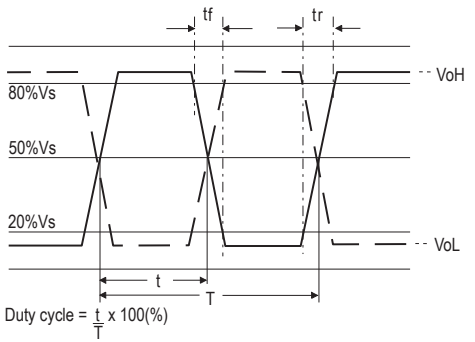
- Frequency + Model Number + Operating Temperature Code (if applicable) + Frequency Stability

Outline (mm)

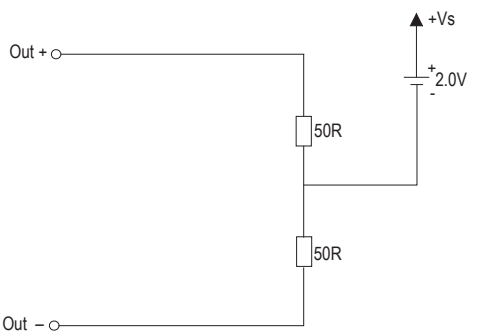


Note 1. Connect pad 2 to pad 3
Note 2. Connect pad 7 to pad 8

Output Waveform



Test Circuit



PROGRAMMABLE OSCILLATORS

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Electrical Specifications - maximum limiting values

Frequency Range	Frequency Stability	Supply Voltage	Supply Current	Rise Time (tr) (20-80%)	Fall Time (tf) (80-20%)	Model Number
1.0 to 133.0MHz	±50ppm, ±100ppm	+5.0V ±5%	100mA	1.5ns	1.5ns	CFPP-600, CFPP-600I
		+3.3V ±5%				CFPP-601 CFPP-601I

Ordering Example 50.0MHz CFPP-601 I C

Frequency

Model No.

Operating Temperature Code: I = –40 to 85°C; not applicable for 0 to 70°C

Frequency Stability: B = ±50ppm, C = ±100ppm

Please note that the rise and fall times listed are the maximum values we specify to cover various frequency breaks. In practice the actual values are generally lower depending upon the spot frequency chosen. For typical values please contact our sales office.

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