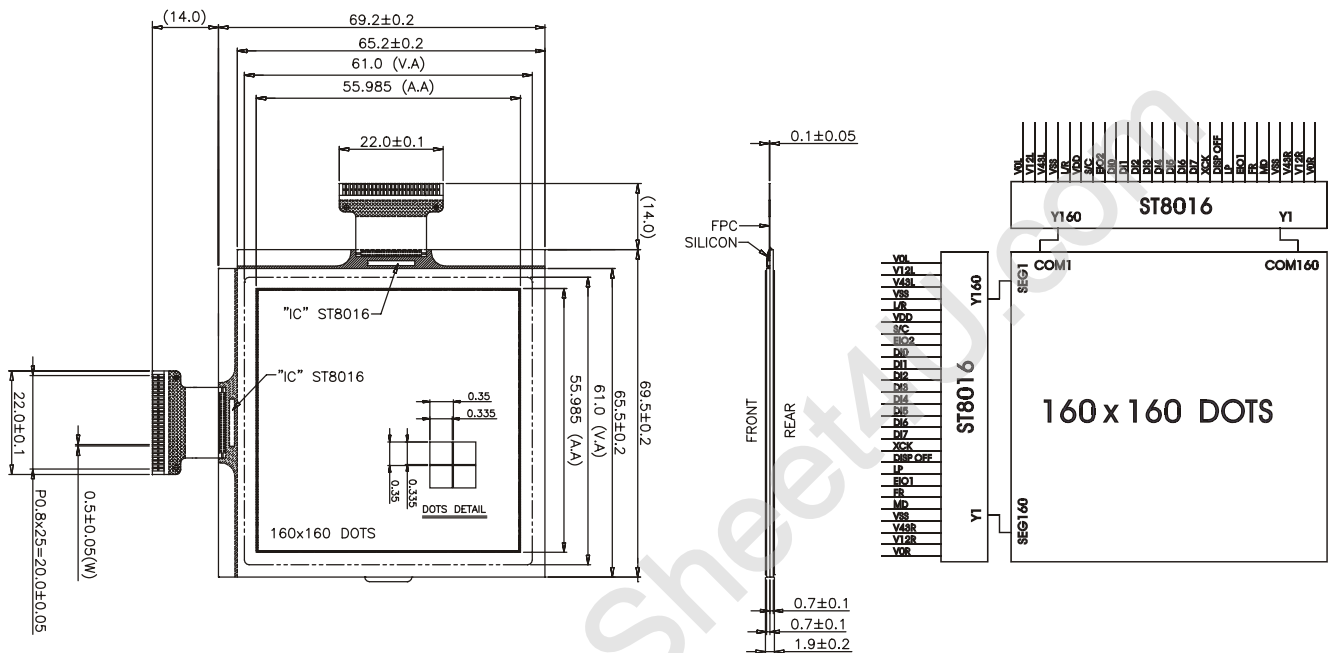


OUTLINE DIMENSION & BLOCK DIAGRAM



MECHANICAL SPECIFICATION (The tolerance unless classified $\pm 0.1\text{mm}$)			
Overall Size	83.20 x 83.50 mm	Module	H2 / H1
View Area	61.0 x 61.0 mm	W/O B/L	- / 1.90
Dot Size	0.335 x 0.335 mm	EL B/L	- / -
Dot Pitch	0.35 x 0.35 mm	LED B/L	- / -

PIN ASSIGNMENT		
Pin No.	Symbol	Function
1~3	V0R, V12R, V43R	Bias power supply for LCD drive voltage
4	VSS	Ground
5	MD	Mode selection
6	FR	AC signal input for LCD drive waveform
7	EI01	Input/output for chip selection
8	LP	Latch pulse input for display data
9	DISPOFF	Control input for output of non-select
10	XCK	Clock input for taking display data
11~18	DI7~DI0	Display data input
19	EIO2	Input/output selection
20	S/C	Segment/common mode selection
21	VDD	Logic power supply
22	L/R	Input selection to reading direction
23	VSS	Ground
24~26	V43L, V12L, V0L	Bias power supply for LCD drive voltage

ABSOLUTE MAXIMUM RATING					
Item	Symbol	Condition	Min.	Max.	Units
Supply for logic voltage	$V_{DD}-V_{SS}$	25°C	-0.3	7.0	V
LCD driving supply voltage	$V_{DD}-V_S$	25°C	-0.3	+30.0	V
Input Voltage	V_{IN}	25°C	-0.3	$V_{DD}+0.3$	V

ELECTRICAL CHARACTERISTICS							
Item	Symbol	Condition	Min.	Typical	Max.	Units	
Power supply voltage	$V_{DD}-V_{SS}$	25°C	2.7	3.0	5.5	V	
LCD operation voltage	V_{OP}	T _{OP}	N	W	N	W	V
		-20°C	-	-	-	-	V
		0°C	-	-	-	-	V
		25°C	-	19.0	-	-	V
		50°C	-	-	-	-	V
		70°C	-	-	-	-	V
LCM current consumption (No B/L)	I _{DD}	$V_{DD}=3.0\text{V}$	-	0.1	-	mA	
	I _{EE}	$V_{OP}=19.0\text{V}$	-	4.0	-	mA	
Backlight current consumption	-	-	-	-	--	mA	

REMARK

LCD option: STN, FSTN, FFSTN
 Backlight Option: EL