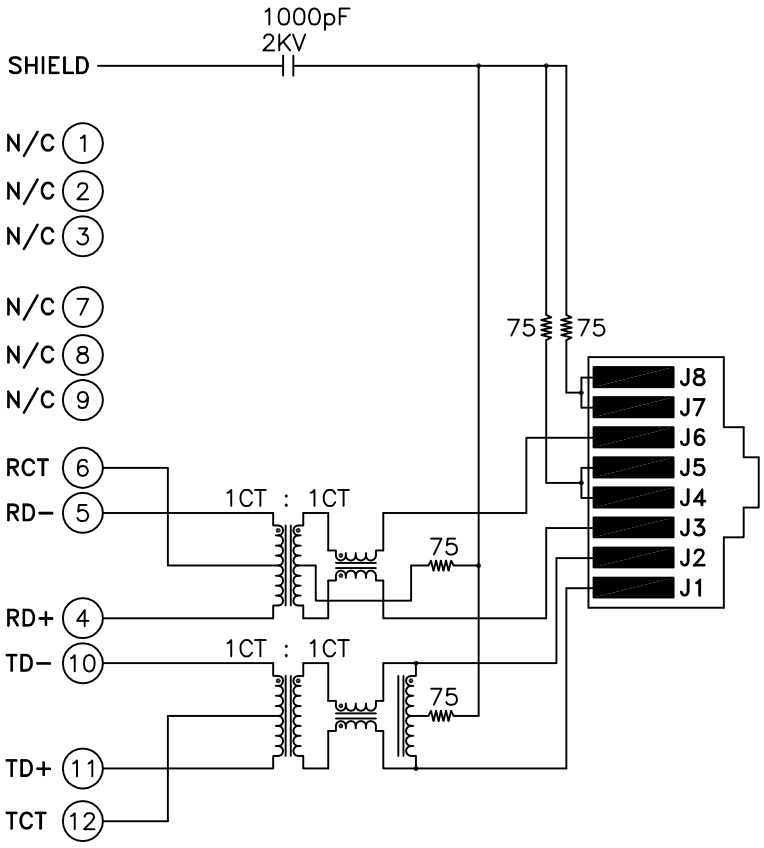

ELECTRICAL SPECIFICATIONS:

1.0	TURNS RATIO: (P5–P6–P4) : (J6–J3) (P10–P12–P11) : (J2–J1)	: 1CT : 1CT ±2% : 1CT : 1CT ±2%
2.0	INDUCTANCE: (P10–P11) ; (P5–P4)	: 350 uH MIN. @ 0.01V, 100KHz, 8 mA DC BIAS
3.0	LEAKAGE INDUCTANCE: P10–P11 (WITH J1 AND J2 SHORT) P5–P4 (WITH J6 AND J3 SHORT)	: 0.3uH MAX. @ 1MHz : 0.3uH MAX. @ 1MHz
4.0	INTERWINDING CAPACITANCE: (P10–P11) : (J1–J2) (P5–P4) : (J6–J3)	: 30pf MAX @ 1MHz : 30pf MAX @ 1MHz
5.0	DC RESISTANCE: (J6–J3) ; (J2–J1)	: 1.2 ohms Max.

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6.0 RETURN LOSS:	1MHz TO 30MHz	: −18dB MIN.
	60MHz TO 80MHz	: −12dB MIN.
7.0 DIELECTRIC WITHSTAND:	(J1,J2) TO (P10,P11) ; (J5,J4) TO (P4−P5)	: 1500 VAC
	(J3,J6) TO (P5,P4) ; (J8,J7) TO (P10, P11)	: 1500 VAC
8.0 INSERTION LOSS:	RS=RL=100 ohms	
	100KHz TO 125MHz	: −1.1 dB TYP
9.0 RISE TIME:	RS=100 OHMS AND RL = 100 OHMS	
	OUTPUT VOLTAGE = 1 V peak	: 3.0 nS MAX
	PULSE WIDTH= 112nS	: 3.0 nS MAX
10.0 CROSS TALK:	1−100 MHz	: −35dB TYP
11.0 COMMON TO COMMON MODE ATTENUATION:	1MHz TO 100MHz	: −35dB TYP

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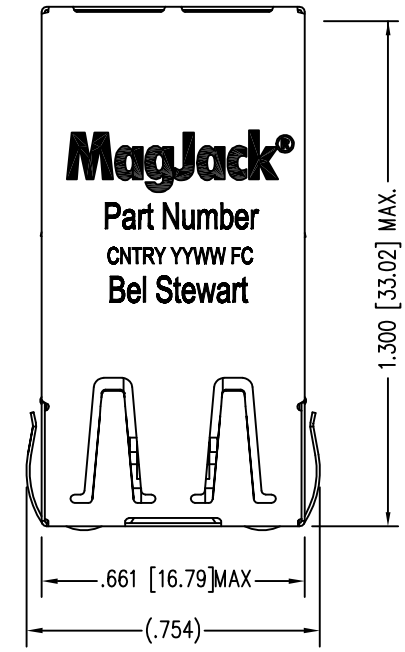
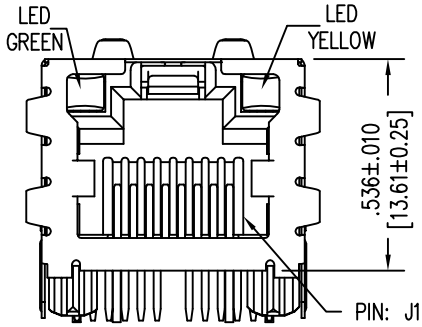
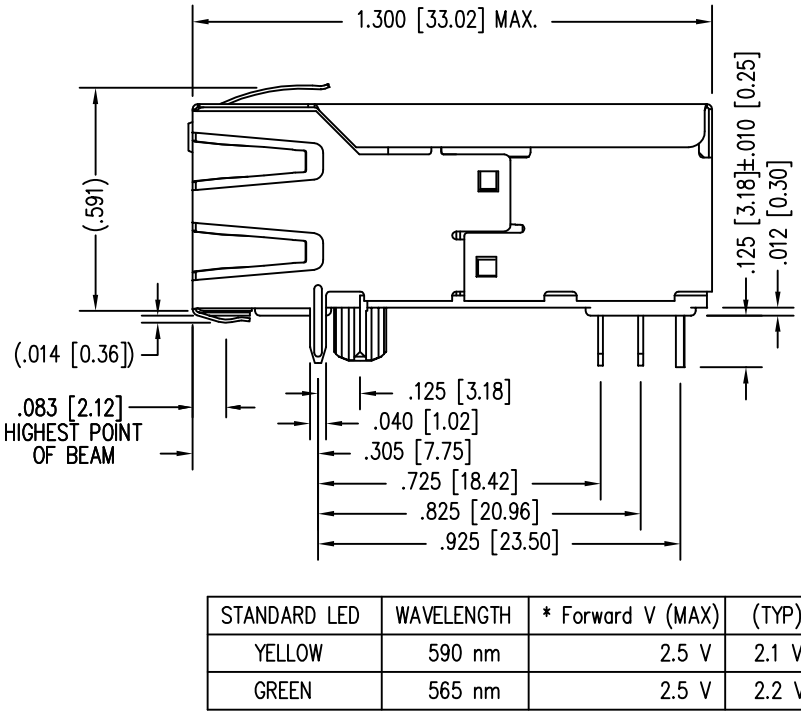
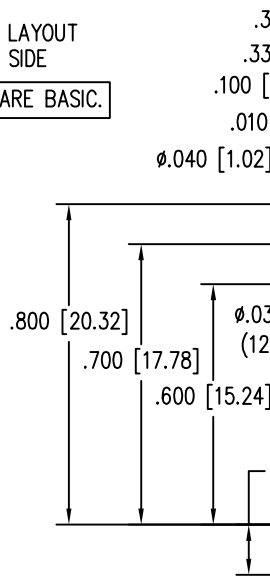






P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC.



ø.062 [1.57] (2) (SHIEL

.068 [

NOTES:

1. CONNECTOR MA
   HOUSING: THE
   CONTACT/SHIEL
   SHIELD PLATIN
   CONTACT PLAT
2. PIN NOT ELECT
   ALL PINS ARE
3. TOLERANCES C
4. THE SUGGESTE
   USER THE ABIL
   CLEARANCES,
5. SOLDER REFLO

STANDARD LED	WAVELENGTH	* Forward V (MAX)	(TYP)
YELLOW	590 nm	2.5 V	2.1 V
GREEN	565 nm	2.5 V	2.2 V

* WITH A FORWARD CURRENT OF 20 mA

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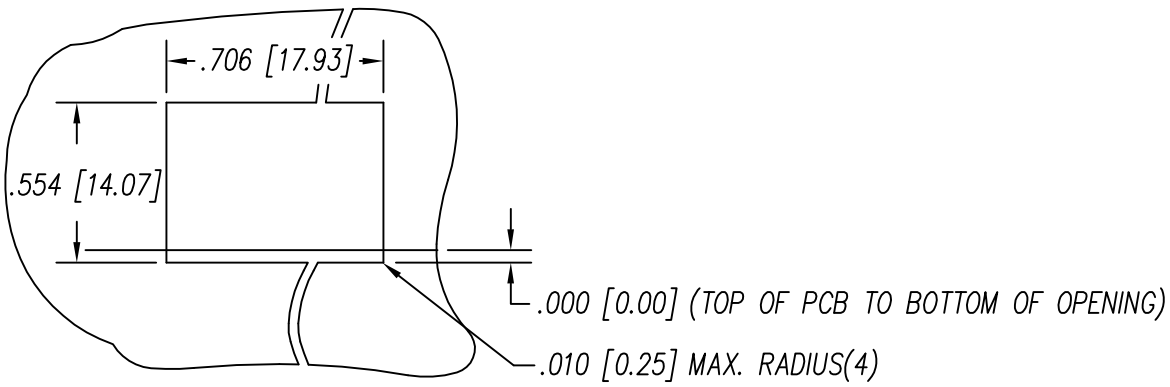
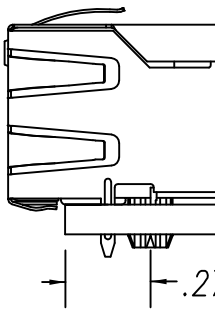


SUGGESTED PANEL OPENING



THE SUGGESTED
TO GIVE THE US
REASONABLE JA
YET MAINTAIN R
CAPABILITY.

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