

UNISONIC TECHNOLOGIES CO., LTD

UTT80N10 Preliminary Power MOSFET

80A, 100V N-CHANNEL POWER MOSFET

■ DESCRIPTION

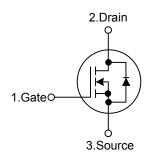
The UTC **UTT80N10** is an N-channel power MOSFET using UTC's advanced technology to provide the customers with perfect $R_{\text{DS(ON)}}$, high switching speed, high current capacity and low gate charge.

The UTC **UTT80N10** is suitable for DC-DC converters, Off-Line UPS, High Voltage Synchronous Rectifier, Primary Switch for 48V and 24V Systems, etc.

■ FEATURES

- * $R_{DS(ON)}$ =18m Ω @ V_{GS} =10V, I_D =80A
- * High Switching Speed
- * High Current Capacity
- * Low Gate Charge(typical 49nC)

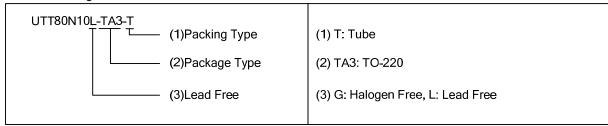
■ SYMBOL

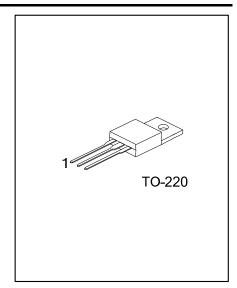


ORDERING INFORMATION

Ordering Number		Doolsons	Pin Assignment			Da alsisası
Lead Free	Halogen Free Package		1	2	3	Packing
UTT80N10L-TA3-T	UTT80N10G-TA3-T	TO-220	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source





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■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{ m DSS}$	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I_{D}	80	Α
	Pulsed (Note 2)	I _{DM}	320	Α
Single Pulsed Avalanche Energy (Note 3)		E _{AS}	416	mJ
Power Dissipation		P_D	211	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%
- 3. L = 0.13mH, I_{AS} = 80A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT °C/W	
Junction to Ambient	$ heta_{JA}$	62		
Junction to Case	θ_{JC}	0.59	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TVD	MAX	LINIT
OFF CHARACTERISTICS		STIVIBUL	TEST CONDITIONS	IVIIIN	ITP	IVIAA	UNIT
	_	D) /	1 050:4 1/ 01/	400			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Drain-Source Breakdown Voltage		BV _{DSS}	$I_D=250\mu A, V_{GS}=0V$ 100				V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =80V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	Forward	- I _{GSS}	V_{GS} =+20V, V_{DS} =0V			+100	nA
	Reverse		V_{GS} =-20V, V_{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	3.5	4.5	5.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =80A		15	18	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}			4152		рF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		485		pF
Reverse Transfer Capacitance		C_{RSS}			220		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G			350		nC
Gate to Source Charge		Q_GS	V_{GS} =10V, V_{DD} =50V, I_{D} =80A		23		nC
Gate to Drain Charge		Q_GD			16		nC
Turn-ON Delay Time		$t_{D(ON)}$			90		ns
Rise Time		t_R	V _{DD} =50V, I _D =80A, V _{GS} =10V,		100		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _{GS} =5.0Ω		450		ns
Fall-Time		t _F			200		ns
SOURCE- DRAIN DIODE RATIN	NGS AND (CHARACTERI	STICS				
Drain-Source Diode Forward Vol	tage	V _{SD}	I _{SD} =80A		0.99	1.25	V
Maximum Body-Diode Continuous Current		I _S				80	Α
Maximum Body-Diode Pulsed Current		I _{SM}				320	Α
Body Diode Reverse Recovery Time		t _{rr}	1 404 11/11 4004/		70	105	ns
Body Diode Reverse Recovery Charge		Q_{RR}	I _S =40A, dI/dt=100A/μs		202	303	nC

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