

RJK60S7DPP-E0

600V -30A - SJ MOS FET High Speed Power Switching

R07DS0643EJ0100 Rev.1.00 Apr 23, 2012

Features

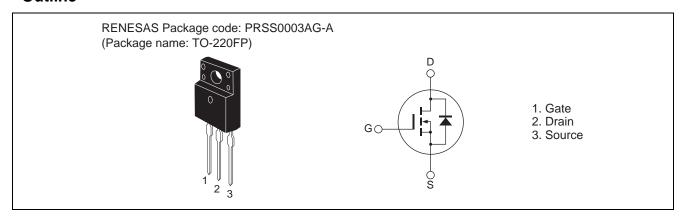
- Superjunction MOSFET
- Low on-resistance

 $R_{DS(on)} = 0.100 \Omega \text{ typ. (at } I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}, Ta = 25^{\circ}\text{C})$

• High speed switching

tf = 15 ns typ. (at I_D = 15 A, V_{GS} = 10 V, R_L = 20 $\Omega,$ Rg = 10 $\Omega,$ Ta = 25°C)

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

| Item | | Symbol | Ratings | Unit |
|---------------------------------------------|------------|-------------------------------|-------------|------|
| Drain to source voltage | | V _{DSS} | 600 | V |
| Gate to source voltage | | V _{GSS} | +30, -20 | V |
| Drain current | Tc = 25°C | I _D Note1 | 30 | Α |
| | Tc = 100°C | I _D Note1 | 19 | А |
| Drain peak current | | I _{D (pulse)} Note1 | 60 | А |
| Body-drain diode reverse drain current | | I _{DR} Note1 | 30 | А |
| Body-drain diode reverse drain peak current | | I _{DR (pulse)} Note1 | 60 | А |
| Avalanche current | | I _{AP} Note3 | 7.5 | А |
| Avalanche energy | | E _{AR} Note3 | 3.05 | mJ |
| Channel dissipation | | Pch Note2 | 34.7 | W |
| Channel to case thermal impedance | | θch-c | 3.6 | °C/W |
| Channel temperature | | Tch | 150 | °C |
| Storage temperature | | Tstg | -55 to +150 | °C |

Notes: 1. Limited by Tch max.

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

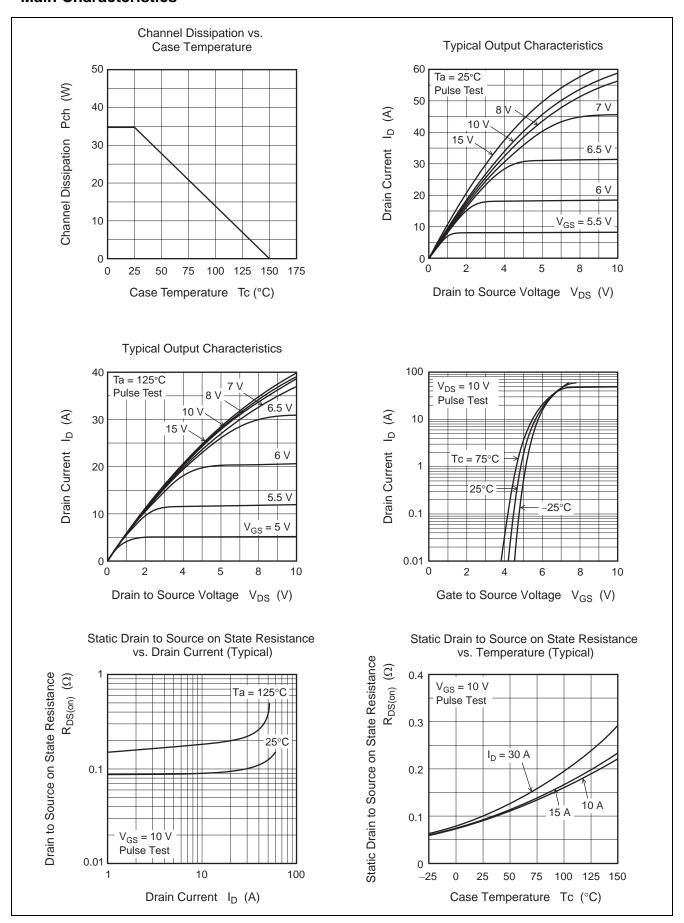
Electrical Characteristics

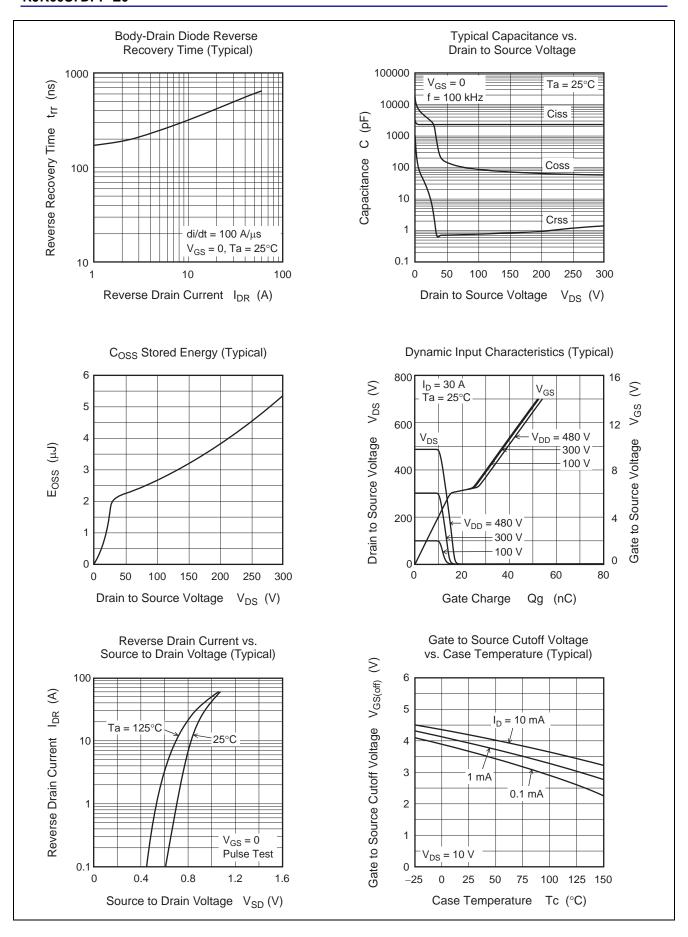
 $(Ta = 25^{\circ}C)$

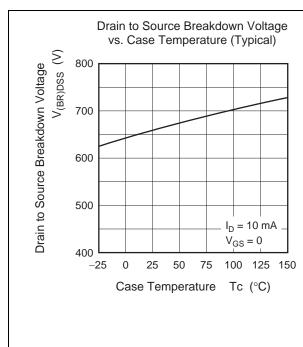
| Item | Symbol | Min | Тур | Max | Unit | Test conditions | |
|----------------------------------------|----------------------|-----|-------|-------|------|----------------------------------------------------------------------|--|
| Drain to source breakdown voltage | $V_{(BR)DSS}$ | 600 | _ | _ | V | $I_D = 10 \text{ mA}, V_{GS} = 0$ | |
| Zero gate voltage drain current | I _{DSS} | _ | _ | 1 | mA | $V_{DS} = 600 \text{ V}, V_{GS} = 0$ | |
| Gate to source leak current | I _{GSS} | _ | _ | ±0.1 | μΑ | $V_{GS} = +30V, -20 V, V_{DS} = 0$ | |
| Gate to source cutoff voltage | V _{GS(off)} | 3 | _ | 5 | V | $V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$ | |
| Static drain to source on state | R _{DS(on)} | _ | 0.100 | 0.125 | Ω | $I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$ | |
| resistance | R _{DS(on)} | _ | 0.25 | _ | Ω | Ta = 150°C | |
| | | | | | | $I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$ | |
| Gate resistance | Rg | _ | 1.7 | _ | Ω | f = 1 MHz | |
| | | | | | | $V_{DS} = 25 \text{ V}, V_{GS} = 0$ | |
| Input capacitance | Ciss | _ | 2300 | _ | pF | $V_{DS} = 25 \text{ V}$ | |
| Output capacitance | Coss | | 3000 | _ | pF | $V_{GS} = 0$ | |
| Reverse transfer capacitance | Crss | _ | 10 | | pF | f = 100 kHz | |
| Turn-on delay time | t _{d(on)} | _ | 27 | _ | ns | $I_D = 15 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 20 \Omega$ Noted | |
| Rise time | t _r | _ | 28 | _ | ns | | |
| Turn-off delay time | t _{d(off)} | _ | 55 | _ | ns | | |
| Fall time | t _f | _ | 9 | _ | ns | $Rg = 10 \Omega^{Note4}$ | |
| Total gate charge | Qg | _ | 39 | _ | nC | V _{DD} = 480 V | |
| Gate to source charge | Qgs | _ | 15 | _ | nC | $V_{GS} = 10 \text{ V}$ $I_D = 30 \text{ A}^{\text{Note4}}$ | |
| Gate to drain charge | Qgd | _ | 11 | _ | nC | | |
| Body-drain diode forward voltage | V_{DF} | _ | 1.0 | 1.6 | V | $I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$ | |
| Body-drain diode reverse recovery time | t _{rr} | | 490 | _ | ns | I _F = 30 A | |
| Body-drain diode reverse recovery | Irr | _ | 26 | _ | Α | $V_{GS} = 0$ | |
| current | | | | | | $di_F/dt = 100 A/\mu s^{Note4}$ | |
| Body-drain diode reverse recovery | Qrr | _ | 7.1 | _ | μС | | |
| charge | | | | | | | |

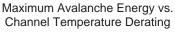
Notes: 4 Pulse test

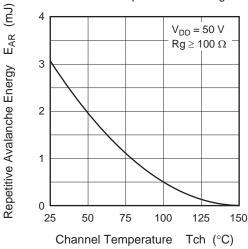
Main Characteristics



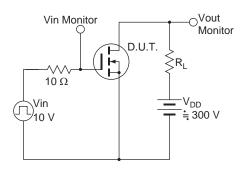




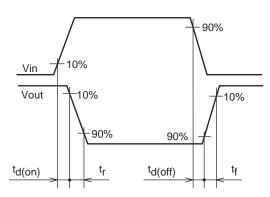




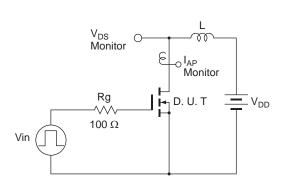
Switching Time Test Circuit



Waveform

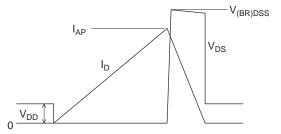


Avalanche Test Circuit

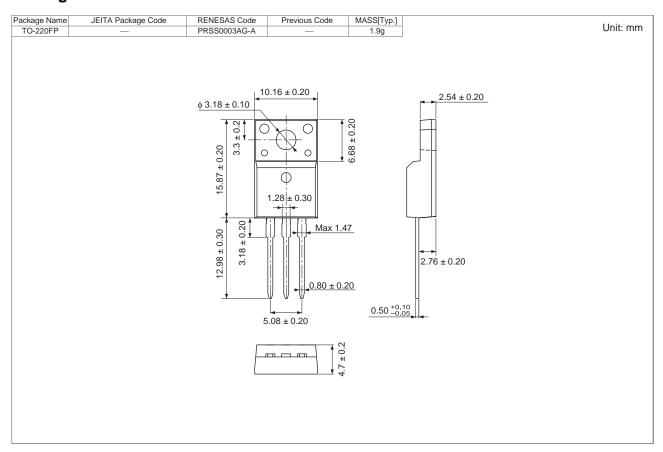


Avalanche Waveform

$$\mathsf{E}_{\mathsf{AR}} = \frac{1}{2} \; \mathsf{L} \bullet \mathsf{I}_{\mathsf{AP}}^2 \bullet \; \frac{\mathsf{V}_{\mathsf{DSS}}}{\mathsf{V}_{\mathsf{DSS}} - \mathsf{V}_{\mathsf{DD}}}$$



Package Dimension



Ordering Information

| Orderable Part Number | Quantity | Shipping Container |
|-----------------------|----------|--------------------|
| RJK60S7DPP-E0#T2 | 1000 pcs | Box (Tube) |

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