# $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus 

## General Description

The MAX3224E/MAX3225E/MAX3226E/MAX3227E/ MAX3244E/MAX3245E are 3V-powered EIA/TIA-232 and V.28/V. 24 communications interfaces with automatic shutdown/wakeup features, high data-rate capabilities, and enhanced electrostatic discharge (ESD) protection. All transmitter outputs and receiver inputs are protected to $\pm 15 \mathrm{kV}$ using IEC 1000-4-2 Air-Gap Discharge, $\pm 8 \mathrm{kV}$ using IEC 1000-4-2 Contact Discharge, and $\pm 15 \mathrm{kV}$ using the Human Body Model.
All devices achieve a $1 \mu \mathrm{~A}$ supply current using Maxim's revolutionary AutoShutdown Plus ${ }^{\text {™ }}$ feature. These devices automatically enter a low-power shutdown mode when the RS-232 cable is disconnected or the transmitters of the connected peripherals are inactive, and the UART driving the transmitter inputs is inactive for more than 30 seconds. They turn on again when they sense a valid transition at any transmitter or receiver input. AutoShutdown Plus saves power without changes to the existing BIOS or operating system.
The MAX3225E/MAX3227E/MAX3245E also feature MegaBaud ${ }^{\text {TM }}$ operation, guaranteeing 1 Mbps for highspeed applications such as communicating with ISDN modems. The MAX3224E/MAX3226E/MAX3244E guarantee 250 kbps operation. The transceivers have a proprietary low-dropout transmitter output stage enabling true $\mathrm{RS}-232$ performance from $\mathrm{a}+3.0 \mathrm{~V}$ to +5.5 V supply with a dual charge pump. The charge pump requires only four small $0.1 \mu \mathrm{~F}$ capacitors for operation from a 3.3 V supply. The MAX3224E-MAX3227E feature a logiclevel output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.
All devices are available in a space-saving TQFN, SSOP, and TSSOP (MAX3224E/MAX3225E/MAX3244E/ MAX3245E) packages.

## Applications

Notebook, Subnotebook, and Palmtop Computers Cellular Phones
Battery-Powered Equipment
Hand-Held Equipment
Peripherals
Printers

## Next Generation Device Features

- For Space-Constrained Applications: MAX3228E/MAX3229E: $\pm 15 \mathrm{kV}$ ESD-Protected, +2.5 V to +5.5 V , RS-232 Transceivers in UCSP'M MAX3222E/MAX3232E/MAX3241E/MAX3246E: $\pm 15 \mathrm{kV}$ ESD-Protected, Down to $10 \mathrm{nA},+3.0 \mathrm{~V}$ to +5.5 V , Up to 1 Mbps , True RS-232 Transceivers (MAX3246E Available in UCSP)
- For Low-Voltage or Data Cable Applications: MAX3380E/MAX3381E: +2.35 V to $+5.5 \mathrm{~V}, 1 \mu \mathrm{~A}$, $2 T x / 2 R x$ RS-232 Transceivers with $\pm 15 \mathrm{kV}$ ESD-Protected I/O and Logic Pins


## Ordering Information

| PART | TEMP RANGE | PIN-PACKAGE |
| :--- | :--- | :--- |
| MAX3224ECTP + | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 TQFN-EP* |
| MAX3224ECUP + | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 TSSOP |
| MAX3224ECAP + | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 SSOP |
| MAX3224ECPP + | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 Plastic DIP |
| MAX3224EETP + | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 TQFN-EP* |
| MAX3224EEUP + | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 TSSOP |
| MAX3224EEAP + | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 SSOP |
| MAX3224EEPP+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 Plastic DIP |
| MAX3224EAAP + | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 20 SSOP |

+Denotes a lead(Pb)-free/RoHS-compliant package. *EP = Exposed pad
Ordering Information continued at end of data sheet.
Selector Guide

| PART | NO. OF DRIVERS/ RECEIVERS | GUARANTEED DATA RATE (bps) | READY OUTPUT | AutoShutdown Plus |
| :---: | :---: | :---: | :---: | :---: |
| MAX3224E | 2/2 | 250k | $\checkmark$ | $\checkmark$ |
| MAX3225E | 2/2 | 1M | $\checkmark$ | $\checkmark$ |
| MAX3226E | 1/1 | 250k | $\checkmark$ | $\checkmark$ |
| MAX3227E | 1/1 | 1M | $\checkmark$ | $\checkmark$ |
| MAX3244E | 3/5 | 250k | - | $\checkmark$ |
| MAX3245E | 3/5 | 1M | - | $\checkmark$ |

[^0]For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

## MAX3224E/MAX3225E/

MAX3226E/MAX3227E/MAX3244E/MAX3245E
$\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus

## ABSOLUTE MAXIMUM RATINGS

```
\(V_{C c}\) to GND
+ to GND (Note 1)
V- to GND (Note 1) ................................................... 0.3 V to -7V
V+ + V-| (Note 1) ...............................................................+13V
Input Voltages
T_IN, FORCEON, \(\overline{\text { FORCEOFF }}\) to GND ................. -0.3 V to +6 V
R_IN to GND
Output Voltages
T_OUT to GND
..\(\pm 13.2 \mathrm{~V}\)
R_OUT, INVALID, READY to GND .........-0.3V to (VCc + 0.3V) Short-Circuit Duration
T_OUT to GND
Continuous
Continuous Power Dissipation \(\left(\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)\)
16-Pin SSOP (derate \(7.14 \mathrm{~mW} /{ }^{\circ} \mathrm{C}\) above \(+70^{\circ} \mathrm{C}\) ) ......... 571 mW
16-Pin TSSOP (derate \(9.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}\) above \(+70^{\circ} \mathrm{C}\) ).... .754 .7 mW
16-Pin TQFN (derate \(20.8 \mathrm{~mW} /{ }^{\circ} \mathrm{C}\) above \(+70^{\circ} \mathrm{C}\) ) \(\ldots . .1666 .7 \mathrm{~mW}\)
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 20-Pin Plastic DIP (derate $11.11 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ... 889 mW 20-Pin SSOP (derate $8.00 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ......... 640 mW 20-Pin TSSOP (derate $10.9 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ....... 879 mW 28-Pin Wide SO (derate $12.5 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ )............ 1 W 28-Pin SSOP (derate $9.52 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ......... 762 mW 28-Pin TSSOP (derate $12.8 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ....... 1026 mW 36-Pin TQFN (derate $26.3 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ )........... 2105 mW Operating Temperature Ranges

Note 1: $\mathrm{V}+$ and V - can have maximum magnitudes of 7 V , but their absolute difference cannot exceed 13 V
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{VCC}=+3 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \mathrm{C} 1-\mathrm{C} 4=0.1 \mu \mathrm{~F}$, tested at $3.3 \mathrm{~V} \pm 10 \% ; \mathrm{C}_{1}=0.047 \mu \mathrm{~F}, \mathrm{C} 2-\mathrm{C} 4=0.33 \mu \mathrm{~F}$, tested at $5.0 \mathrm{~V} \pm 10 \% ; \mathrm{T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$ unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DC CHARACTERISTICS ( $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}$ or 5.0V, $\left.\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right)$ |  |  |  |  |  |  |
| Supply Current, AutoShutdown Plus |  | $\text { FORCEON }=\text { GND, } \overline{\text { FORCEOFF }}=V_{C C},$ <br> all R_IN idle, all T_IN idle |  | 1 | 10 | $\mu \mathrm{A}$ |
| Supply Current, Shutdown |  | $\overline{\text { FORCEOFF }}=$ GND |  | 1 | 10 | $\mu \mathrm{A}$ |
| Supply Current, AutoShutdown Plus Disabled |  | FORCEON $=\overline{\text { FORCEOFF }}=\mathrm{V}_{\mathrm{Cc}}$, no load |  | 0.3 | 1 | mA |

LOGIC INPUTS AND RECEIVER OUTPUTS

| Input Logic Threshold Low | T_IN, FORCEON, $\overline{\text { FORCEOFF }}$ |  |  | 0.8 | V |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input Logic Threshold High | $\begin{aligned} & \hline \text { T_IN, FORCEON, } \\ & \text { FORCEOFF } \end{aligned}$ | $V_{C C}=3.3 \mathrm{~V}$ | 2 |  | V |
|  |  | V CC $=5.0 \mathrm{~V}$ | 2.4 |  |  |
| Transmitter Input Hysteresis |  |  | 0.5 |  | V |
| Input Leakage Current | T_IN, FORCEON, $\overline{\text { FORCEOFF }}$ |  | $\pm 0.01$ | $\pm 1$ | $\mu \mathrm{A}$ |
| Output Leakage Current | R_OUT (MAX3244E/MAX3245E), receivers disabled |  | $\pm 0.05$ | $\pm 10$ | $\mu \mathrm{A}$ |
| Output Voltage Low | IOUT $=1.6 \mathrm{~mA}$ |  |  | 0.4 | V |
| Output Voltage High | IOUT $=-1.0 \mathrm{~mA}$ |  | $V_{C C}-0.6 V_{C C}-0.1$ |  | V |

RECEIVER INPUTS

| Input Voltage Range |  |  | -25 |  | +25 | V |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Threshold Low | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}$ | 0.6 | 1.2 |  | V |
|  |  | $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ | 0.8 | 1.5 |  |  |
| Input Threshold High | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{V}_{C C}=3.3 \mathrm{~V}$ |  | 1.5 | 2.4 | V |
|  |  | $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ |  | 1.8 | 2.4 |  |
| Input Hysteresis |  |  |  | 0.5 |  | V |
| Input Resistance | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | 3 | 5 | 7 | k $\Omega$ |

## ELECTRICAL CHARACTERISTICS (continued)

$\left(\mathrm{V}_{C C}=+3 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \mathrm{C} 1-\mathrm{C} 4=0.1 \mu \mathrm{~F}$, tested at $3.3 \mathrm{~V} \pm 10 \% ; \mathrm{C}_{1}=0.047 \mu \mathrm{~F}, \mathrm{C} 2-\mathrm{C} 4=0.33 \mu \mathrm{~F}$, tested at $5.0 \mathrm{~V} \pm 10 \% ; \mathrm{T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| TRANSMITTER OUTPUTS |  |  |  |  |  |  |
| Output Voltage Swing |  | All transmitter outputs loaded with $3 \mathrm{k} \Omega$ to ground | $\pm 5$ | $\pm 5.4$ |  | V |
| Output Resistance |  | $\mathrm{V} \mathrm{CC}=\mathrm{V}+=\mathrm{V}$ - $=0 \mathrm{~V}$, transmitter outputs $= \pm 2 \mathrm{~V}$ | 300 | 10M |  | $\Omega$ |
| Output Short-Circuit Current |  |  |  |  | $\pm 60$ | mA |
| Output Leakage Current |  | $\mathrm{V}_{\mathrm{CC}}=0 \mathrm{~V}$ or 3 V to $5.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}= \pm 12 \mathrm{~V}$, Transmitters disabled |  |  | $\pm 25$ | $\mu \mathrm{A}$ |
| MOUSE DRIVEABILITY (MAX3244E/MAX3245E) |  |  |  |  |  |  |
| Transmitter Output Voltage |  | $\mathrm{T} 1 \mathrm{IN}=\mathrm{T} 2 \mathrm{IN}=\mathrm{GND}, \mathrm{T} 3 \mathrm{IN}=\mathrm{V}_{\mathrm{CC}}$, T3OUT loaded with $3 k \Omega$ to GND, T1OUT and T2OUT loaded with 2.5 mA each | $\pm 5$ |  |  | V |
| ESD PROTECTION |  |  |  |  |  |  |
| R_IN, T_OUT |  | IEC 1000-4-2 Air Discharge |  | $\pm 15$ |  | kV |
|  |  | IEC 1000-4-2 Contact Discharge |  | $\pm 8$ |  |  |
|  |  | Human Body Model |  | $\pm 15$ |  |  |
| AutoShutdown Plus (FORCEON = GND, $\overline{\text { FORCEOFF }}=\mathrm{V}_{\text {CC }}$ ) |  |  |  |  |  |  |
| Receiver Input Threshold to INVALID Output High |  | Figure 4a |  |  | 2.7 | V |
|  |  |  | -2.7 |  |  |  |
| Receiver Input Threshold to INVALID Output Low |  | Figure 4a | -0.3 |  | +0.3 | V |
| INVALID, READY Output Voltage Low (MAX3224E-MAX3227E) |  | IOUT $=-1.6 \mathrm{~mA}$ |  |  | 0.4 | V |
| INVALID, READY Output Voltage High (MAX3224E-MAX3227E) |  | IOUT $=-1.0 \mathrm{~mA}$ | $V_{\text {cc }}-0.6$ |  |  | V |
| Receiver Positive or Negative Threshold to INVALID High | tinvi | $V_{C C}=5 \mathrm{~V}$, Figure 4 b |  | 1 |  | $\mu \mathrm{S}$ |
| Receiver Positive or Negative Threshold to INVALID Low | tinvL | $V_{C C}=5 \mathrm{~V}$, Figure 4 b |  | 30 |  | $\mu \mathrm{s}$ |
| Receiver or Transmitter Edge to Transmitters Enabled | twu | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$, Figure 5b (Note 2) |  | 100 |  | $\mu \mathrm{s}$ |
| Receiver or Transmitter Edge to Transmitters Shutdown | ${ }^{\text {t AUTOSHDN }}$ | VCC $=5 \mathrm{~V}$, Figure 5b (Note 2) | 15 | 30 | 60 | S |

## TIMING CHARACTERISTICS—MAX3224E/MAX3226E/MAX3244E

$\left(\mathrm{V}_{\mathrm{CC}}=+3 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \mathrm{C} 1-\mathrm{C} 4=0.1 \mu \mathrm{~F}$, tested at $3.3 \mathrm{~V} \pm 10 \% ; \mathrm{C}_{1}=0.047 \mu \mathrm{~F}, \mathrm{C} 2-\mathrm{C} 4=0.33 \mu \mathrm{~F}$, tested at $5.0 \mathrm{~V} \pm 10 \% ; \mathrm{T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$ unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum Data Rate |  | $R_{L}=3 \mathrm{k} \Omega, C_{L}=1000 \mathrm{pF},$ one transmitter switching |  | 250 |  |  | kbps |
| Receiver Propagation Delay | tPHL | R_IN to R_OUT, CL = 150pF |  | 0.15 |  |  | $\mu \mathrm{s}$ |
|  | tPLH |  |  | 0.15 |  |  |  |
| Receiver Output Enable Time |  | Normal operation (MAX3244E only) |  |  | 200 |  | ns |
| Receiver Output Disable Time |  | Normal operation (MAX3244E only) |  |  | 200 |  | ns |
| Transmitter Skew | tPHL - tPLH | (Note 3) |  |  | 100 |  | ns |
| Receiver Skew | tPHL - tPLH \| |  |  |  | 50 |  | ns |
| Transition-Region Slew Rate |  | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C},$ $R \mathrm{~L}=3 \mathrm{k} \Omega \text { to } 7 \mathrm{k} \Omega \text {, }$ <br> measured from +3 V to -3 V or -3 V to +3 V , one transmitter switching | $\begin{aligned} & C_{L}=150 \mathrm{pF} \\ & \text { to } 1000 \mathrm{pF} \end{aligned}$ | 6 |  | 30 | V/us |

## TIMING CHARACTERISTICS—MAX3225E/MAX3227E/MAX3245E

$\left(\mathrm{V}_{C C}=+3 \mathrm{~V}\right.$ to $+5.5 \mathrm{~V}, \mathrm{C} 1-\mathrm{C} 4=0.1 \mu \mathrm{~F}$, tested at $3.3 \mathrm{~V} \pm 10 \% ; \mathrm{C}_{1}=0.047 \mu \mathrm{~F}, \mathrm{C} 2-\mathrm{C} 4=0.33 \mu \mathrm{~F}$, tested at $5.0 \mathrm{~V} \pm 10 \% ; \mathrm{T}_{\mathrm{A}}=\mathrm{T}_{\mathrm{MIN}}$ to $\mathrm{T}_{\mathrm{MAX}}$ unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum Data Rate |  | $R_{L}=3 k \Omega, C_{L}=1000 \mathrm{pF},$ one transmitter switching | 250 |  |  | kbps |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=3.0 \mathrm{~V} \text { to } 4.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=3 \mathrm{k} \Omega \\ & \mathrm{C}_{\mathrm{L}}=250 \mathrm{pF} \text {, one transmitter switching } \end{aligned}$ | 1000 |  |  |  |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V} \text { to } 5.5 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=3 \mathrm{k} \Omega \text {, } \\ & \mathrm{C}_{\mathrm{L}}=1000 \mathrm{pF} \text {, one transmitter switching } \end{aligned}$ | 1000 |  |  |  |
| Receiver Propagation Delay | tPHL | R_IN to R_OUT, CL = 150pF | 0.15 |  |  | $\mu \mathrm{S}$ |
|  | tPLH |  |  | 0.15 |  |  |
| Receiver Output Enable Time |  | Normal operation (MAX3245E only) |  | 200 |  | ns |
| Receiver Output Disable Time |  | Normal operation (MAX3245E only) |  | 200 |  | ns |
| Transmitter Skew | \| tPHL - tPLH | | (Note 3) |  | 25 |  | ns |
| Receiver Skew | \| tPHL - tPLH | |  |  | 50 |  | ns |
| Transition-Region Slew Rate |  | $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, <br> $R_{L}=3 \mathrm{k} \Omega$ to $7 \mathrm{k} \Omega, C_{L}=150 \mathrm{pF}$ to 1000 pF , measured from +3 V to -3 V or -3 V to +3 V , one transmitter switching | 24 |  | 150 | V/us |

Note 2: A transmitter/receiver edge is defined as a transition through the transmitter/receiver input logic thresholds.
Note 3: Transmitter skew is measured at the transmitter zero cross points.

## MAX3226E/MAX3227E/MAX3244E/MAX3245E

 $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps, 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown PlusTypical Operating Characteristics
( $\mathrm{V} C \mathrm{CC}=+3.3 \mathrm{~V}$, 250kbps data rate, $0.1 \mu \mathrm{~F}$ capacitors, all transmitters loaded with $3 \mathrm{k} \Omega$ and $\mathrm{C}_{\mathrm{L}}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)
 RS-232 Transceivers with AutoShutdown Plus

## Typical Operating Characteristics (continued)

( $\mathrm{V}_{\mathrm{CC}}=+3.3 \mathrm{~V}$, 250 kbps data rate, $0.1 \mu \mathrm{~F}$ capacitors, all transmitters loaded with $3 \mathrm{k} \Omega$ and $\mathrm{C}_{\mathrm{L}}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise noted.)

## MAX3244E

TRANSMITTER OUTPUT VOLTAGE vs. LOAD CAPACITANCE


MAX3245E
TRANSMITTER OUTPUT VOLTAGE vs. LOAD CAPACITANCE


MAX3245E
OPERATING SUPPLY CURRENT vs. LOAD CAPACITANCE


MAX3244E


MAX3244E
OPERATING SUPPLY CURRENT vs. LOAD CAPACITANCE


MAX3245E
SLEW RATE vs. LOAD CAPACITANCE


MAX3245E TRANSMITTER SKEW vs. LOAD CAPACITANCE


Pin Description

| PIN |  |  |  |  |  | NAME | FUNCTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MAX3224E MAX3225E |  | MAX3226E MAX3227E |  | $\begin{gathered} \text { MAX3244E } \\ \text { MAX3245E } \\ \text { SO/ SSOP/ TSSOP } \end{gathered}$ | $\begin{gathered} \text { MAX3245E } \\ \text { TQFN } \end{gathered}$ |  |  |
| TQFN | $\begin{aligned} & \text { DIP/ } \\ & \text { SSOP/ } \\ & \text { TSSOP } \end{aligned}$ | TQFN | $\begin{aligned} & \text { SSOP/ } \\ & \text { TSSOP } \end{aligned}$ |  |  |  |  |
| 19 | 1 | 14 | 1 | - | - | READY | Ready to Transmit Output, active-high. READY is enabled high when V- goes below -4 V and the device is ready to transmit. |
| 1 | 2 | 16 | 2 | 28 | 31 | C1+ | Positive Terminal of VoltageDoubler Charge-Pump Capacitor |
| 20 | 3 | 15 | 3 | 27 | 30 | V+ | +5.5 V generated by the charge pump |
| 2 | 4 | 1 | 4 | 24 | 26 | C1- | Negative Terminal of Voltage- <br> Doubler Charge-Pump <br> Capacitor |
| 3 | 5 | 2 | 5 | 1 | 33 | C2+ | Positive Terminal of Inverting Charge-Pump Capacitor |
| 4 | 6 | 3 | 6 | 2 | 34 | C2- | Negative Terminal of Inverting Charge-Pump Capacitor |
| 5 | 7 | 4 | 7 | 3 | 35 | V- | -5.5 V Generated by the Charge Pump |
| 6, 15 | 8, 17 | 10 | 13 | 9, 10, 11 | 7, 8, 9 | T_OUT | RS-232 Transmitter Outputs |
| 7, 14 | 9, 16 | 5 | 8 | 4-8 | 1-5 | R_IN | RS-232 Receiver Inputs |
| 8, 13 | 10, 15 | 6 | 9 | 15-19 | $\begin{gathered} 15,16,17, \\ 19,20 \end{gathered}$ | R_OUT | TTL/CMOS Receiver Outputs |
| 9 | 11 | 7 | 10 | 21 | 23 | $\overline{\text { INVALID }}$ | Valid Signal Detector Output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input. |
| 10, 11 | 12, 13 | 8 | 11 | 12-14 | 11, 12, 13 | T_IN | TTL/CMOS Transmitter Inputs |

Pin Description (continued)

| PIN |  |  |  |  |  | NAME | FUNCTION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MAX3224E MAX3225E |  | MAX3226E MAX3227E |  | $\begin{gathered} \text { MAX3244E } \\ \text { MAX3245E } \\ \text { SO/ SSOP/ TSSOP } \end{gathered}$ | $\begin{aligned} & \text { MAX3245E } \\ & \text { TQFN } \end{aligned}$ |  |  |
| TQFN | $\begin{aligned} & \text { DIP/ } \\ & \text { SSOP/ } \\ & \text { TSSOP } \end{aligned}$ | TQFN | $\begin{aligned} & \text { SSOP/ } \\ & \text { TSSOP } \end{aligned}$ |  |  |  |  |
| 12 | 14 | 9 | 12 | 23 | 25 | FORCEON | Force-On Input, Active High. Drive high to override AutoShutdown Plus, keeping transmitters and receivers on (FORCEOFF must be high) (Table 1). |
| 16 | 18 | 11 | 14 | 25 | 27 | GND | Ground |
| 17 | 19 | 12 | 15 | 26 | 29 | VCC | +3.0 V to +5.5 V Single Supply Voltage |
| 18 | 20 | 13 | 16 | 22 | 24 | $\overline{\text { FORCEOFF }}$ | Force-Off Input, Active Low. Drive low to shut down transmitters, receivers (except R2OUTB), and charge pump. This overrides AutoShutdown Plus and FORCEON (Table 1). |
| - | - | - | - | 20 | 21 | R2OUTB | TTL/CMOS Noninverting Complementary Receiver Outputs. Always active. |
| - | - | - | - | - | $\begin{gathered} 6,10,14, \\ 18,22,28, \\ 32,36 \end{gathered}$ | N.C. | No Connection. Not internally connected. |
| - | - | - | - | - | - | EP | Exposed Pad. Solder the exposed pad to the ground plane or leave unconnected. |

## Detailed Description

## Dual Charge-Pump Voltage Converter

The MAX3224E-MAX3227E/MAX3244E/MAX3245E's internal power supply consists of a regulated dual charge pump that provides output voltages of +5.5 V (doubling charge pump) and -5.5 V (inverting charge pump), over the +3.0 V to +5.5 V range. The charge pump operates in discontinuous mode: if the output voltages are less than 5.5 V , the charge pump is
enabled; if the output voltages exceed 5.5 V , the charge-pump is disabled. Each charge pump requires a flying capacitor (C1, C2) and a reservoir capacitor (C3, C4) to generate the $\mathrm{V}+$ and V - supplies.
The READY output (MAX3224E-MAX3227E) is low when the charge pumps are disabled in shutdown mode. The READY signal asserts high when V- goes below -4 V .


Figure 1. Interface Under Control of PMU

RS-232 Transmitters
The transmitters are inverting level translators that convert CMOS-logic levels to 5.0V EIA/TIA-232 levels. The MAX3224E/MAX3226E/MAX3244E guarantee a 250kbps data rate (1Mbps, for the MAX3225E/MAX3227E/ MAX3245E) with worst-case loads of $3 k \Omega$ in parallel with 1000 pF , providing compatibility with PC-to-PC communication software (such as LapLink ${ }^{\circledR}$ ). Transmitters can be paralleled to drive multiple receivers. Figure 1 shows a complete system connection.
When $\overline{\text { FORCEOFF }}$ is driven to ground or when the AutoShutdown Plus circuitry senses that all receiver and transmitter inputs are inactive for more than 30s, the transmitters are disabled and the outputs go into a highimpedance state. When powered off or shut down, the outputs can be driven to $\pm 12 \mathrm{~V}$. The transmitter inputs do not have pullup resistors. Connect unused inputs to GND or VCC.

LapLink is a registered trademark of Laplink Software, Inc.


Figure 2. The MAX3244E/MAX3245E detect RS-232 activity when the UART and interface are shut down.

## Table 1. Output Control Truth Table

| OPERATION STATUS | FORCEON | $\overline{\text { FORCEOFF }}$ | VALID RECEIVER LEVEL | RECEIVER OR TRANSMITTER EDGE WITHIN 30s | T_OUT | R OUT (MAX3224E/ MAX3225E/ MAX3226E/ MAX3227E) | R_OUT (MAX3244E/ MAX3245E) | R2OUTB (MAX3244E/ MAX3245E) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Shutdown (Forced Off) | X | 0 | X | X | High-Z | Active | High-Z | Active |
| Normal Operation (Forced On) | 1 | 1 | X | X | Active | Active | Active | Active |
| Normal Operation (AutoShutdown Plus) | 0 | 1 | X | Yes | Active | Active | Active | Active |
| Shutdown (AutoShutdown Plus) | 0 | 1 | X | No | High-Z | Active | Active | Active |
| Normal Operation | $\overline{\text { INVALID }}$ | 1 | Yes | X | Active | Active | Active | Active |
| Normal Operation | $\overline{\text { INVALID }}$ | 1 | X | Yes | Active | Active | Active | Active |
| Shutdown | $\overline{\text { INVALID* }}$ | 1 | No | No | High-Z | Active | Active | Active |
| Normal Operation (AutoShutdown) | $\overline{\text { INVALID* }}$ | $\overline{\text { INVALID }}$ ** | Yes | X | Active | Active | Active | Active |
| Shutdown (AutoShutdown) | $\overline{\text { INVALID* }}$ | $\overline{\text { INVALID }}$ ** | No | X | High-Z | Active | High-Z | Active |

$X=$ Don't care

* INVALID connected to FORCEON
** $\overline{\text { INVALID }}$ connected to FORCEON and $\overline{\text { FORCEOFF }}$

RS-232 Receivers
The receivers convert RS-232 signals to CMOS-logic output levels. The MAX3224E-MAX3227E feature inverting outputs that always remain active (Table 1). The MAX3244E/MAX3245E have inverting three-state outputs that are high impedance when shut down (FORCEOFF = GND) (Table 1).
The MAX3244E/MAX3245E feature an extra, always active, noninverting output, R2OUTB. R2OUTB output monitors receiver activity while the other receivers are high impedance, allowing ring indicator applications to be monitored without forward biasing other devices connected to the receiver outputs. This is ideal for systems where $\mathrm{V}_{\text {CC }}$ is set to ground in shutdown to accommodate peripherals such as UARTs (Figure 2).

The MAX3224E-MAX3227E/MAX3244E/MAX3245E feature an INVALID output that is enabled low when no valid RS-232 voltage levels have been detected on all receiver inputs. Because INVALID indicates the receiver input's condition, it is independent of FORCEON and FORCEOFF states (Figures 3 and 4).

## AutoShutdown Plus Mode

The MAX3224E-MAX3227E/MAX3244E/MAX3245E achieve a $1 \mu \mathrm{~A}$ supply current with Maxim's AutoShutdown Plus feature, which operates when FORCEOFF is high and a FORCEON is low. When these devices do not sense a valid signal transition on any receiver and transmitter input for 30 s, the on-board charge pumps are shut down, reducing supply current to $1 \mu \mathrm{~A}$. This occurs if the RS-232 cable is disconnected or if the connected

$\overline{\operatorname{INVALID}}$ ASSERTED IF ALL RECEIVER INPUTS ARE BETWEEN $+0.3 V$ AND - $0.3 \mathrm{3V}$ FOR AT LEAST 30 us .

Figure 3a. $\overline{\text { INVALID Functional Diagram, } \overline{I N V A L I D ~} \text { Low }}$


INVALID DEASSERTED IF ANY RECEIVER INPUT HAS BEEN BETWEEN +2.7V AND -2.7V FOR LESS THAN $30 \mu \mathrm{~S}$.

Figure 3b. $\overline{\text { INVALID }}$ Functional Diagram, $\overline{\text { INVALID }}$ High

## Table 2. $\overline{\text { INVALID }}$ Truth Table

| RS-232 SIGNAL <br> PRESENT AT ANY <br> RECEIVER INPUT | INVALID OUTPUT |
| :---: | :---: |
| Yes | High |
| No | Low |

peripheral transmitters are turned off, and the UART driving the transmitter inputs is inactive. The system turns on again when a valid transition is applied to any RS-232 receiver or transmitter input. As a result, the system saves power without changes to the existing BIOS or operating system.
Figures $3 a$ and 3b depict valid and invalid RS-232 receiver voltage levels. INVALID indicates the receiver input's condition, and is independent of FORCEON and $\overline{\text { FORCEOFF }}$ states. Figure 3 and Tables 1 and 2 summarize the operating modes of the MAX3224EMAX3227E/MAX3244E/MAX3245E. FORCEON and FORCEOFF override AutoShutdown Plus circuitry. When neither control is asserted, the IC selects between these states automatically based on the last receiver or transmitter input edge received.


Figure 3c. AutoShutdown Plus Logic


Figure 3d. Power-Down Logic
When shut down, the device's charge pumps turn off, $V+$ is pulled to $V_{C C}, V$ - is pulled to ground, the transmitter outputs are high impedance, and READY (MAX3224E-MAX3227E) is driven low. The time required to exit shutdown is typically 100 $\mu$ (Figure 8).
By connecting FORCEON to INVALID, the MAX3224EMAX3227E/MAX3244E/MAX3245E shut down when no valid receiver level and no receiver or transmitter edge is detected for 30s, and wake up when a valid receiver level or receiver or transmitter edge is detected.


Figure 4a. Receiver Positive/Negative Thresholds for INVALID


Figure 4b. AutoShutdown Plus, $\overline{I N V A L I D}$, and READY Timing Diagram

By connecting FORCEON and FORCEOFF to INVALID, the MAX3224E-MAX3227E/MAX3244E/MAX3245E shut down when no valid receiver level is detected and wake up when a valid receiver level is detected (same functionality as AutoShutdown feature on MAX3221E/ MAX3223E/MAX3243E).
A mouse or other system with AutoShutdown Plus may need time to wake up. Figure 5 shows a circuit that forces the transmitters on for 100 ms , allowing enough time for the other system to realize that the MAX3244E/ MAX3245E is awake. If the other system outputs valid RS-232 signal transitions within that time, the RS-232 ports on both systems remain enabled.

## Software-Controlled Shutdown

If direct software control is desired, use INVALID to indicate DTR or ring indicator signal. Tie FORCEOFF and FORCEON together to bypass the AutoShutdown Plus so the line acts like a SHDN input.

## $\pm 15 k V$ ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic
discharges encountered during handling and assembly. The driver outputs and receiver inputs of the MAX3224E-MAX3227E/MAX3244E/MAX3245E have extra protection against static electricity. Maxim's engineers have developed state-of-the-art structures to protect


Figure 5. AutoShutdown Plus Initial Turn-On to Wake Up a Mouse or Another System


Figure 6a. Human Body ESD Test Model


Figure 6b. Human Body Current Waveform
these pins against ESD of $\pm 15 \mathrm{kV}$ without damage. The ESD structures withstand high ESD in all states: normal operation, shutdown, and powered down. After an ESD event, Maxim's E versions keep working without latchup, whereas competing RS-232 products can latch and must be powered down to remove latchup.
ESD protection can be tested in various ways; the transmitter outputs and receiver inputs of this product family are characterized for protection to the following limits:

1) $\pm 15 \mathrm{kV}$ using the Human Body Model
2) $\pm 8 \mathrm{kV}$ using the Contact-Discharge Method specified in IEC 1000-4-2
3) $\pm 15 \mathrm{kV}$ using IEC 1000-4-2's Air-Gap Method.


Figure 7a. IEC 1000-4-2 ESD Test Model


Figure 7b. IEC 1000-4-2 ESD Generator Current Waveform

## ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

Human Body Model
Figure 6a shows the Human Body Model and Figure 6b shows the current waveform it generates when discharged into a low impedance. This model consists of a 100 pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a $1.5 \mathrm{k} \Omega$ resistor.

# MAX3226E/MAX3227E/MAX3244E/MAX3245E $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus 

IEC 1000-4-2
The IEC 1000-4-2 standard covers ESD testing and performance of finished equipment; it does not specifically refer to integrated circuits. The MAX3224E-MAX3227E, MAX3244E/MAX3245E help you design equipment that meets Level 4 (the highest level) of IEC 1000-4-2, without the need for additional ESD-protection components.
The major difference between tests done using the Human Body Model and IEC 1000-4-2 is higher peak current in IEC 1000-4-2, because series resistance is lower in the IEC 1000-4-2 model. Hence, the ESD withstand voltage measured to IEC 1000-4-2 is generally lower than that measured using the Human Body Model. Figure 7a shows the IEC 1000-4-2 model and Figure 7 b shows the current waveform for the 8 kV , IEC 1000-4-2, Level 4, ESD Contact-Discharge Method.
The Air-Gap Method involves approaching the device with a charged probe. The Contact-Discharge Method connects the probe to the device before the probe is energized.

## Machine Model

The Machine Model for ESD tests all pins using a 200pF storage capacitor and zero discharge resistance. Its objective is to emulate the stress caused by contact that occurs with handling and assembly during manufacturing. Of course, all pins require this protection during manufacturing, not just RS-232 inputs and outputs. Therefore, after PC board assembly, the Machine Model is less relevant to I/O ports.

## Applications Information

Capacitor Selection The capacitor type used for C1-C4 is not critical for proper operation; polarized or nonpolarized capacitors

## Table 3. Required Minimum Capacitance Values

| VCC <br> $\mathbf{( V )}$ | C1, CBYPASS <br> $(\boldsymbol{\mu F})$ | $\mathbf{C 2 , ~ C 3 , ~ C 4 ~}$ <br> $\mathbf{( \boldsymbol { \mu F } )}$ |
| :---: | :---: | :---: |
| 3.0 to 3.6 | 0.22 | 0.22 |
| 3.15 to 3.6 | 0.1 | 0.1 |
| 4.5 to 5.5 | 0.047 | 0.33 |
| 3.0 to 5.5 | 0.22 | 1 |



Figure 8. Transmitter Outputs when Exiting Shutdown or Powering Up
can be used. The charge pump requires $0.1 \mu \mathrm{~F}$ capacitors for 3.3V operation. For other supply voltages, see Table 3 for required capacitor values. Do not use values smaller than those listed in Table 3. Increasing the capacitor values (e.g., by a factor of 2 ) reduces ripple on the transmitter outputs and slightly reduces power consumption. C2, C3, and C4 can be increased without changing C1's value. However, do not increase C1 without also increasing the values of C2, C3, C4, and CbyPASS, to maintain the proper ratios (C1 to the other capacitors).
When using the minimum required capacitor values, make sure the capacitor value does not degrade excessively with temperature. If in doubt, use capacitors with a larger nominal value. The capacitor's equivalent series resistance (ESR), which usually rises at low temperatures, influences the amount of ripple on $\mathrm{V}+$ and V -

## Power-Supply Decoupling

In most circumstances, a $0.1 \mu \mathrm{~F} \mathrm{~V}_{\mathrm{C}}$ bypass capacitor is adequate. In applications that are sensitive to powersupply noise, use a capacitor of the same value as charge-pump capacitor C1. Connect bypass capacitors as close to the IC as possible.

## Transmitter Outputs when Exiting Shutdown

Figure 8 shows two transmitter outputs when exiting shutdown mode. As they become active, the two transmitter outputs are shown going to opposite RS-232 levels (one transmitter input is high, the other is low). Each

# MAX3226E/MAX3227E/MAX3244E/MAX3245E <br> $\pm 15 \mathrm{kV}$ ESD-Protected, $1 \mu A, 1 \mathrm{Mbps} 3.0 \mathrm{~V}$ to 5.5V, RS-232 Transceivers with AutoShutdown Plus 

transmitter is loaded with $3 k \Omega$ in parallel with 1000 pF . The transmitter outputs display no ringing or undesirable transients as they come out of shutdown. Note that the transmitters are enabled only when the magnitude of V - exceeds approximately -3 V .

## High Data Rates

The MAX3224E/MAX3226E/MAX3244E maintain the RS-232 $\pm 5.0 \mathrm{~V}$ minimum transmitter output voltage even at high data rates. Figure 9 shows a transmitter loopback test circuit. Figure 10 shows a loopback test result at 120 kbps , and Figure 11 shows the same test at 250kbps. For Figure 10, all transmitters were driven simultaneously at 120kbps into RS-232 loads in parallel with 1000 pF. For Figure 11, a single transmitter was driven at 250kbps, and all transmitters were loaded with an RS-232 receiver in parallel with 250pF.
The MAX3225E/MAX3227E/MAX3245E maintain the RS-232 $\pm 5.0 \mathrm{~V}$ minimum transmitter output voltage at data rates up to 1Mbps (MegaBaud). Figure 12 shows a loopback test result with a single transmitter driven at 1 Mbps and all transmitters loaded with an RS-232 receiver in parallel with 250pF


Figure 9. Loopback Test Circuit


Figure 10. MAX3224E/MAX3226E/MAX3244E Loopback Test Result at 120kbps


Figure 11. MAX3224E/MAX3226E/MAX3244E Loopback Test Result at 250kbps


Figure 12. MAX3225E/MAX3227E/MAX3245E Loopback Test Result at 1Mbps RS-232 Transceivers with AutoShutdown Plus


Figure 13a. Mouse Driver Test Circuit

## MAX3224E/MAX3225E/

## MAX3226E/MAX3227E/MAX3244E/MAX3245E $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus



Figure 13b. MAX324_E Transmitter Output Voltage vs. Load Current per Transmitter

Mouse Driveability
The MAX3244E/MAX3245E are specifically designed to power serial mice while operating from low-voltage power supplies. They have been tested with leading mouse brands from manufacturers such as Microsoft and Logitech. The MAX3244E/MAX3245E successfully drove all serial mice tested and met their respective current and voltage requirements. The MAX3244E/ MAX3245E dual charge pump ensures the transmitters supply at least $\pm 5 \mathrm{~V}$ during worst-case conditions. Figure 13b shows the transmitter output voltages under increasing load current. Figure 13a shows a typical mouse connection.

Interconnection with 3V and 5V Logic The MAX3224E-MAX3227E/MAX3244E/MAX3245E can directly interface with various 5V logic families, including ACT and HCT CMOS. See Table 4 for more information on possible combinations of interconnections.
Table 5 lists other Maxim ESD-powered transceivers.

Table 4. Logic Family Compatibility with Various Supply Voltages

| SYSTEM <br> POWER-SUPPLY <br> VOLTAGE (V) | VCc SUPPLY <br> VOLTAGE <br> $\mathbf{( V )}$ | COMPATIBILITY |
| :---: | :---: | :--- |
| 3.3 | 3.3 | Compatible with all CMOS families |
| 5 | 5 | Compatible with all TTL and CMOS families |
| 5 | 3.3 | Compatible with ACT and HCT CMOS, and with AC, HC, or CD4000 CMOS |

Table 5. $\pm 15 \mathrm{kV}$ ESD-Protected, 3.0V to 5.5V Powered RS-232 Transceivers from Maxim

| PART | SUPPLY <br> VOLTAGE <br> RANGE <br> $\mathbf{( V )}$ | NO. <br> OF <br> Tx/Rx | SUPPLY <br> CURRENT <br> ( $\boldsymbol{\mu A}$ ) | Auto- <br> Shutdown <br> Plus | Auto- <br> Shutdown | Human <br> Body <br> Model <br> $\mathbf{( k V ) ~}$ | IEC 1000-4-2 <br> Contact <br> Discharge <br> $\mathbf{( k V ) ~}$ | IEC 1000-4-2 <br> Air-Gap <br> Discharge <br> (kV) | GUARANTEED <br> DATA RATE <br> (kbps) |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MAX3241E | +3.0 to +5.5 | $3 / 5$ | 300 | - | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3243E | +3.0 to +5.5 | $3 / 5$ | 1 | - | Yes | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3244E | +3.0 to +5.5 | $3 / 5$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3245E | +3.0 to +5.5 | $3 / 5$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 1 Mbps |
| MAX3232E | +3.0 to +5.5 | $2 / 2$ | 300 | - | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3222E | +3.0 to +5.5 | $2 / 2$ | 300 | - | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3223E | +3.0 to +5.5 | $2 / 2$ | 1 | - | Yes | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3224E | +3.0 to +5.5 | $2 / 2$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3225E | +3.0 to +5.5 | $2 / 2$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 1 Mbps |
| MAX3221E | +3.0 to +5.5 | $1 / 1$ | 1 | - | Yes | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3226E | +3.0 to +5.5 | $1 / 1$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 250 |
| MAX3227E | +3.0 to +5.5 | $1 / 1$ | 1 | Yes | - | $\pm 15$ | $\pm 8$ | $\pm 15$ | 1 Mbps |



# MAX3224E/MAX3225E/ <br> MAX3226E/MAX3227E/MAX3244E/MAX3245E <br> $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus 

Pin Configurations


## _Ordering Information (continued)

| PART | TEMP RANGE | PIN-PACKAGE |
| :---: | :---: | :---: |
| MAX3225ECUP+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 TSSOP |
| MAX3225ECTP+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 TQFN-EP* |
| MAX3225ECAP+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 SSOP |
| MAX3225ECPP+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 20 Plastic DIP |
| MAX3225EETP+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 TQFN -EP* |
| MAX3225EEUP+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 TSSOP |
| MAX3225EEAP+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 SSOP |
| MAX3225EEPP+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 20 Plastic DIP |
| MAX3225EAAP+ | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 20 SSOP |
| MAX3226ECTE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 TQFN-EP* |
| MAX3226ECUE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 TSSOP |
| MAX3226ECAE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3226EEAE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3226EETE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 TQFN-EP* |
| MAX3226EEUE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 TSSOP |
| MAX3226EAAE+ | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3227ECAE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3227ECTE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 TQFN-EP* |
| MAX3227ECUE+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 16 TSSOP |
| MAX3227EEAE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3227EEAE/V+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3227EETE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 TQFN-EP* |
| MAX3227EEUE+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 16 TSSOP |
| MAX3227EAAE+ | $-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ | 16 SSOP |
| MAX3244ECWI+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 28 Wide SO |
| MAX3244ECAI+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 28 SSOP |
| MAX3244ECUI+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 28 TSSOP |
| MAX3244EEWI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 Wide SO |
| MAX3244EEAI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 SSOP |
| MAX3244EEUI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 TSSOP |
| MAX3245ECWI+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 28 Wide SO |
| MAX3245ECAI+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 28 SSOP |
| MAX3245ECTX+ | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ | 36 TQFN- EP* |
| MAX3245EEAI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 SSOP |
| MAX3245EEWI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 WIDE SO |
| MAX3245EEUI+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 28 TSSOP |
| MAX3245EETX+ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 36 TQFN- EP* |

+Denotes a lead(Pb)-free/RoHS-compliant package.
*EP = Exposed pad.
$N$ denotes an automotive qualified part.

## Chip Information

PROCESS: BICMOS

## Package Information

For the latest package outline information and land patterns (footprints), go to www.maxim-ic.com/packages. Note that a "+", "\#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

| PACKAGE <br> TYPE | PACKAGE <br> CODE | OUTLINE <br> NO. | LAND <br> PATTERN NO. |
| :---: | :---: | :---: | :---: |
| 16 SSOP | $\mathrm{A} 16+2$ | $\underline{\underline{21-0056}}$ | $\underline{\underline{90-0106}}$ |
| 16 TSSOP | $\mathrm{U} 16+1$ | $\underline{\underline{21-0066}}$ | $\underline{\underline{90-0117}}$ |
| 16 TQFN-EP | $\mathrm{T} 1655+2$ | $\underline{\underline{21-0140}}$ | $\underline{\underline{90-0072}}$ |
| 20 DIP | $\mathrm{P} 20+3$ | $\underline{\underline{21-0043}}$ | - |
| 20 SSOP | $\mathrm{A} 20+1$ | $\underline{\underline{21-0056}}$ | $\underline{\underline{90-0094}}$ |
| 20 TSSOP | $\mathrm{U} 20+2$ | $\underline{\underline{21-0066}}$ | $\underline{\underline{90-0116}}$ |
| 20 TQFN-EP | T2055+5 | $\underline{\underline{21-0140}}$ | $\underline{\underline{90-0010}}$ |
| 28 Wide SO | $\mathrm{W} 28+6$ | $\underline{\underline{21-0042}}$ | $\underline{\underline{90-0109}}$ |
| 28 SSOP | $\mathrm{A} 28+1$ | $\underline{\underline{21-0056}}$ | $\underline{\underline{90-0095}}$ |
| 28 TSSOP | $\mathrm{U} 28+2$ | $\underline{\underline{21-0066}}$ | $\underline{90-0171}$ |
| 36 TQFN | T3666+3 | $\underline{\underline{21-0141}}$ | $\underline{\underline{90-0050}}$ |

# MAX3224E/MAX3225E/ <br> MAX3226E/MAX3227E/MAX3244E/MAX3245E <br> $\pm 15 k V$ ESD-Protected, $1 \mu A, 1$ Mbps 3.0V to 5.5V, RS-232 Transceivers with AutoShutdown Plus 

Revision History

| REVISION <br> NUMBER | REVISION <br> DATE | DESCRIPTION | PAGES <br> CHANGED |
| :---: | :---: | :--- | :---: |
| 10 | $3 / 11$ | Added an automotive qualified part to the Ordering Information; changed all the <br> parts listed in the Ordering Information to lead free | 1,20 |

maxim
integrated $_{\text {ww }}$

[^1]
[^0]:    AutoShutdown Plus, MegaBaud, and UCSP are trademarks of
    Maxim Integrated Products, Inc.

[^1]:    Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time. The parametric values (min and max limits) shown in the Electrical Characteristics table are guaranteed. Other parametric values quoted in this data sheet are provided for guidance.

