

DIGITAL OUTPUT PHOTO REFLECTOR

■ GENERAL DESCRIPTION

The NJL5809K is thin package digital output type photo reflector, which consist of New JRC original designed one chip photo receiving IC and high output LED.

■ FEATURES

- Normally on type
- With schmitt trigger circuit
- TTL Compatible
- Built-in visible light cut-off filter.
- With pull up resistance

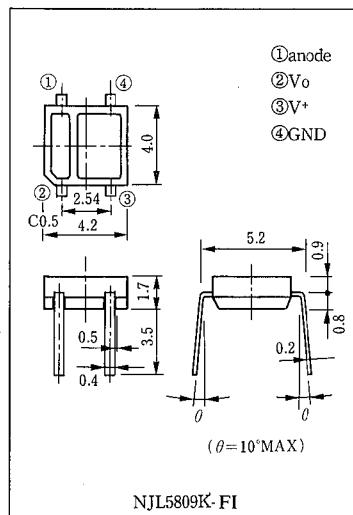
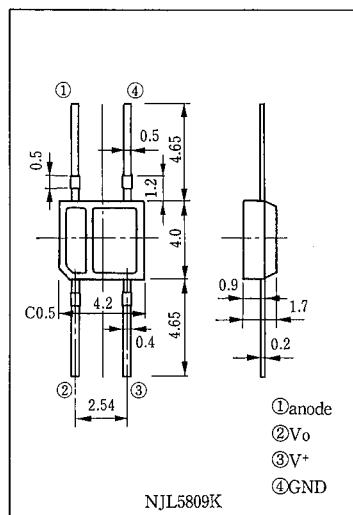
■ APPLICATIONS

- Tape end sensor
- Reel rotation sensor
- Paper detector, Paper end sensor
- Bar code reader
- Sensor of FDD, Robot, manufacturing installation, etc.

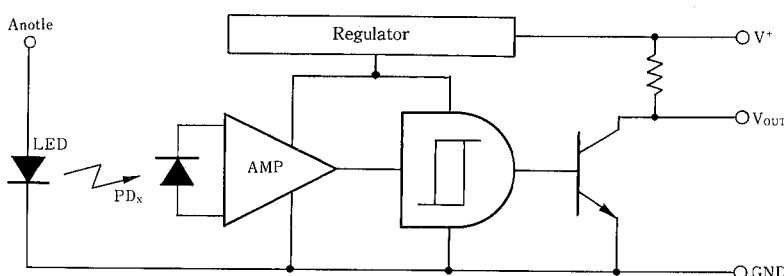
■ ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Emitter			
Forward Current (Continous)	I _F	30	mA
Reverse Voltage (Continous)	V _R	6	V
Power Dissipation	P _D	45	mW
Detector			
Supply Voltage	V ⁺	6	V
High Level Output Voltage	V _{OH}	6	V
Low Level Output Current	I _{OL}	3	mA
Power Dissipation	P _O	55	mW
Coupled			
Total Power Dissipation	P _{tot}	100	mW
Operating Temperature	T _{opr}	-10~+60	°C
Storage Temperature	T _{stg}	-30~+100	°C
Soldering Temperature	T _{sot}	260	°C
		(5sec. 1.5mm from body)	

■ OUTLINE(typ.) Unit : mm



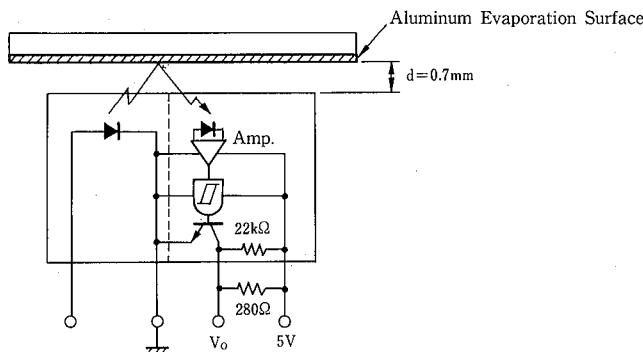
■ BLOCK DIAGRAM



■ ELECTRO-OPTICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$)

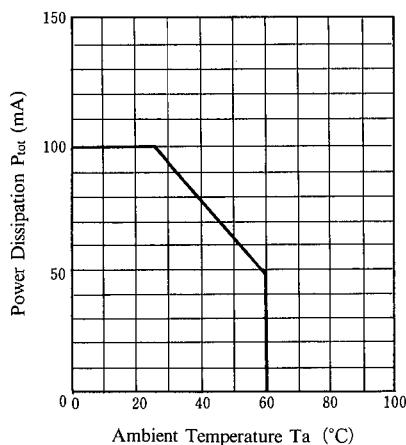
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Emitter						
Forward Voltage	V_F	$I_F=13.5\text{mA}$	—	—	1.4	V
Reverse Current	I_R	$V_R=6\text{V}$	—	—	1.0	μA
Capacitance	C_t	$V_R=0\text{V}, f=1\text{MHz}$	—	25	—	pF
Detector						
Supply Voltage Range	V^+	—	4.5	—	5.5	V
Low Level Output Voltage	V_{OL}	$I_{OL}=3\text{mA}, V^+=5\text{V}, I_F=0\text{mA}, d=0.7\text{mm}$	—	—	0.5	V
High Level Output Voltage	V_{OH}	$V^+=5\text{V}, I_F=13.5\text{mA}, d=0.7\text{mm}$	—	—	4.9	V
Low Level Supply Current	I_{CL}	$V^+=5\text{V}, I_F=0\text{mA}$	—	2	5	mA
High Level Supply Current	I_{CH}	$V^+=5\text{V}, I_F=13.5\text{mA}, D=0.7\text{mm}$	—	2	5	mA
Coupled						
L→H Threshold Input Current	I_{FLH}	$V^+=5\text{V}, d=0.7\text{mm}$	—	8	13.5	mA
Hysteresis	$I_{FH/L}/I_{FLH}$	$V^+=5\text{V}, d=0.7\text{mm}$	—	0.7	—	
L→H Delay Time	t_{pLH}	$V^+=5\text{V}, R_L=280\Omega, I_F=13.5\text{mA}, d=0.7\text{mm}$	—	5	—	μs
H→L Delay Time	t_{pHL}	$V^+=5\text{V}, R_L=280\Omega, I_F=13.5\text{mA}, d=0.7\text{mm}$	—	5	—	μs
Rise time	t_r	$V^+=5\text{V}, R_L=1\text{k}\Omega, I_F=13.5\text{mA}, d=0.7\text{mm}$	—	0.1	—	μs
Fall time	t_f	$V^+=5\text{V}, R_L=1\text{k}\Omega, I_F=13.5\text{mA}, d=0.7\text{mm}$	—	0.1	—	μs

■ MEASURING SPECIFICATION FOR THRESHOLD INPUT CURRENT

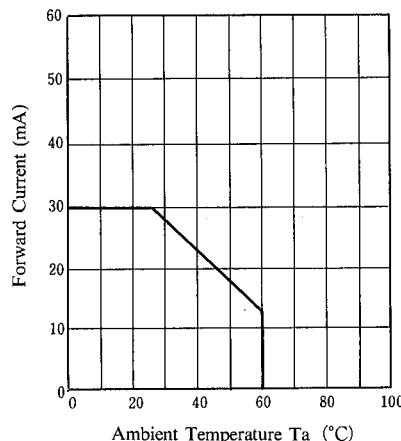


■ MAXIMUM RATING CURVES

Power Dissipation vs. Temperature



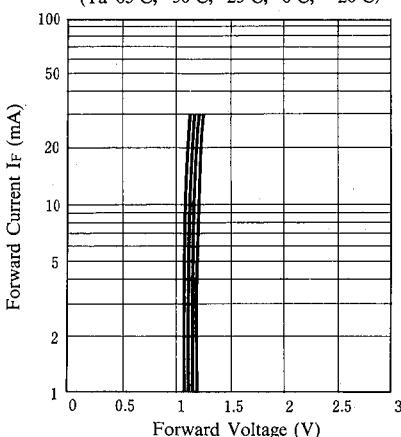
Forward Current vs. Temperature



■ TYPICAL CHARACTERISTICS

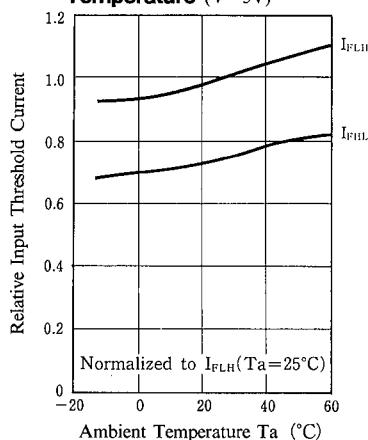
Forward Current vs. Forward Voltage

(Ta=85°C, 50°C, 25°C, 0°C, -20°C)

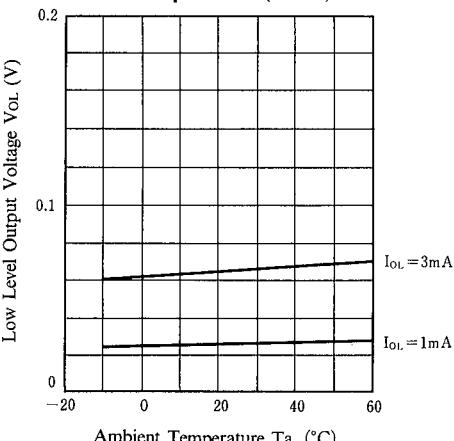


Input Threshold Current vs.

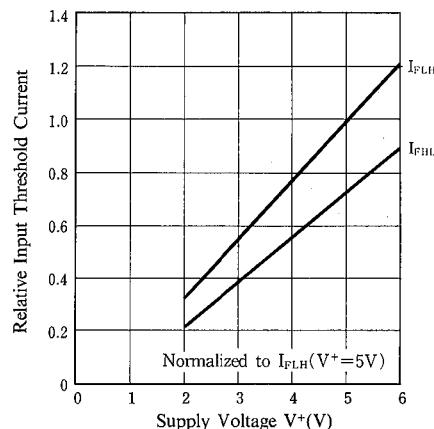
Temperature (V⁺=5V)



Low Level Output Voltage vs. Temperature (V⁺=5V)

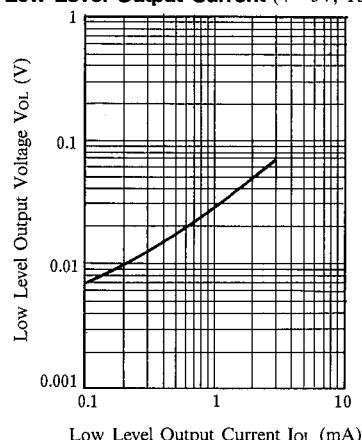


Input Threshold Current vs. Supply Voltage (Ta=25°C)

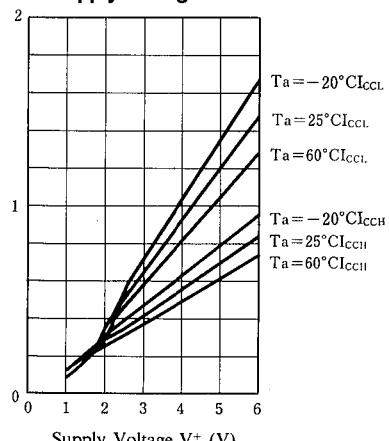


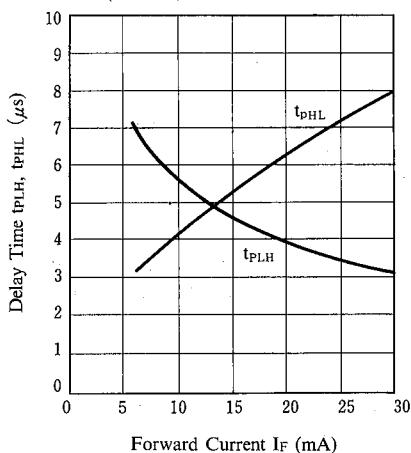
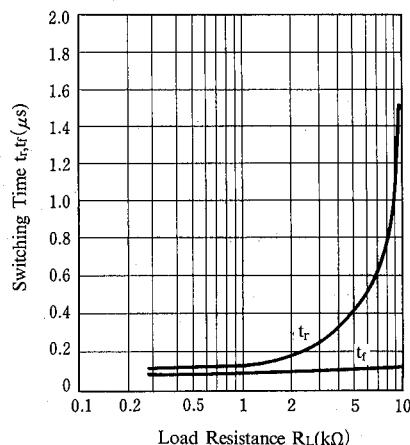
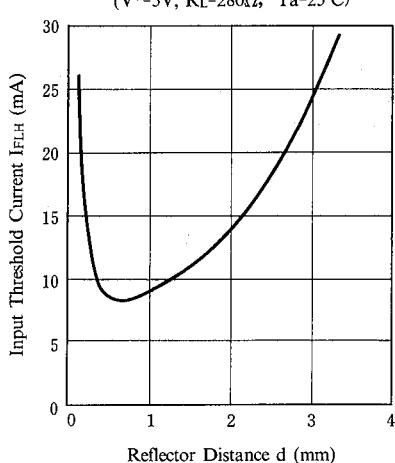
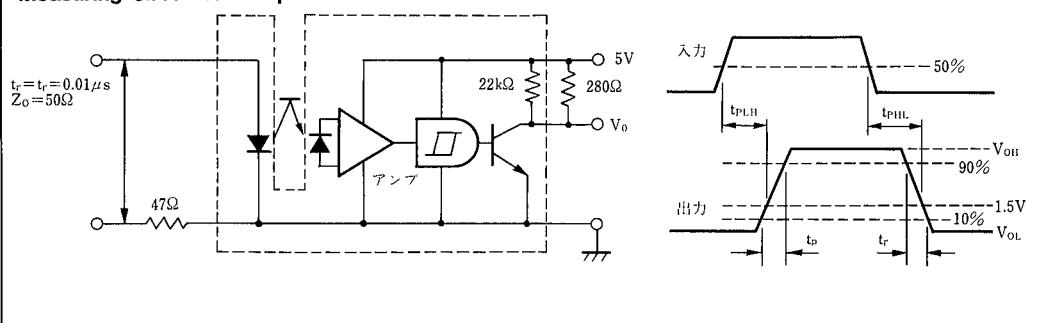
Low Level Output Voltage vs.

Low Level Output Current (V⁺=5V, Ta=25°C)



Supply Current vs. Supply Voltage



Delay Time vs. Forward Current(V⁺=5V, RL=280Ω, Ta=25°C)**Switching Time vs. Load Resistance**(V⁺=5V, IF=13.5mA, Ta=25°C)**Input Threshold Current vs. Distance**(V⁺=5V, RL=280Ω, Ta=25°C)**Measuring Circuit for Response Time**

NJL5809K

MEMO

[CAUTION]

The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.