

100mA / 50V Digital transistor (with built-in resistor)

DTC125TUA / DTC125TKA

Applications

Inverter, Interface, Driver

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on / off conditions need to be set for operation, making the device design easy.
- 4) Higher mounting densities can be achieved.

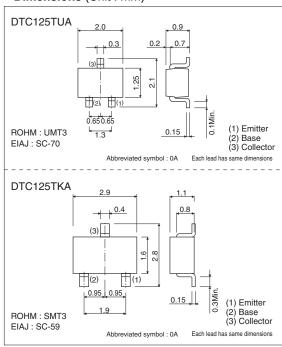
Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

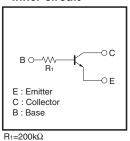
Packaging specifications

	Package	UMT3	SMT3
	Packaging type	Taping	Taping
	T106	T146	
Part No.	Basic ordering unit (pieces)	3000	3000
DTC125TUA		0	-
DTC125TKA		-	0

• Dimensions (Unit : mm)



• Inner circuit



• Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	50	V
Collector-emitter voltage	VCEO	50	V
Emitter-base voltage	VEBO	5	V
Collector current	lc 100		mA
Collector power dissipation	Pc	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

● Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	50	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	5	_	-	V	Iε=50μA
Collector cutoff current	Ісво	-	_	0.5	μΑ	Vcb=50V
Emitter cutoff current	ІЕВО	-	_	0.5	μΑ	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	-	_	0.3	V	Ic=0.5mA , I _B =0.05mA
DC current transfer ratio	hfe	100	250	600	_	Ic=1mA , VcE=5V
Input resistance	R ₁	140	200	260	kΩ	-
Transition frequency	f⊤ *	_	250	_	MHz	Vc=10V , I=-5mA , f=100MHz

^{*} Characteristics of built-in transistor

• Electrical characteristic curves

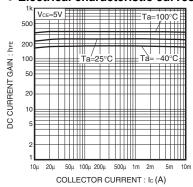


Fig.1 DC current gain vs. Collector current

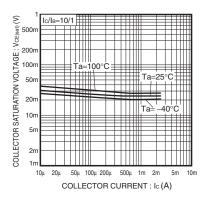


Fig.2 Collector-Emitter saturation voltage vs. Collector current

Notes

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