GeneSiC SEMICONDUCTOR

GB05SLT12-252

Silicon Carbide Power Schottky Diode

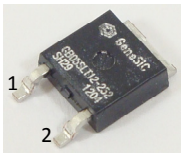
V_{RRM}	=	1200 V
I_F	=	5 A
Q_C	=	35 nC

Features

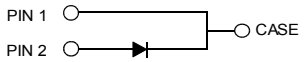
- 1200 V Schottky rectifier
- 175 °C maximum operating temperature
- Temperature independent switching behavior
- Superior surge current capability
- Positive temperature coefficient of V_F
- Extremely fast switching speeds
- Superior figure of merit Q_C/I_F

Package

- RoHS Compliant



TO – 252



Advantages

- Improved circuit efficiency (Lower overall cost)
- Low switching losses
- Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Low reverse recovery current
- Low device capacitance
- Low reverse leakage current at operating temperature

Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Maximum Ratings at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current	I_F	$T_C \le 155$ °C	5	A
RMS forward current	$I_{F(RMS)}$	$T_C \le 155$ °C	8	A
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	T_C = 25 °C, t_P = 10 ms T_C = 155 °C, t_P = 10 ms	32 26	A
Non-repetitive peak forward current	$I_{F,max}$	T_C = 25 °C, t_P = 10 µs	120	A
I^2t value	$\int i^2\,dt$	T_C = 25 °C, t_P = 10 ms T_C = 155 °C, t_P = 10 ms	5 3.4	A^2s
Power dissipation	P_{tot}	T_C = 25 °C	117	W
Operating and storage temperature	T_j , T_{stg}		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions		Values min.	Values typ.	Values max.	Unit
Diode forward voltage	V_F	I_F = 5 A, T_j = 25 °C I_F = 5 A, T_j = 175 °C			1.7 2.7	2.0 3.0	V
Reverse current	I_R	V_R = 1200 V, T_j = 25 °C V_R = 1200 V, T_j = 175 °C			3 6	20 50	µA
Total capacitive charge	Q_C	$I_F \le I_{F,MAX}$ dI_F/dt = 200 A/µs T_j = 175 °C	V_R = 400 V V_R = 960 V		21 35		nC
Switching time	t_s		V_R = 400 V V_R = 960 V		< 25		ns
Total capacitance	C	V_R = 1 V, f = 1 MHz, T_j = 25 °C V_R = 400 V, f = 1 MHz, T_j = 25 °C V_R = 1000 V, f = 1 MHz, T_j = 25 °C			260 25 20		pF

Thermal Characteristics

Thermal resistance, junction - case	R_{thJC}	1.4	°C/W

Mechanical Properties

Mounting torque	M	0.6	Nm

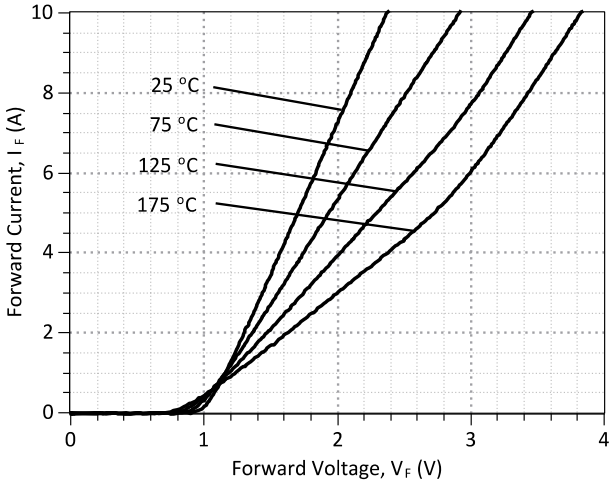


Figure 1: Typical Forward Characteristics

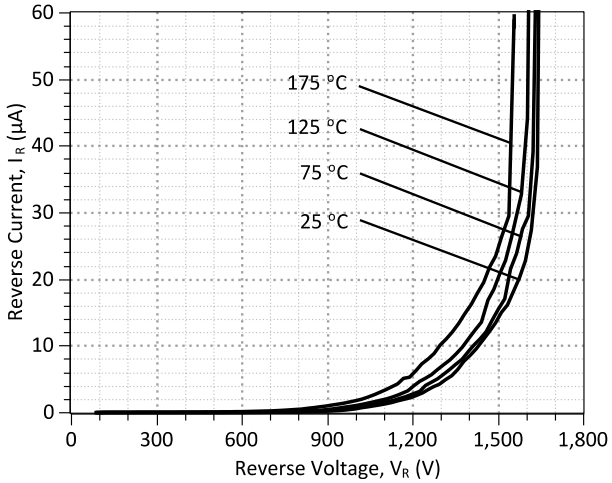


Figure 2: Typical Reverse Characteristics

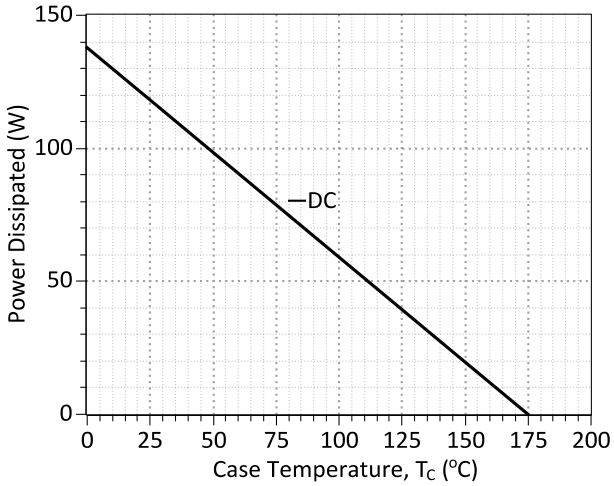


Figure 3: Power Derating Curve

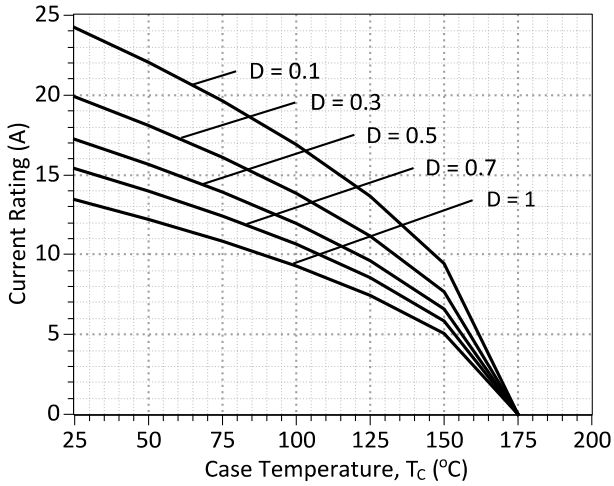


Figure 4: Current Derating Curves (D = t_P/T, t_P= 400 µs) (Considering worst case Z_{th} conditions)

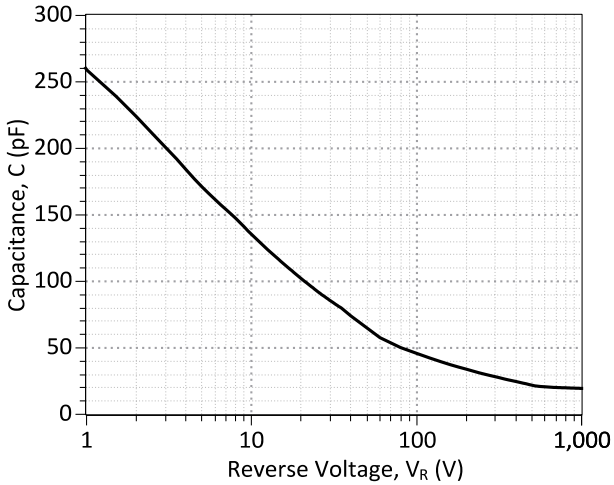


Figure 5: Typical Junction Capacitance vs Reverse Voltage

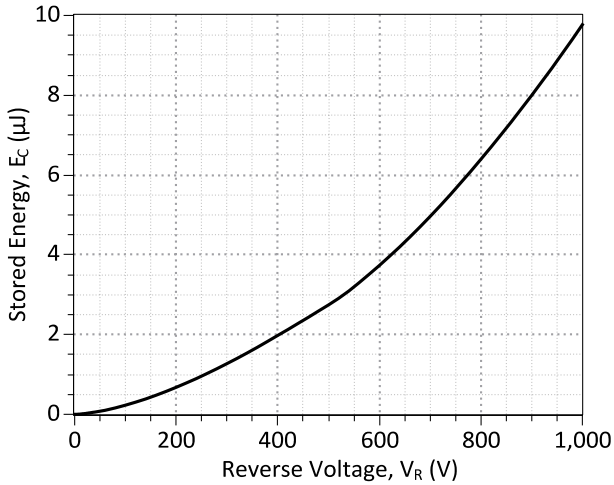


Figure 6: Typical Switching Energy vs Reverse Voltage

Characteristics

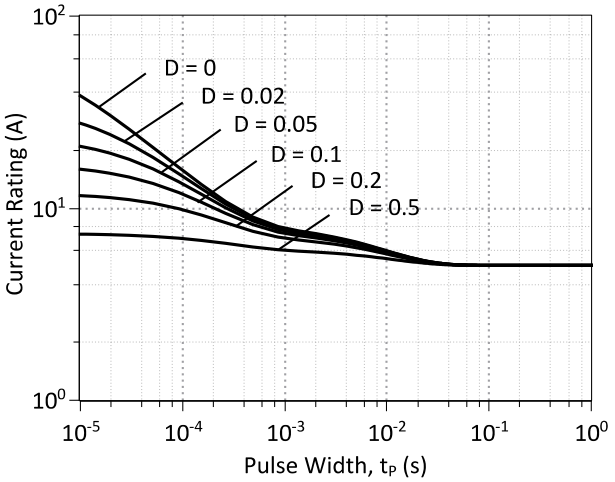


Figure 7: Current vs Pulse Duration Curves at T_C = 155 °C

Characteristics

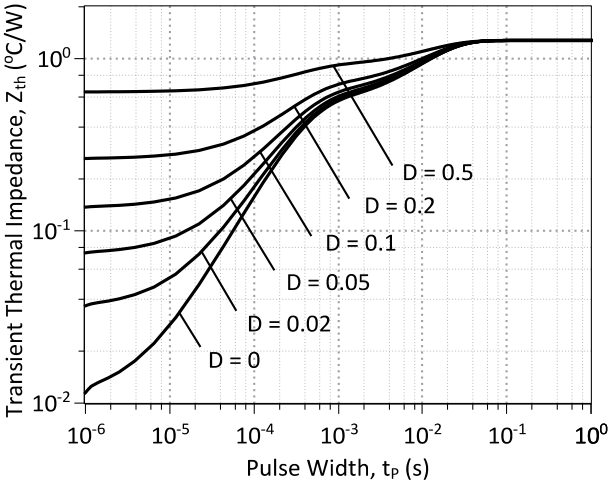
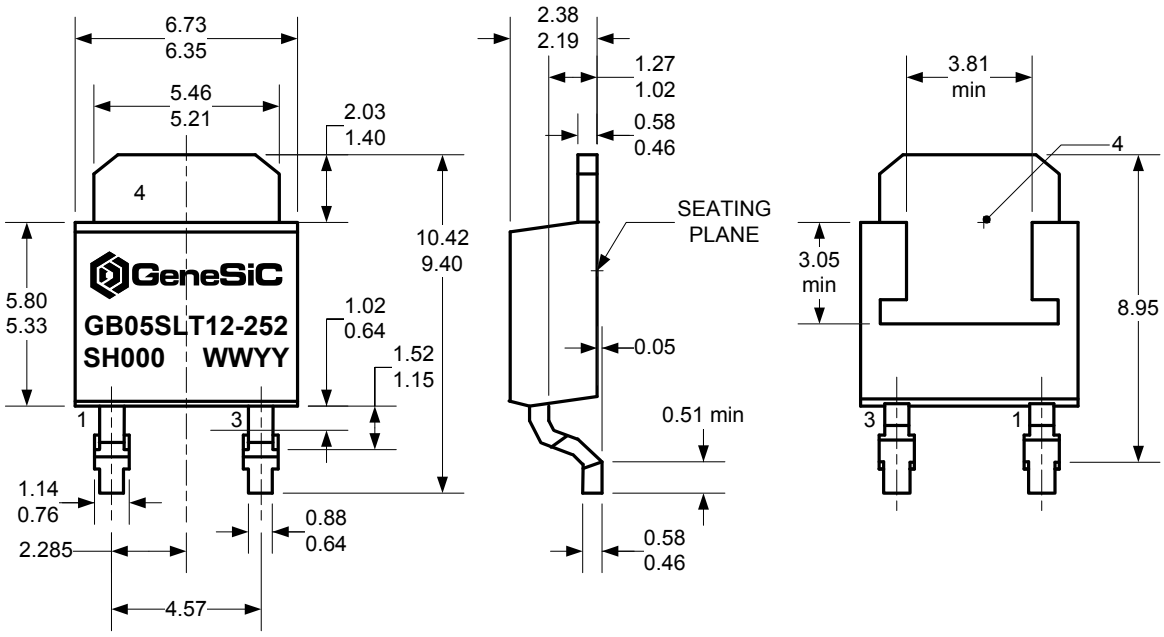


Figure 8: Transient Thermal Impedance

Package Dimensions:

TO-252 **PACKAGE OUTLINE**



NOTE
1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS
3. CONTROLLED LEAD COPLANARITY <D> 0.004 INCH MAXIMUM

Revision History			
Date	Revision	Comments	Supersedes
2012/12/19	2	Second generation update	
2012/05/22	1	Second generation release	
2010/12/14	0	Initial release	

Published by
GeneSiC Semiconductor, Inc.
43670 Trade Center Place Suite 155
Dulles, VA 20166