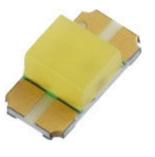
VLMW1300



Vishay Semiconductors

Highbright 0603 ChipLED



DESCRIPTION

The new 0603 ChipLED series have been designed in the smallest SMD package. This innovative 0603 ChipLED technology opens the way to

- smaller products of higher performance
- more design in flexibility
- enhanced applications

The 0603 LED is an obvious solution for small-scale products that are expected to work reliably in an arduous environment.

This package is filled with a mixture of epoxy and yellow converter.

This yellow converter converts the blue emission partially to yellow, which mixes the remaining blue to give white.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD 0603 ChipLED
- Product series: standard
- Angle of half intensity: ± 65°

FEATURES

- High efficient InGaN technology
- Super thin ChipLED with exceptional brightness 1.6 mm x 0.8 mm x 0.55 mm (L x W x H)
- High reliability PCB based
- Temperature range 20 °C to + 80 °C
- Chromaticity coordinate categorized according to CIE 1931 per packing unit
- Typical color temperature 7000 K
- EIA standard package
- Compatible to IR reflow soldering
- Available on 7" diameter reel
- Preconditioning: according to JEDEC level 2a
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- ESD-sensitive device

Note

** Please see document "Vishay Material Category Policy": <u>www.vishay.com/doc?99902</u>

APPLICATIONS

- Telecommunication: indicator and backlighting in telephone and fax
- · Backlighting for audio and video equipment
- Backlighting in office equipment
- Indoor and outdoor message boards
- · Flat backlight for LCDs, switches and symbols

PARTS TABLE												
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F (mA)	WAVELENGTH (nm)		FORWARD VOLTAGE (V)		TECHNOLOGY			
		MIN.	TYP.	MAX.	(IIIA)	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
VLMW1300-GS08	White	45	-	180	5	-	-	-	2.70	-	3.15	InGaN/ yellow converter

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) VLMW1300						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
DC forward current	T _{amb} ≤ 25 °C	I _F	20	mA		
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	А		
Power dissipation		Pv	70	mW		
Operating temperature range		T _{amb}	- 20 to + 80	°C		
Storage temperature range		T _{stg}	- 55 to + 105	°C		
Thermal resistance junction/ambient		R _{thJA}	550	K/W		

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Pb-free



RoHS COMPLIANT GREEN (5-2008)**

VLMW1300



Vishay Semiconductors

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified) VLMW1300, WHITE							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Luminous intensity	I _F = 5 mA	Ι _V	45	-	180	mcd	
Chromatically coordinate x acc. to CIE 1931	I _F = 5 mA	х	-	0.294	-		
Chromatically coordinate y acc. to CIE 1931	I _F = 5 mA	У	-	0.286	-		
Angle of half intensity	I _F = 5 mA	φ	-	± 65	-	deg	
Forward voltage	I _F = 5 mA	V _F	2.70	-	3.15	V	
Reverse current ⁽¹⁾	$V_R = 5 V$	I _R	-	10	-	μΑ	

Note

⁽¹⁾ Driving the LED in reverse direction is suitable for short term application

LUMINOUS INTENSITY CLASSIFICATION					
GROUP	LUMINOUS INTENSITY (mcd) at 5 mA				
	MIN.	MAX.			
Р	45	71			
Q	71	112			
R	112	180			

Note

 Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 15 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

GROUP	FORWARD VOLTAGE (V)				
GROOP	MIN.	MAX.			
A	2.7	2.85			
В	2.85	3.0			
С	3.0	3.13			

Note

• Forward voltage is measured with a tolerance of ± 0.1 V.

CHROMATICITY COORDINATED GROUPS FOR WHITE SMD LED							
	X	Y		X	Y		
	0.274	0.226		0.294	0.286		
S1	0.274	0.258	S4	0.294	0.319		
31	0.294	0.286	54	0.314	0.347		
	0.294	0.254		0.314	0.315		
	0.274 0.258		0.314	0.282			
S2	0.274	0.291	S5	0.314	0.315		
52	0.294	0.319		0.334	0.343		
	0.294	0.286		0.334	0.311		
	0.294	0.254		0.314	0.315		
S3	0.294	0.286	S6	0.314	0.347		
33	0.314	0.315		0.334	0.376		
	0.314	0.282		0.334	0.343		

Note

• Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of ± 0.01.



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

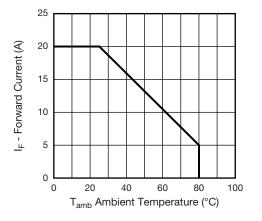


Fig. 1 - Forward Current vs. Ambient Temperature

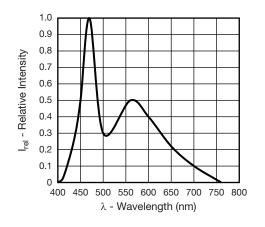


Fig. 2 - Relative Intensity vs. Wavelength

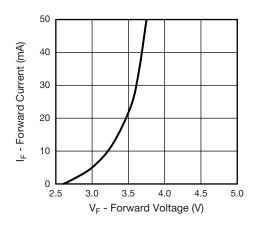


Fig. 3 - Forward Current vs. Forward Voltage

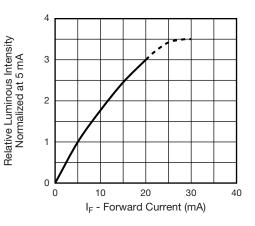


Fig. 4 - Relative Luminous Intensity vs. Forward Current

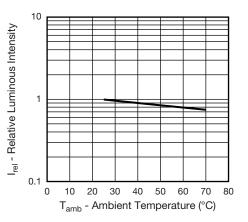


Fig. 5 - Rel. Luminous Intensity vs. Ambient Temperature

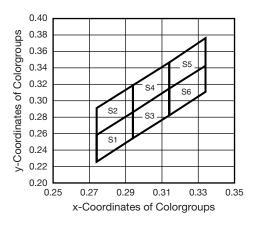


Fig. 6 - Coordinates of Colorgroups

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3 For technical questions, contact: <u>LED@vishay.com</u>

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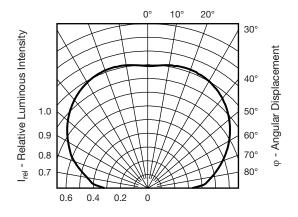
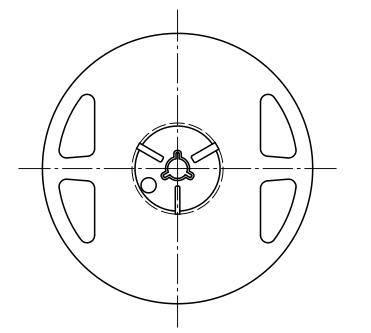
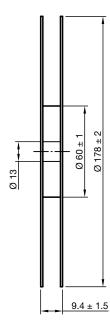
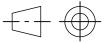


Fig. 7 - Relative Luminous Intensity vs. Angular Displacement

REEL DIMENSIONS in millimeters







technical drawings according to DIN specifications

Drawing-No.: 9.800-5122.01-4 Issue: 2; 03.11.11 22611

4
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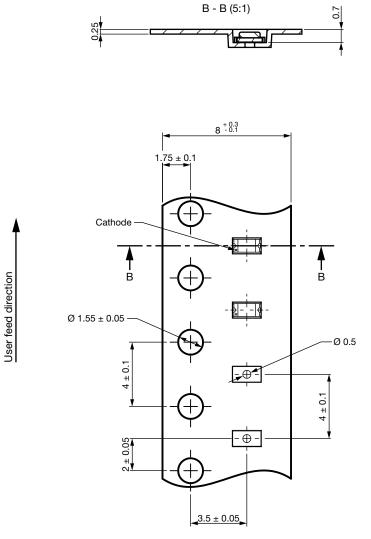
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TAPE DIMENSIONS in millimeters





technical drawings according to DIN specifications

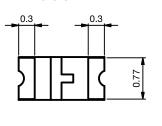
Drawing-No.: 9.700-5385.01-4 Issue: 1; 17.10.11 22612

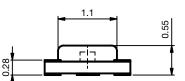
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PACKAGE DIMENSIONS in millimeters

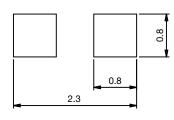
www.vishay.com

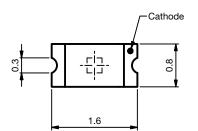
SHA





Recommended solder pad footprint







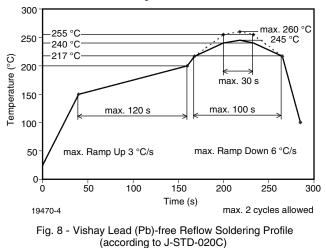


technical drawings according to DIN specifications

Not indicated tolerances ± 0.2

Drawing-No.: 6.541-5091.01-4 Issue: 1; 17.10.11 22613

SOLDERING PROFILE



IR Reflow Soldering Profile for lead (Pb)-free Soldering Preconditioning acc. to JEDEC Level 2

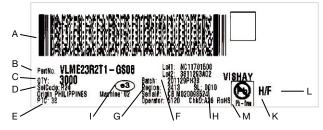
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BAR CODE PRODUCT LABEL (example)

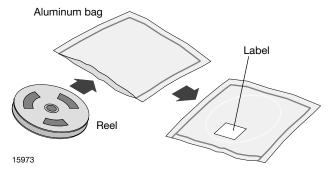


A) 2D barcode

- B) PartNo = Vishay part number
- C) QTY = quantity
- D) SelCode = selection code (binning)
- E) PTC = code of manufacturing plant
- F) Batch = date code: year/week/plant code
- G) Region code
- H) SL = sales location
- I) Terminations finishing
- J) Lead (Pb)-free symbol
- K) Halogen-free symbol
- L) RoHS symbol

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

Vishay Semiconductors

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

or

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen)

11 at 40 C + 5 C - 0 C and < 5 % HT (dry an / nic

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC Standard JESD22-A112 Level 2a label is included on all dry bags.

CAUTION This bag contains MOISTURE -SENSITIVE DEVICES
1. Shelf life in sealed bag 12 months at <40°C and < 90% relative humidity (RH)
 After this bag is opened devices that will be subjected to infrared reflow, vapor-phase reflow, or equivalent processing (peak package body temp. 260°C) must be: Mounted within 672 hours at factory condition of ≤ 30°C/60%RH or Stored at ≤10% RH.
 3. Devices require baking before mounting if: a) Humidity Indicator Card is >10% when read at 23°C ± 5°C or b) 2a or 2b is not met.
4. If baking is required, devices may be baked for:
192 hours at 40°C + 5°C/-0°C and $<5\%$ RH (dry air/nitrogen) or
96 hours at 60±5°Cand <5%RH
Bag Seal Date:
Note: LEVEL defined by EIA JEDEC Standard JESD22-A113

Example of JESD22-A112 Level 2a Label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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