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## 2SK2936

# Silicon N Channel MOS FET High Speed Power Switching

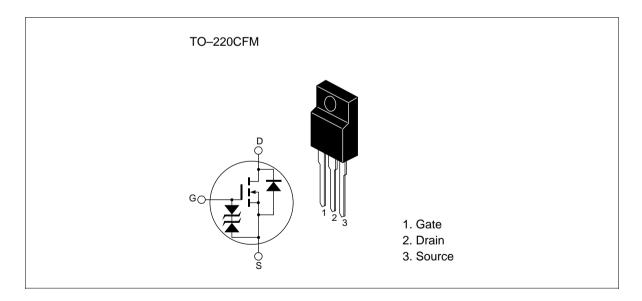


ADE-208-559B (Z) 3rd. Edition Jul. 1998

### **Features**

- Low on-resistance  $R_{DS} = 0.010 \Omega$  typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

## **Outline**



## 2SK2936

## **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	45	A
Drain peak current	Note1	180	A
Body-drain diode reverse drain current	I <sub>DR</sub>	45	A
Avalanche current	I Note3	45	A
Avalanche energy	E <sub>AR</sub> Note3	173	mJ
Channel dissipation	Pch Note2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

- Note: 1. PW  $\leq$  10 $\mu$ s, duty cycle  $\leq$  1 %
  - 2. Value at Tc = 25°C
  - 3. Value at Tch =  $25^{\circ}$ C, Rg  $50\Omega$

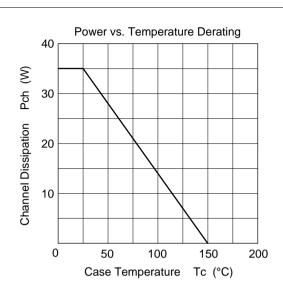
## **Electrical Characteristics** (Ta = 25°C)

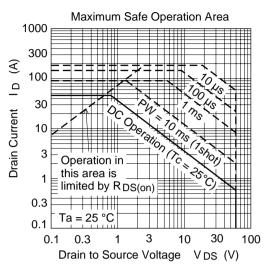
Symbol	Min	Тур	Max	Unit	Test Conditions
$V_{(BR)DSS}$	60	_	_	V	$I_{D} = 10 \text{mA}, V_{GS} = 0$
$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
$V_{GS(off)}$	1.5	_	2.5	V	$I_D = 1$ mA, $V_{DS} = 10$ V
	_	0.010	0.013	Ω	$I_D = 20A$ , $V_{GS} = 10V^{Note4}$
	_	0.015	0.025	Ω	$I_D = 20A$ , $V_{GS} = 4V^{Note4}$
y <sub>fs</sub>	24	40	_	S	$I_D = 20A, V_{DS} = 10V^{Note4}$
Ciss	_	2200	_	pF	V <sub>DS</sub> = 10V
Coss	_	1050	_	pF	$V_{GS} = 0$
Crss	_	320	_	pF	f = 1MHz
t <sub>d(on)</sub>	_	25	_	ns	$I_D = 20A, V_{GS} = 10V$
t <sub>r</sub>	_	200	_	ns	$V_{GS} = 10V, I_{D} = 20A$
t <sub>d(off)</sub>	_	320	_	ns	$R_{L} = 1.5$
t <sub>f</sub>	_	240	_	ns	
$V_{DF}$	_	0.95	_	V	$I_F = 45A, V_{GS} = 0$
t <sub>rr</sub>		60	_	ns	$I_F = 45A, V_{GS} = 0$ diF/ dt =50A/µs
	$\begin{array}{c} V_{(BR)DSS} \\ V_{(BR)GSS} \\ I_{GSS} \\ I_{DSS} \\ \end{array}$ $\begin{array}{c} I_{DSS} \\ V_{GS(off)} \\ R_{DS(on)} \\ I_{Jf_s} \\ \end{array}$ $\begin{array}{c} Ciss \\ Coss \\ Crss \\ t_{d(on)} \\ t_r \\ t_{d(off)} \\ \end{array}$	$\begin{array}{c ccccc} V_{(BR)GSS} & \pm 20 \\ I_{GSS} & - & \\ I_{DSS} & - & \\ V_{GS(off)} & 1.5 \\ R_{DS(on)} & - & \\ R_{DS(on)} & - & \\ I_{DSS} & - & \\ Ciss & - & \\ Coss & - & \\ Crss & - & \\ Crss & - & \\ t_{d(on)} & - & \\ t_r & - & \\ t_{d(off)} & - & \\ t_f & - & \\ V_{DF} & - & \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

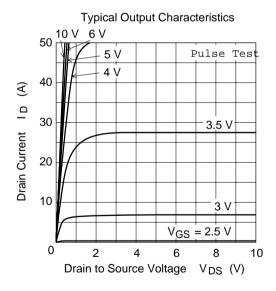
Note: 4. Pulse test

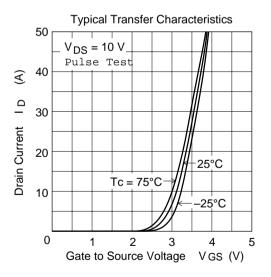
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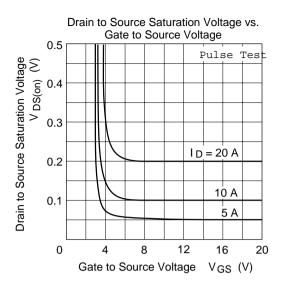
### **Main Characteristics**

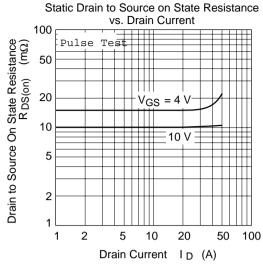


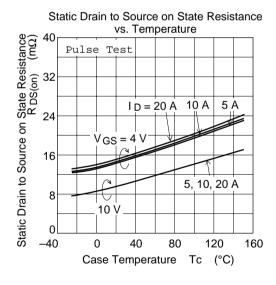


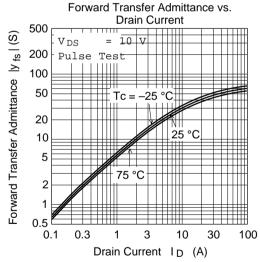


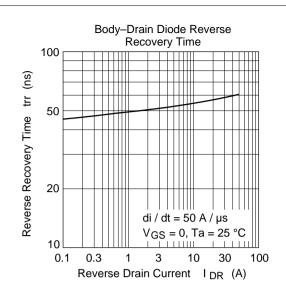


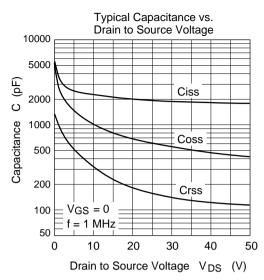


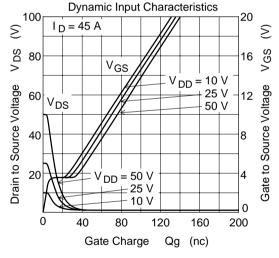


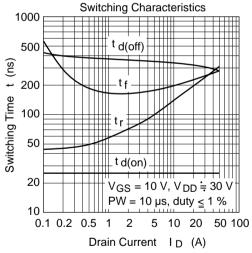


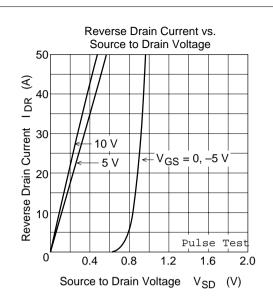


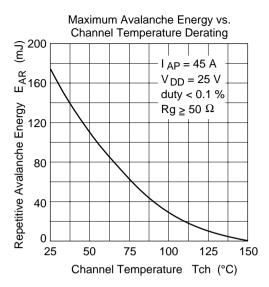






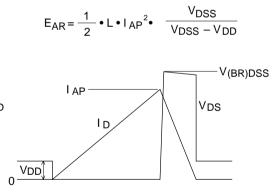




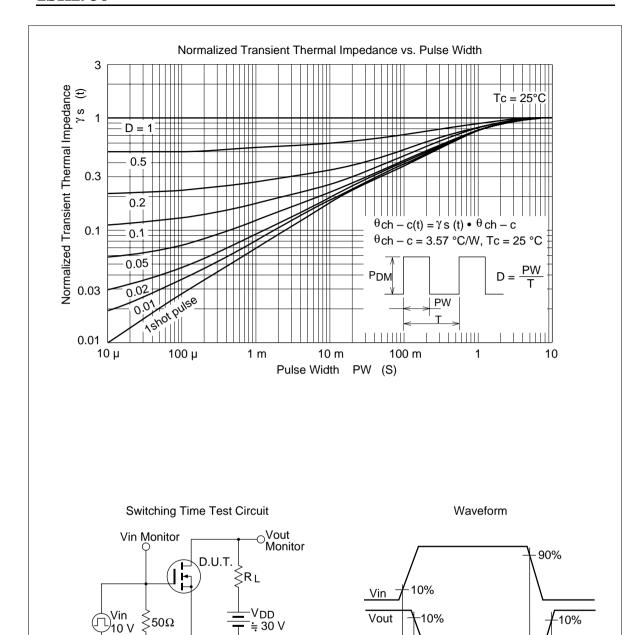


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Avalanche Test Circuit



Avalanche Waveform





-10%

tf

90%

td(off)

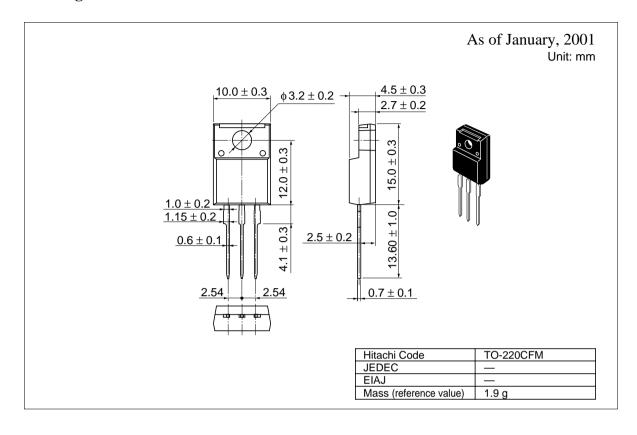
90%

tr

td(on)

**\$50Ω** 

## **Package Dimensions**



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