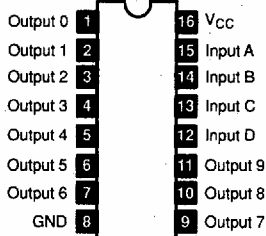


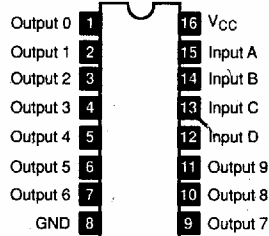
# INTEGRATED CIRCUITS – TTL

## (TRANSISTOR TRANSISTOR LOGIC)

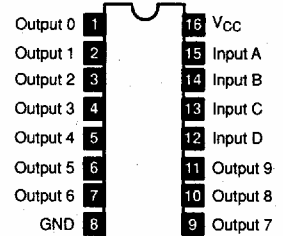
**NTE7442, NTE74C42, NTE74LS42** 16-Lead DIP, See Diag. 249  
4-Line-to-10-Line BCD-to-Decimal Decoder



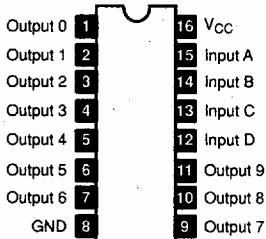
**NTE7443** 16-Lead DIP, See Diag. 249  
4-Line-to-10-Line Excess 3-to-Decimal Decoder



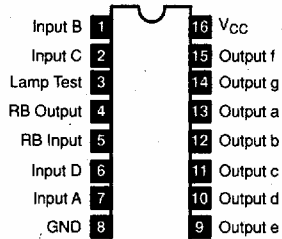
**NTE7444** 16-Lead DIP, See Diag. 249  
4-Line-to-10-Line Excess 3-Gray-to-Decimal Decoder



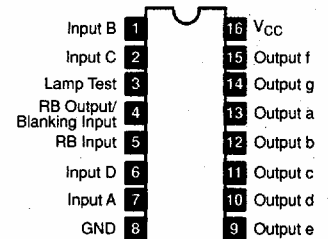
**NTE7445** 16-Lead DIP, See Diag. 249  
BCD-to-Decimal Decoder/Driver



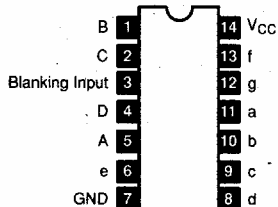
**NTE7446, NTE7447, NTE74LS47** 16-Lead DIP, See Diag. 249  
BCD-to-Seven-Segment Decoder/Driver  
\*/Active Low, Open Collector Outputs



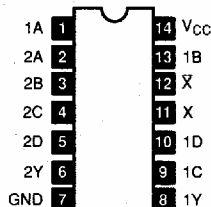
**NTE7448, NTE74C48, NTE74LS48** 16-Lead DIP, See Diag. 249  
BCD-to-Seven-Segment Decoder/Driver  
\*/Internal Pull-Up Outputs



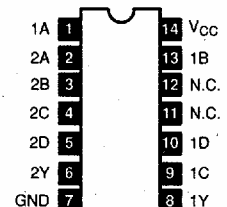
**NTE74LS49** 14-Lead DIP, See Diag. 247  
BCD-to-Seven-Segment Decoder/Driver  
\*/Open Collector Outputs



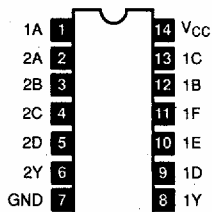
**NTE7450, NTE74H50** 14-Lead DIP, See Diag. 247  
Dual 2-Wide 2-Input AND/OR Invert Gate  
(One Gate Expandable)



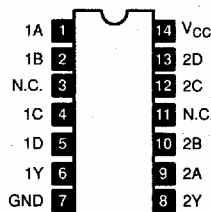
**NTE7451, NTE74H51, NTE74S51** 14-Lead DIP, See Diag. 247  
Dual 2-Wide 2-Input AND/OR Gate



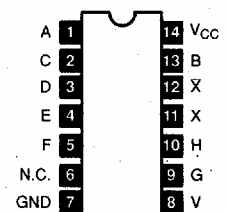
**NTE74LS51** 14-Lead DIP, See Diag. 247  
Dual 2-Wide 2-Input AND/OR Gate



**NTE74H52** 14-Lead DIP, See Diag. 247  
Expandable 4-Wide AND/OR Gate



**NTE7453** 14-Lead DIP, See Diag. 247  
Expandable AND/OR Invert Gate



See Diagrams, beginning on Page 1-293