

September 2012

FDMC86520DC

N-Channel Dual CoolTM PowerTrench[®] MOSFET 60 V, 40 A, 6.3 m Ω

Features

- Dual CoolTM Top Side Cooling PQFN package
- Max $r_{DS(on)} = 6.3 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 17 \text{ A}$
- Max $r_{DS(on)} = 8.7 \text{ m}\Omega$ at $V_{GS} = 8 \text{ V}$, $I_D = 14.5 \text{ A}$
- High performance technology for extremely low r_{DS(on)}
- RoHS Compliant

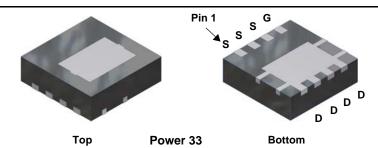
General Description

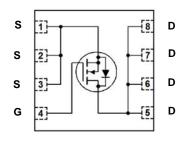
This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process. Advancements in both silicon and Dual Cool TM package technologies have been combined to offer the lowest $r_{DS(on)}$ while maintaining excellent switching performance by extremely low Junction-to-Ambient thermal resistance.

Applications

- Primary DC-DC Switch
- Motor Bridge Switch
- Synchronous Rectifier







MOSFET Maximum Ratings TA= 25 °C unless otherwise noted

Symbol	Parame	eter		Ratings	Units
V _{DS}	Drain to Source Voltage			60	V
V _{GS}	Gate to Source Voltage			±20	V
	Drain Current -Continuous	T _C = 25 °C		40	
I _D	-Continuous	T _A = 25 °C	(Note 1a)	17	Α
	-Pulsed			80	
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	128	mJ
D	Power Dissipation	T _C = 25 °C		73	W
P_{D}	Power Dissipation	T _A = 25 °C	(Note 1a)	3.0	VV
T _J , T _{STG}	Operating and Storage Junction Tempera	ture Range		-55 to + 150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Top Source)	4.2	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Bottom Drain)	1.7	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	42	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	105	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1i)	17	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1j)	26	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1k)	12	

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
86520	FDMC86520DC	Dual Cool TM Power 33	13"	12 mm	3000 units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	cteristics					
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25 °C		30		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48 V, V _{GS} = 0 V			1	μΑ
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	2.5	3.7	4.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25 °C		-10		mV/°C
		V _{GS} = 10 V, I _D = 17 A		5.1	6.3	
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 8 \text{ V}, I_D = 14.5 \text{ A}$		6.5	8.7	mΩ
	$V_{GS} = 10 \text{ V}, I_D = 17 \text{ A}, T_J = 125^{\circ}\text{C}$		8.2	10.2		
9 _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 17 A		49		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 20 V V 0 V		2097	2790	pF
C _{oss}	Output Capacitance	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$		557	745	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1/11/2		13	40	pF
R_g	Gate Resistance		0.1	0.5	2.5	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		18	33	ns
t _r	Rise Time	V _{DD} = 30 V, I _D = 17 A,	6.6	14	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$	19	35	ns
t _f	Fall Time		4	10	ns
Q_{g}	Total Gate Charge	V _{GS} = 0 V to 10 V	29	40	nC
Q_{g}	Total Gate Charge	$V_{GS} = 0 \ V \text{ to 8 V} \ V_{DD} = 30 \ V,$	23	33	nC
Q _{gs}	Gate to Source Charge	I _D = 17 A	12		nC
Q_{qd}	Gate to Drain "Miller" Charge		5.5		nC

Drain-Source Diode Characteristics

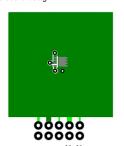
V _{SD} Source-Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V, } I_S = 17 \text{ A}$ (Note 2)		0.83	1.3	\/	
V _{SD}	Source-Drain Diode Porward Voltage	$V_{GS} = 0 \text{ V, } I_S = 2.5 \text{ A}$ (Note 2)		0.74	1.2	V
t _{rr}	Reverse Recovery Time	I _F = 17 A, di/dt = 100 A/μs		41	65	ns
Q _{rr}	Reverse Recovery Charge	- I _F = 17 A, αι/αι = 100 A/μs		23	37	nC

Thermal Characteristics

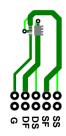
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Top Source)	4.2	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Bottom Drain)	1.7	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	42	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	105	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1c)	29	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1d)	40	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1e)	19	90044
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1f)	23	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1g)	30	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1h)	79	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1i)	17	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1j)	26	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1k)	12	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1I)	16	

NOTES:

1. R_{0JA} is determined with the device mounted on a FR-4 board using a specified pad of 2 oz copper as shown below. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



a. 42 °C/W when mounted on a 1 in² pad of 2 oz copper



b. 105 °C/W when mounted on a minimum pad of 2 oz copper

- c. Still air, 20.9x10.4x12.7mm Aluminum Heat Sink, 1 in² pad of 2 oz copper
- d. Still air, 20.9x10.4x12.7mm Aluminum Heat Sink, minimum pad of 2 oz copper
- e. Still air, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, 1 in² pad of 2 oz copper
- f. Still air, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, minimum pad of 2 oz copper
- g. 200FPM Airflow, No Heat Sink,1 in² pad of 2 oz copper
- h. 200FPM Airflow, No Heat Sink, minimum pad of 2 oz copper
- i. 200FPM Airflow, 20.9x10.4x12.7mm Aluminum Heat Sink, 1 \mbox{in}^2 pad of 2 oz copper
- j. 200FPM Airflow, 20.9x10.4x12.7mm Aluminum Heat Sink, minimum pad of 2 oz copper
- k. 200FPM Airflow, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, 1 in² pad of 2 oz copper
- I. 200FPM Airflow, 45.2x41.4x11.7mm Aavid Thermalloy Part # 10-L41B-11 Heat Sink, minimum pad of 2 oz copper
- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.
- 3. E_{AS} of 128 mJ is based on starting T_J = 25 $^{\circ}$ C, L = 1 mH, I_{AS} = 16 A, V_{DD} = 54 V, V_{GS} = 10 V. 100% test at L = 0.3 mH, I_{AS} = 24 A.

Typical Characteristics T_J = 25 °C unless otherwise noted

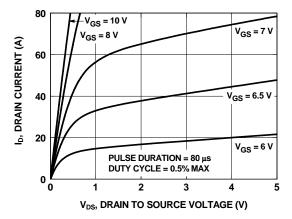


Figure 1. On-Region Characteristics

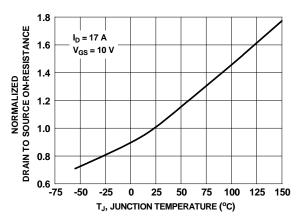


Figure 3. Normalized On-Resistance vs Junction Temperature

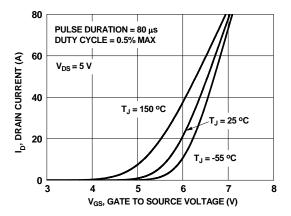


Figure 5. Transfer Characteristics

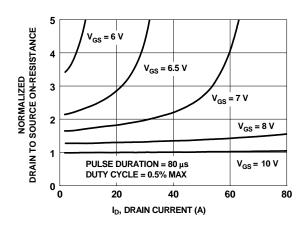


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

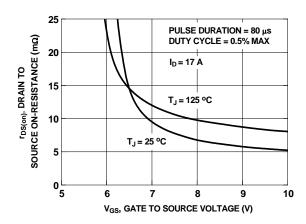


Figure 4. On-Resistance vs Gate to Source Voltage

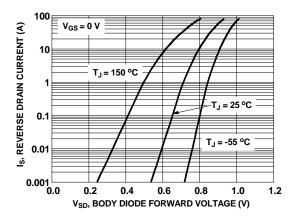


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics $T_J = 25$ °C unless otherwise noted

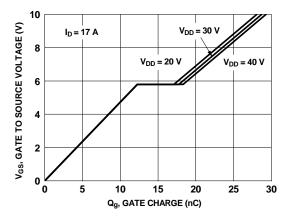


Figure 7. Gate Charge Characteristics

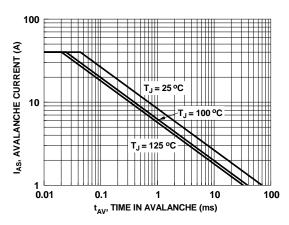


Figure 9. Unclamped Inductive Switching Capability

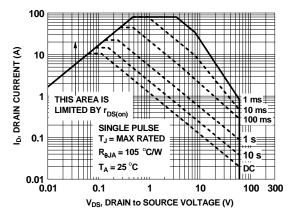


Figure 11. Forward Bias Safe Operating Area

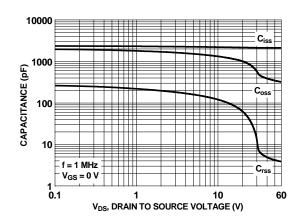


Figure 8. Capacitance vs Drain to Source Voltage

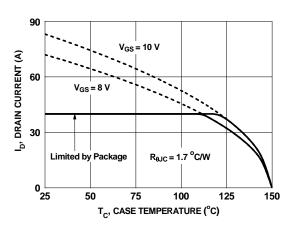


Figure 10. Maximum Continuous Drain Current vs Case Temperature

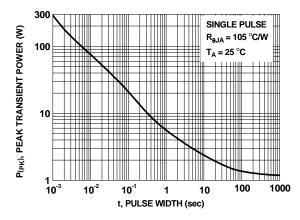


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25 °C unless otherwise noted

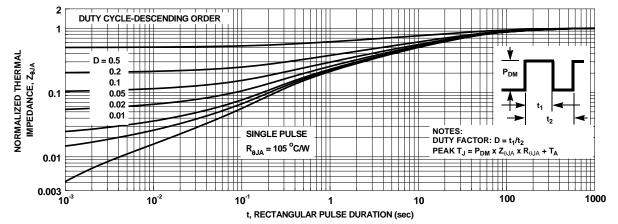
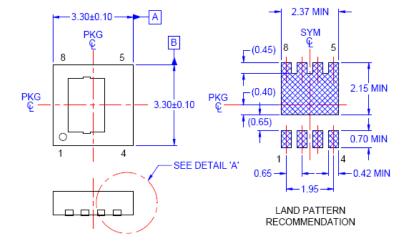
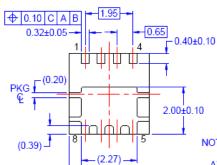


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Dimensional Outline and Pad Layout





NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC MO-240, ISSUE A, VAR. BA, DATED OCTOBER 2002.

 B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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