# 10 kPa On-Chip Temperature Compensated & Calibrated Silicon Pressure Sensors

The MPXM2010 device is a silicon piezoresistive pressure sensors providing a highly accurate and linear voltage output — directly proportional to the applied pressure. The sensor is a single, monolithic silicon diaphragm with the strain gauge and a thin-film resistor network integrated on-chip. The chip is laser trimmed for precise span and offset calibration and temperature compensation.

## **Features**

- Temperature Compensated Over 0°C to +85°C
- Available in Easy-to-Use Tape & Reel
- Ratiometric to Supply Voltage
- Gauge Ported & Non Ported Options

# **Application Examples**

- Respiratory Diagnostics
- · Air Movement Control
- Controllers
- · Pressure Switching

Figure 1 shows a block diagram of the internal circuitry on the stand-alone pressure sensor chip.

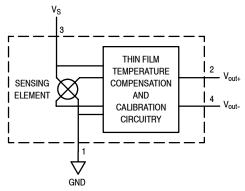


Figure 1. Temperature Compensated Pressure Sensor Schematic

# **VOLTAGE OUTPUT versus APPLIED DIFFERENTIAL PRESSURE**

The differential voltage output of the sensor is directly proportional to the differential pressure applied.

The output voltage of the differential or gauge sensor increases with increasing pressure applied to the pressure side (P1) relative to the vacuum side (P2). Similarly, output voltage increases as increasing vacuum is applied to the vacuum side (P2) relative to the pressure side (P1).

Preferred devices are Freescale Semiconductor recommended choices for future use and best overall value.

# MPXM2010 SERIES

Freescale Semiconductor Preferred Device 0 to 10 kPa (0 to 1.45 psi) 25 mV FULL SCALE SPAN (TYPICAL)

## **MPAK PACKAGE**



SCALE 1:1

MPXM2010D/DT1 CASE 1320



SCALE 1:1

MPXM2010GS/GST1 CASE 1320A

| PIN NUMBER |                   |   |                   |  |  |  |
|------------|-------------------|---|-------------------|--|--|--|
| 1          | Gnd               | 3 | $V_S$             |  |  |  |
| 2          | +V <sub>out</sub> | 4 | -V <sub>out</sub> |  |  |  |



# MAXIMUM RATINGS(NOTE)

| Rating                     | Symbol           | Value       | Unit |  |
|----------------------------|------------------|-------------|------|--|
| Maximum Pressure (P1 > P2) | P <sub>max</sub> | 75          | kPa  |  |
| Storage Temperature        | T <sub>stg</sub> | -40 to +125 | °C   |  |
| Operating Temperature      | T <sub>A</sub>   | -40 to +125 | °C   |  |

NOTE: Exposure beyond the specified limits may cause permanent damage or degradation to the device.

# OPERATING CHARACTERISTICS (V<sub>S</sub> = 10 Vdc, T<sub>A</sub> = 25°C unless otherwise noted, P1 > P2)

| Characteristic  | Symbol             | Min  | Тур  | Max  | Unit              |
|---|--------------------|------|------|------|-------------------|
| Pressure Range <sup>(1)</sup>                           | P <sub>OP</sub>    | 0    | _    | 10   | kPa               |
| Supply Voltage <sup>(2)</sup>                           | Vs                 | _    | 10   | 16   | Vdc               |
| Supply Current  | Io                 | _    | 6.0  | _    | mAdc              |
| Full Scale Span <sup>(3)</sup>                          | V <sub>FSS</sub>   | 24   | 25   | 26   | mV                |
| Offset <sup>(4)</sup>                                   | V <sub>off</sub>   | -1.0 | =    | 1.0  | mV                |
| Sensitivity   | ΔV/ΔΡ              | _    | 2.5  | _    | mV/kPa            |
| Linearity <sup>(5)</sup>                                | _                  | -1.0 | _    | 1.0  | %V <sub>FSS</sub> |
| Pressure Hysteresis <sup>(5)</sup> (0 to 10 kPa)        | _                  | _    | ±0.1 | _    | %V <sub>FSS</sub> |
| Temperature Hysteresis <sup>(5)</sup> (-40°C to +125°C) |                    | _    | ±0.5 | _    | %V <sub>FSS</sub> |
| Temperature Effect on Full Scale Span <sup>(5)</sup>    | TCV <sub>FSS</sub> | -1.0 | _    | 1.0  | %V <sub>FSS</sub> |
| Temperature Effect on Offset <sup>(5)</sup>             | TCV <sub>off</sub> | -1.0 | _    | 1.0  | mV                |
| Input Impedance   | Z <sub>in</sub>    | 1000 | _    | 2550 | Ω                 |
| Output Impedance  | Z <sub>out</sub>   | 1400 | _    | 3000 | Ω                 |
| Response Time <sup>(6)</sup> (10% to 90%)               | t <sub>R</sub>     | _    | 1.0  | _    | ms                |
| Warm-Up   | _                  | _    | 20   | _    | ms                |
| Offset Stability <sup>(7)</sup>                         | _                  | _    | ±0.5 | _    | %V <sub>FSS</sub> |

# NOTES:

- 1. 1.0 kPa (kiloPascal) equals 0.145 psi.
- 2. Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- 3. Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 4. Offset  $(V_{\text{off}})$  is defined as the output voltage at the minimum rated pressure.
- 5. Accuracy (error budget) consists of the following:
  - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
  - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure
  - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
  - TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.
     TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- 6. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 7. Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

## **LINEARITY**

Linearity refers to how well a transducer's output follows the equation:  $V_{out} = V_{off} + \text{sensitivity } \times P$  over the operating pressure range. There are two basic methods for calculating nonlinearity: (1) end point straight line fit (see Figure 2) or (2) a least squares best line fit. While a least squares fit gives the "best case" linearity error (lower numerical value), the calculations required are burdensome.

Conversely, an end point fit will give the "worst case" error (often more desirable in error budget calculations) and the calculations are more straightforward for the user. Freescale Semiconductor's specified pressure sensor linearities are based on the end point straight line method measured at the midrange pressure.

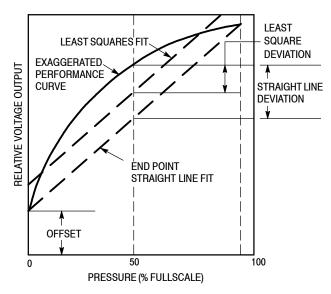


Figure 2. Linearity Specification Comparison

# **ON-CHIP TEMPERATURE COMPENSATION and CALIBRATION**

Figure 3 shows the minimum, maximum and typical output characteristics of the MPXM2010 series at 25°C. The output is directly proportional to the differential pressure and is essentially a straight line.

A silicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm.

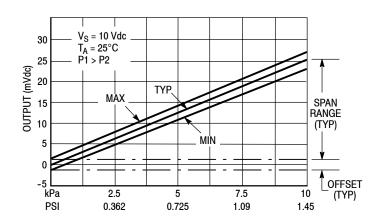
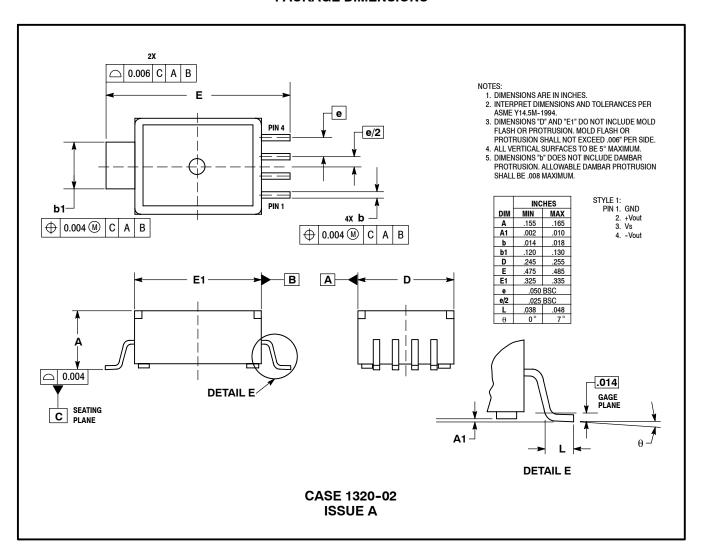
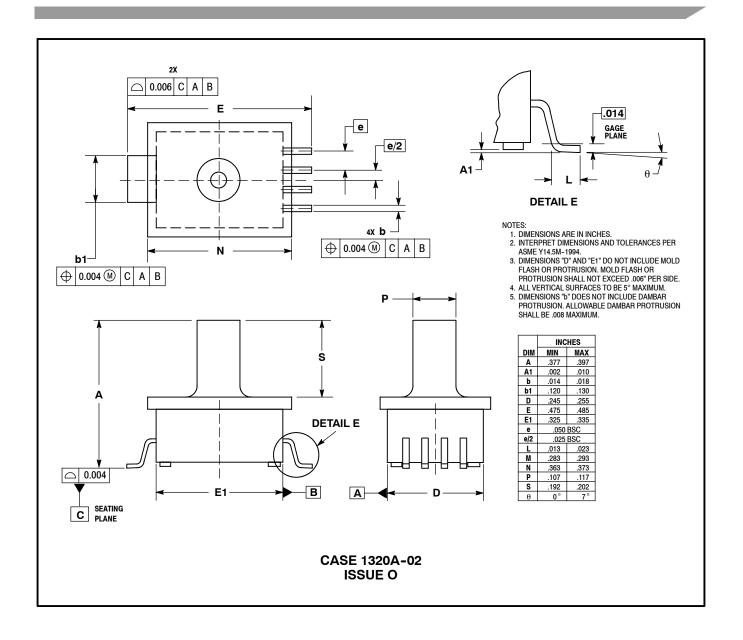


Figure 3. Output versus Pressure Differential

| ORDERING INFORMATION |                           |          |  |  |  |  |
|----------------------|---------------------------|----------|--|--|--|--|
| Device Type Options  |                           | Case No. |  |  |  |  |
| MPXM2010D            | Non-ported                | 1320     |  |  |  |  |
| MPXM2010DT1          | Non-ported, Tape and Reel | 1320     |  |  |  |  |
| MPXM2010GS           | Ported                    | 1320A    |  |  |  |  |
| MPXM2010GST1         | Ported, Tape and Reel     | 1320A    |  |  |  |  |

# **PACKAGE DIMENSIONS**





# **NOTES**

# **NOTES**

## How to Reach Us:

## **Home Page:**

www.freescale.com

#### E-mail:

support@freescale.com

## **USA/Europe or Locations Not Listed:**

Freescale Semiconductor Technical Information Center, CH370 1300 N. Alma School Road Chandler, Arizona 85224 +1-800-521-6274 or +1-480-768-2130 support@freescale.com

# Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) support@freescale.com

### Japan:

Freescale Semiconductor Japan Ltd. Technical Information Center 3-20-1, Minami-Azabu, Minato-ku Tokyo 106-0047, Japan 0120 191014 or +81 3 3440 3569 support.japan@freescale.com

### Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd. Technical Information Center 2 Dai King Street Tai Po Industrial Estate Tai Po, N.T., Hong Kong +800 2666 8080 support.asia@freescale.com

# For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217

1-800-441-2447 or 303-675-2140

Fax: 303-675-2150

LDCF or Free scale Semiconductor @hibbert group.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale ™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners. © Freescale Semiconductor, Inc. 2004. All rights reserved.

