

Pb

Micro Commercial Components

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20736 Marilla Street Chatsworth

CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 DTC114ECA

Features

- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy

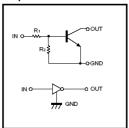
Absolute maximum ratings @ 25°

Symbol	Parameter	Min	Тур	Max	Unit
V _{CC}	Supply voltage		50		V
V_{IN}	Input voltage	-10		40	V
Io	Output current		50	100	mA
P_d	Power dissipation		200		mW
T _j	Junction temperature		150		$^{\circ}\mathbb{C}$
T _{stg}	Storage temperature	-55		150	$^{\circ}\mathbb{C}$

Electrical Characteristics @ 25°C

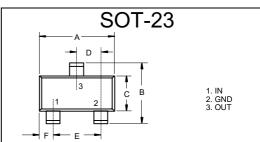
Symbol	Parameter	Min	Тур	Max	Unit
$V_{I(off)}$	Input voltage (V _{CC} =5V, I _O =100 µ A)			0.5	V
$V_{I(on)}$	$(V_0=0.3V, I_0=10mA)$	3.0			V
$V_{O(on)}$	Output voltage (I _O /I _I =10mA/0.5mA)		0.1	0.3	V
l _l	Input current (V _I =5V)			0.88	mA
I _{O(off)}	Output current (V _{CC} =50V, V _I =0)			0.5	μА
Gi	DC current gain (V ₀ =5V, I ₀ =5mA)	30			
R ₁	Input resistance	7.0	10	13	$\mathbf{K}\Omega$
R_2/R_1	Resistance ratio	0.8	1.0	1.2	
f⊤	Transition frequency $(V_{CE}=10V, I_{E}=5mA, f=100MHz)$		250		MHz

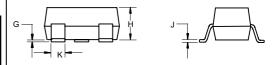
●Equivalent circuit



MARKING: 24

NPN Digital Transistors

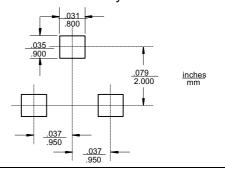




DIMENSIONS

NCHES MM NOTE	BINIZITOIONO					
A .110 .120 2.80 3.04 B .083 .098 2.10 2.64 C .047 .055 1.20 1.40 D .035 .041 .89 1.03 E .070 .081 1.78 2.05 F .018 .024 .45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180		INCH	ES	MM		
B .083 .098 2.10 2.64 C .047 .055 1.20 1.40 D .035 .041 .89 1.03 E .070 .081 1.78 2.05 F .018 .024 .45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	DIM	MIN	MAX	MIN	MIN MAX	
C .047 .055 1.20 1.40 D .035 .041 .89 1.03 E .070 .081 1.78 2.05 F .018 .024 .45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	Α	.110	.120	2.80	3.04	
D .035 .041 .89 1.03 E .070 .081 1.78 2.05 F .018 .024 .45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	В	.083	.098	2.10	2.64	
E .070 .081 1.78 2.05 F .018 .024 45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	С	.047	.055	1.20	1.40	
F .018 .024 .45 .60 G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	D	.035	.041	.89	1.03	
G .0005 .0039 .013 .100 H .035 .044 .89 1.12 J .003 .007 .085 .180	Е	.070	.081	1.78	2.05	
H .035 .044 .89 1.12 J .003 .007 .085 .180	F	.018	.024	.45	.60	
J .003 .007 .085 .180	G	.0005	.0039	.013	.100	
	Н	.035	.044	.89	1.12	
K .015 .020 .37 .51	J	.003	.007	.085	.180	
	K	.015	.020	.37	.51	

Suggested Solder Pad Layout



DTC114ECA



Electrical characteristic curves

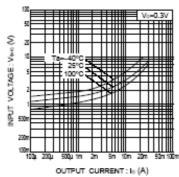


Fig.1 Input voltage vs. output current (ON characteristics)

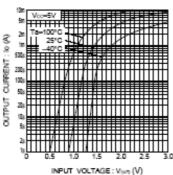


Fig.2 Output current vs. input voltage (OFF characteristics)

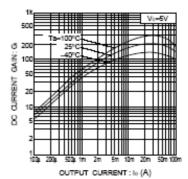


Fig.3 DC current gain vs. output current

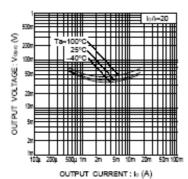


Fig.4 Output voltage vs. output current



Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

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