

# 2SC3508

Silicon NPN Triple-Diffused Planar Darlington Type

High Breakdown Voltage, High Speed Switching

### Features

- High speed switching
- High collector-base voltage ( $V_{CBO}$ )
- Good linearity of DC current gain ( $h_{FE}$ )
- "Full Pack" package for simplified mounting on a heat sink with one screw

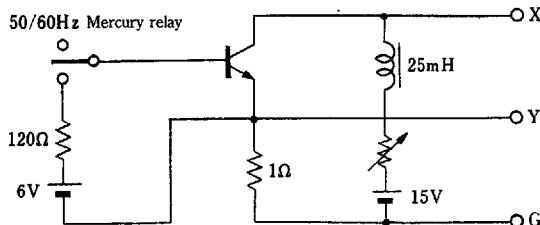
### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ )

Item	Symbol	Value	Unit
Collector-base voltage	$V_{CBO}$	900	V
Collector-emitter voltage	$V_{CEO}$	800	V
Emitter-base voltage	$V_{EBO}$	7	V
Peak collector current	$I_{CP}$	12	A
Collector current	$I_C$	6	A
Base current	$I_B$	3~	A
Collector power dissipation	$T_c=25^\circ\text{C}$	80	W
	$T_a=25^\circ\text{C}$	3	W
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55~+150	$^\circ\text{C}$

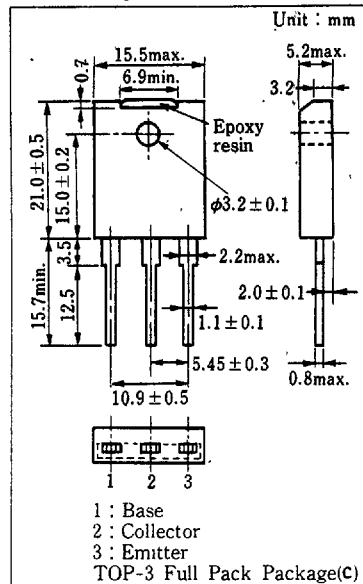
### Electrical Characteristics ( $T_c=25^\circ\text{C}$ )

Item	Symbol	Condition	min.	typ.	max.	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB}=900\text{V}, I_E=0$			100	$\mu\text{A}$
Emitter cutoff current	$I_{EBO}$	$V_{EB}=7\text{V}, I_C=0$			1	mA
Collector-emitter voltage	$V_{CEO(sus)}^*$	$I_C=0.5\text{A}, L=50\text{mH}$	800			V
DC current gain	$h_{FE}$	$V_{CB}=2\text{V}, I_C=3\text{A}$	7			
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=3\text{A}, I_B=0.6\text{A}$			1.5	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=3\text{A}, I_B=0.6\text{A}$			2.5	V
Transition frequency	$f_T$	$V_{CE}=10\text{V}, I_C=0.5\text{A}, f=1\text{MHz}$	15			MHz
Turn-on time	$t_{on}$	$I_C=3\text{A}$			0.5	$\mu\text{s}$
Storage time	$t_{stg}$	$I_{B1}=0.6\text{A}, I_{B2}=-0.6\text{A}$			3	$\mu\text{s}$
Collector current fall time	$t_f$	$V_{CC}=250\text{V}$			0.7	$\mu\text{s}$

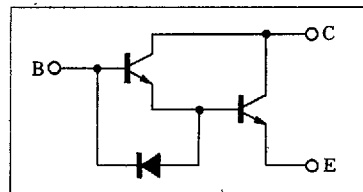
\*  $V_{CEO(sus)}$  Test method

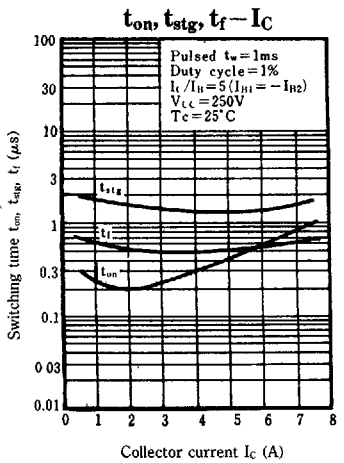
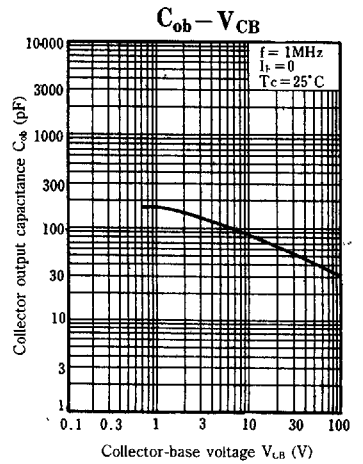
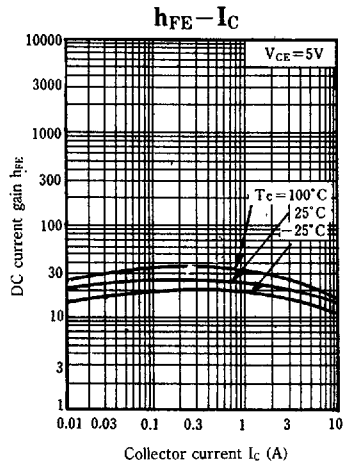
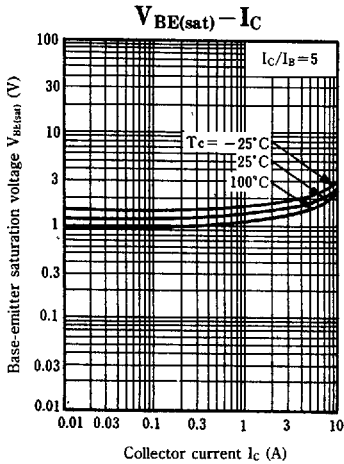
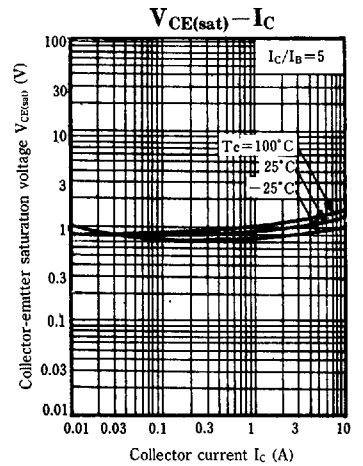
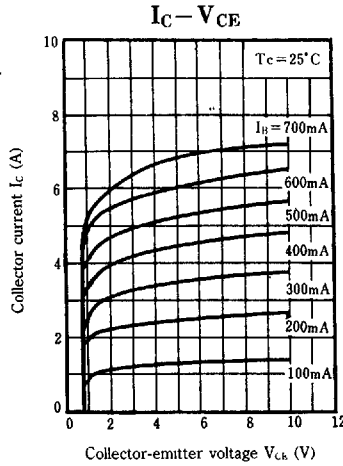
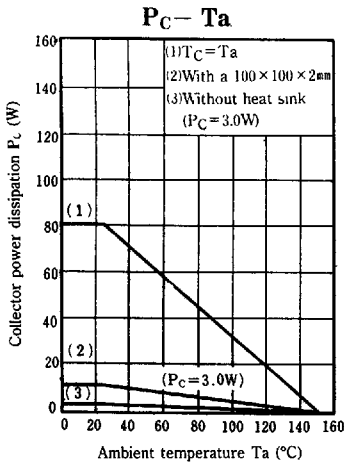


### Package Dimensions

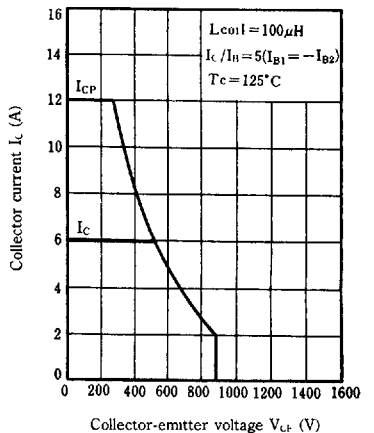
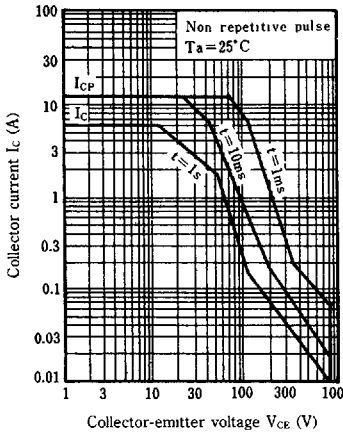


### Inner Circuit





Safety operation area-forward bias (ASO) Safety operation area-reverse bias (ASO)



Measurement circuit of reverse bias ASO.

