# M54410P

#### KEY CONTROLLER FOR TAPE DECK

#### DESCRIPTION

The 54410P is a TTL integrated circuit semiconductor used for controlling the function keys on tape decks.

#### **FEATURES**

- Non-locking function keys can be used
- No need to prevent bounce
- Built-in pull-up resistors for input pins
- STOP mode set when power is switched on
- Wired-AND connection and transistor drive possible for output

#### **APPLICATION**

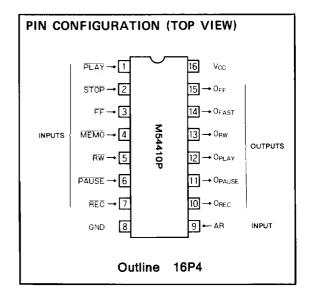
Tape decks, cassette recorders and VTRs

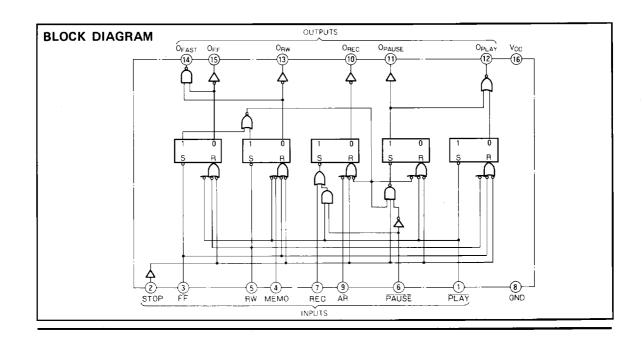
#### **FUNCTION**

The 54410P is configured so that the set and reset pins of the five R-S flip-flops serve as the input pins, and the desired tape mode is controlled by setting these input pins momentarily to low. Non-locking function keys can be used and there is no need to prevent bounce.

The input pins contain pull-up resistors to achieve a high noise margin even under open conditions.

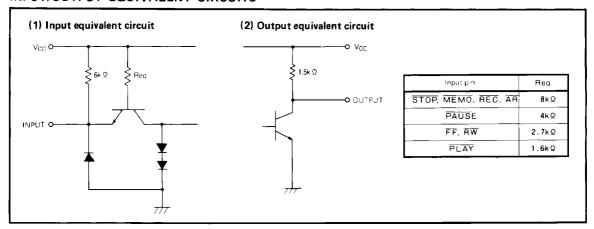
The outputs have a resistive load, and both wired-AND connection and transistor drive are possible. When the power is switched on, the automatic reset function is energized and the mode is set to STOP.





## **KEY CONTROLLER FOR TAPE DECK**

## INPUT/OUTPUT EQUIVALENT CIRCUITS



#### PIN NAMES AND FUNCTIONS

Р	in name	Function				
	STOP	Stop command input pin				
- eg	FF	FF (Fast forward) command input pin				
Function input pin	RW	RW (Rewind) command input pin				
101	REC	REC (Record) command input pin; effective only when it is set to low together with PLAY				
Func	PAUSE	Pause command input pin				
	PLAY	Play start command input pin; when set to low together with REC, recording starts				
Contro-	MEMO	Memory input pin				
input pin	ĀR	Recording prevention input pin				
	OFAST	Output pin that goes high in the FF or RW mode				
_	Off	Output pin that goes high in the FF mode				
Output pin	Onw	Output pin that goes high in the RW mode				
Sutp	OREC	Output pin that goes high in the REC/PLAY ore REC/PAUSE mode				
	OPAUSE	Output pin that goes high in the PAUSE mode				
	OPLAY	Output pin that goes high in the PLAY mode				

# **OPERATIONS WITH EACH INPUT**

Input signal	OFAST	Off	ORW	OREC	OPAUSE	OPLAY	Output mode
STOP	L	L	L	L	L	L	STOP mode
FF	Н	н	L	L	L	L	FF mode
RW	н	L	Н	L	L	L	RW mode
PLAY	L	L	L	L	L	н	PLAY mode
PAUSE	L	L	L,	L	н	L	PAUSE mode
REC PLAY	L	L	L	н	L	н	REC/PLAY mode
REC/PAUSE	L	L	L	н	н	L	REC/PAUSE mode

Note 1. Input signal operates at the Turn fall.

- 2. The output maintains the output state until the next input signal arrives,
- 3. REC/PLAY indicates that REC and PLAY are simultaneously set to low,
- 4. REC/PAUSE indicates that REC and PAUSE are simultaneously set to low.
- MEMO and AR are control input pins, and when MEMO is low, the ORW output is not set to high.
  Should the MEMO = be low when the ORW output is high, the ORW output is set to low.

When  $\overline{A}\overline{A}$  is low, the OREC output is not set to high,

Should the  $\overline{AR}$  be low when the O REC output is high, the O REC output is set to low.



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## **OPERATIONS FROM OUTPOUT MODES**

Present output mode	STOP	FF	RW	PLAY	PAUSE	REC PLAY	REC PAUSE	МЕМО	AR
STOP mode		FF mode	RW mode	PLAY mode	PAUSE	REC/PLAY mode	REC PAUSE mode	STOP mode	STOP mode
FF mode	STOP mode		RW mode	PLAY mode	FFmode	REC/PLAY mode	FFmode	FFmode	FFmode
RW mode	STOP mode	FFmode		PLAY mode	RW mode	REC 'PLAY mode	RW mode	STOP mode	RWmode
PLAY mode	STOP mode	FF mode	RW mode		PAUSE mode	REC/PLAY mode	REC PAUSE mode	PLAY mode	PLAY mode
PAUSE mode	STOP mode	FFmode	RW mode	PLAY mode		REC/PLAY mode	REC PAUSE mode	PAUSE mode	PAUSE mode
REC/PLAY mode	STOP mode	F F mode	RW mode	REC/PLAY mode	REC/PAUSE mode		REC PAUSE	REC PLAY	PLAY mode
REC/PAUSE mode	STOP mode	FFmode	RW mode	REC/PLAY mode	REC <sup>2</sup> PAUSE mode	REC/PLAY mode		REC PAUSE	PAUSE mode

When an input signal enters in the present output mode, the output mode given in the above table is established.

This indicates that the output mode does not change,

## INPUT MULTIPLE PUSH OPERATIONS

A input signal	B input signal	Output mode		
STOP	FF, RW, REC, PAUSE, PLAY	STOP mode		
	RW	STOP mode		
FF	REC, PAUSE	FFmode		
	PLAY	STOPmode		
RW	REC, PAUSE	RWmode		
1744	PLAY	STOP mode		

A input signal	B input signal	Output mode		
	PAUSE	REC/PAUSE mode		
REC	PLAY	REC /PLAY mode		
	PAUSE & PLAY	REC/PAUSE mode		
PAUSE	PLAY	PAUSE mode		

The output modes in the above table are established with multiple input signals A and B. When the multiple push input signal operation has been released, the input signal released last is made effective and the corresponding output mode is established.

(This does not apply to the REC/PAUSE and REC/PLAY modes.)

## **OUTPUT MODE WITH POWER SWITCH-ON**

When the power is switched on, all the outputs are set to low and the STOP mode is established.

# ABSOLUTE MAXIMUM RATINGS ( $T_a = -20 \sim +75 \, \circ$ , unless otherwise noted)

Symbol	Parameter	Condition	Ratings	Unit
Vcc	Supply voltage		7	V
Vı	Inptu voltage		Vcc	.V
Vo	Output voltage	When output is "high"	V <sub>CC</sub>	V
Pd	Power dissipation		500	mW
Topr	Operating temperature		- 20 + 75	·······································
Tstg	Storage temperature		55 +125	,C

# **RECOMMENDED OPERATING CONDITIONS** ( $T_a = -20 \sim +75 \, ^{\circ}$ C, unless otherwise noted)

Symbol	Paramet					
5,55	. Taranci	Min	Тур	Max	Unit	
Vcc	Supply voltage		4.5	5	5.5	٧
1	Low-level output current	V <sub>CC</sub> = 5V ± 10%	_		10	
lor _	V <sub>CC</sub> = 5V ± 20%			7.6	mΑ	
	High-level output current	V <sub>CC</sub> = 5V ± 10%			1.6	
≀он	V <sub>CC</sub> = 5V - 20%				1.4	mΑ



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## **ELECTRICAL CHARACTERISTICS** ( $T_a = -20 \sim +75 \, ^{\circ}$ C, unless othersie noted)

0	Parameter			Limits			Unit
Symbol			Test conditions	Min	Тур*	Max	Unit
VIH	High-level inptu voltage			2			٧
VIL	Low-level input voltage					0.8	٧
V <sub>IC</sub>	Input clamp voltage		V <sub>CC</sub> =4.5V, I <sub>IC</sub> =-12mA		-1.0	-1.5	V
V <sub>I</sub> (open)	Input open voltage		V <sub>CC</sub> =4.5V. I <sub>I</sub> =0mA	3.2			V
Vон	High-level output voltage		$V_{CC} = 4.5V$ , $V_{IH} = 2V$ , $V_{IL} = 0.8V$ $V_{OH} = -0.4mA$	2.9	4.3		V
Vol	Low-level output voltage		V <sub>CC</sub> = 4.5V, V <sub>IH</sub> = 2V, V <sub>IL</sub> = 0.8V I <sub>OL</sub> = 10mA		0.25	0.4	٧
		STOP, MEMO, REC, AR inpu	V <sub>CC</sub> =5.5V, V <sub>I</sub> =0V		-1.4	-2.3	mA.
	Low-level input current	PAUSE input pin			-1.9	-3.3	
IIL		FF, RW input pins			-2.4	-4.2	ША
		PLAY input pin			-3.5	-6.0	
lox	High-level output current		V <sub>CC</sub> =4.5V, V <sub>O</sub> =0.9V	-1.6	-2.6		mA
lcc	Circuit current		V <sub>CC</sub> =5.5V		54	90	mA

<sup>\* :</sup> Typical values are at Ta=25℃.

#### **APPLICATION EXAMPLE**

