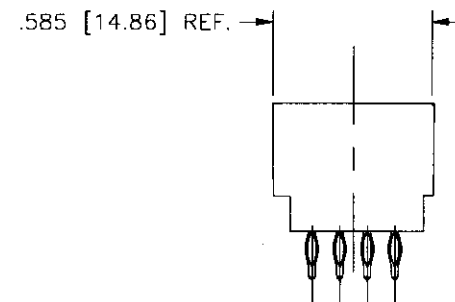
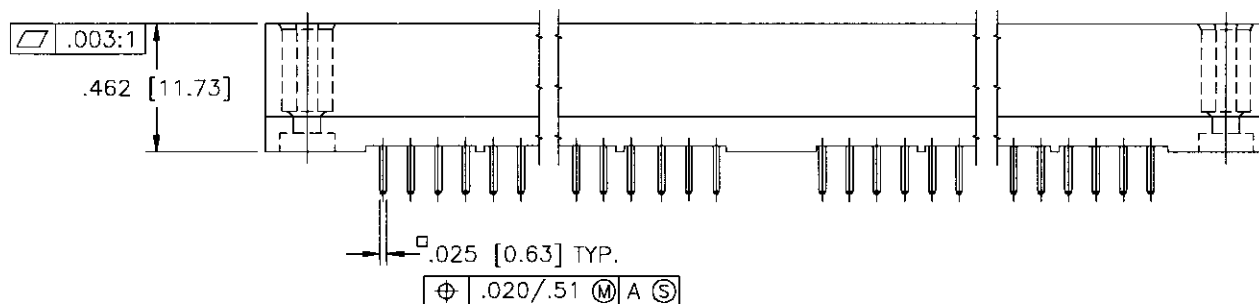
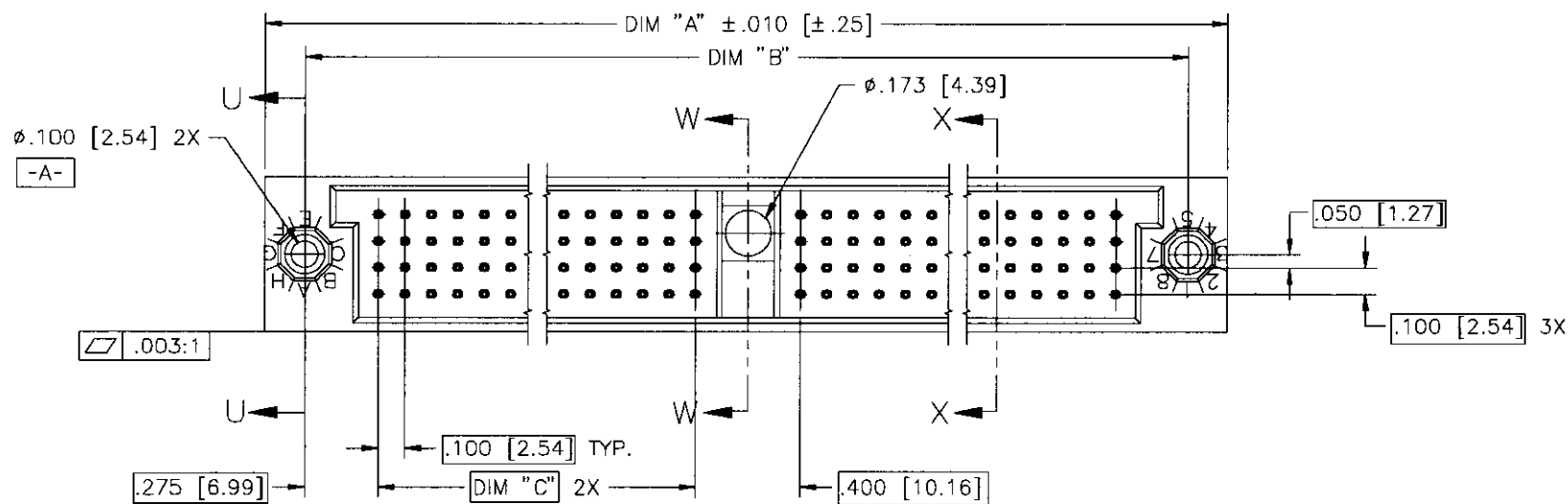
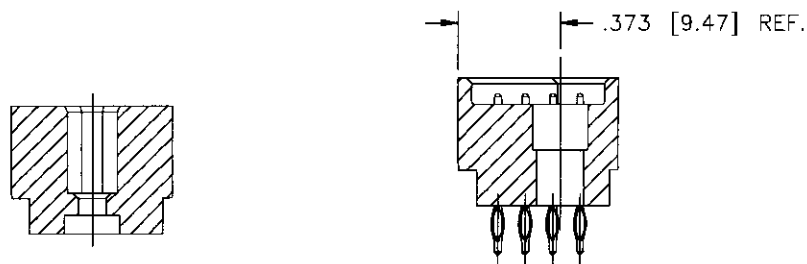


50728-XXXXX

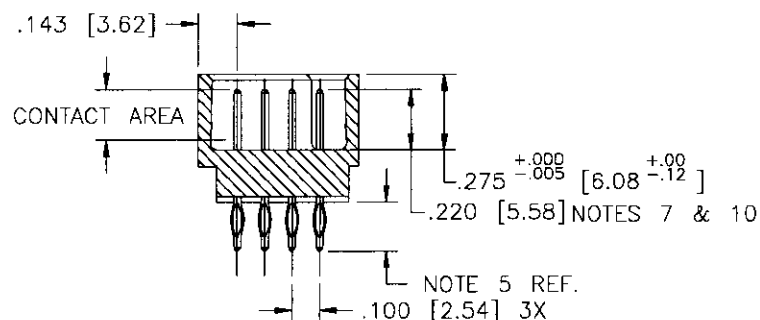
[illegible]

PRODUCT NUMBER	DIM.	LENGTH FORMULAS
50728-XXXX	DIM A	.100/2.54 x NO. OF COLUMNS + 1.05/26.7
	DIM B	.100/2.54 x NO. OF COLUMNS + .750/19.15
	DIM C	.100/2.54 x NO. OF COLUMNS DIVIDED BY 2 - .100/2.54

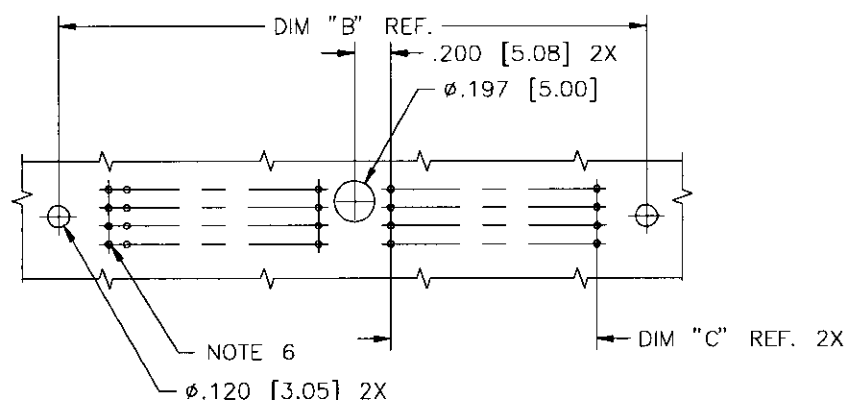


SECTION U-U

SECTION W-W



SECTION X-X



RECOMMENDED PCB LAYOUT
ALL HOLES LOCATED ON .100/2.54 GRID
UNLESS OTHERWISE SPECIFIED

NOTES:

- PRODUCT NUMBERING CODE:
50728 - X XXX X
TAIL LENGTH (NOTE 5)
NO OF POSITIONS (NOTE 2)
PLATING (NOTE 4)
BASE NUMBER
- THIS PRODUCT IS CONFIGURED IN SIZES OF 20 THRU 140 COLUMNS IN INCREMENTS OF TWO. i.e.: 20,22,24,26...138,140.
- MATERIALS:
HOUSING- GLASS FILLED PPS.
PIN- PHOS BRONZE
- PLATING OPTIONS:
A = 50u" Ni UNDER ALL WITH 30u" Au ON CONTACT AREA, SnPb ON PCB INTERFACE AND Au FLASH ON ALL OTHER AREAS.
B = 50u" Ni UNDER ALL WITH 30u" GXT ON CONTACT AREA, SnPb ON PCB INTERFACE AND Au FLASH ON ALL OTHER AREAS.
C = 50u" Ni UNDER ALL WITH 30u" Au ON CONTACT AREA, SnPb ON PCB INTERFACE, 30u" Au ON TAIL SIDE CONTACT AREA AND Au FLASH ON ALL OTHER AREAS.
D = 50u" Ni UNDER ALL WITH 30u" GXT ON CONTACT AREA, SnPb ON PCB INTERFACE, 30u" GXT ON TAIL SIDE CONTACT AREA AND Au FLASH ON ALL OTHER AREAS.
- TAIL LENGTH OPTIONS:
FOR PLATING OPTIONS "A" & "B" ONLY:
A = .177/4.5
G = .250/6.35
FOR PLATING OPTIONS "C" & "D" ONLY:
C = .533/13.54
D = .733/18.62
- Ø.0453/1.151 DRILLED HOLE PLATED WITH .0003/.007 MIN. SnPb OVER .001/.03 TO .003/.08 Cu. PLATING TO ACHIEVE A Ø.040±.003/1.02±.08 HOLE.
- TO ASSURE PROPER INSTALLATION : BERG TOOLING AS SPECIFIED IN INSTRUCTION MANUAL 155915-001 MUST BE USED. PIN MATING LENGTH WILL BE ±.002/.05 AFTER INSTALLATION.
- BERG'S NAME, P/N, AND DATE CODE TO APPEAR ON THIS SURFACE AS PER BUS-12-108.
- FOR DOCUMENTATION MENU, SEE BERG BUS-01-057.
- REFERENCE 50808 FOR F.MLB. AVAILABLE OPTIONS AND WORKSHEET.
- MANUFACTURE AS PER BUS-24-004.

REVISIONS				SHEET INDEX	ISSUE SH NO.	D	MAY	NOTE 3	DR	P. PARSONS	8/2/93	CUSTOMER COPY	TITLE
REV	ECN/DDR#	BY	DATE										
D					2								HPC-II HEADER ASSEMBLY
													4 ROW, IBM SPEC. GUIDE PIN
													SCALE SIZE DWG. NO.
													3:1 C 50728
													2 of 2