

Analog Multiplexers

Part Number	Max $f_{DS(on)}$ (Ω)	Max $I_{S(on)}$ (mA)	Analog Range (V)	Transition Time (μs)	Typical Charge Injection (pC)	Chip Logic Regulator	Max Power Consumption (mW)	Package	Comments
4-Channel Single-Ended/2-Channel Differential									
DG534A	90	20	-5 to 8	0.3	-70	-	37.9	J, N, P	Video, latches
4-Channel Differential									
DG538A	90	20	-5 to 8	0.3	-70	-	37.9	J, N, P	Video, latches
DG409	100	1	-15 to 15	0.25	20	Yes	0.9	J, K, Y, Z	High speed
DG429	100	1	-15 to 15	0.25	4	Yes	1.58	J, K, N, W	Latchable, high speed
DG509A	400	10	-15 to 15	1.0	6	Yes	58.5	J, K, Y, Z	-
DG529	400	10	-15 to 15	1.0	4	Yes	60	J, K	Latchable
DG459	1200	2	-15 to 15	0.5	-	Yes	3	J, K, Z	Fault protected
8-Channel Single-Ended									
DG538A	90	20	-10 to 10	0.3	-70	-	59.5	J, N, P	Video, latches
DG408	100	1	-15 to 15	0.25	20	Yes	30	J, K, Y, Z	High speed
DG428	100	1	-15 to 15	0.25	4	Yes	1.58	J, K, N	Latchable, high speed
DG508A	400	10	-15 to 15	1.0	6	Yes	58.5	J, K, P, X, Y, Z	-
DG528	400	10	-15 to 15	1.0	4	Yes	60	J, K	Latchable
DG458	1200	2	-10 to 10	0.5	-	Yes	3	J, K, Z	Fault protected
8-Channel Differential									
DG407	100	1	-15 to 15	0.3	15	Yes	0.5	J, K, N, Z	High speed
DG507A	400	5	-15 to 15	1.0	6	Yes	58.5	J, K, P, Z	-
16-Channel Single-Ended									
DG535	90	10	0 to 10	0.3	-35	-	0.75	J, P	Low power, video
DG536	90	10	0 to 10	0.3	-35	-	0.75	M, N	Low power, video
DG406	100	1	-15 to 15	0.3	15	Yes	0.5	J, K, N, W, Z	Low power, fast
DG506A	400	10	-15 to 15	1.0	6	Yes	58.5	J, K, N, P, Z	-
Specials									
DG485	85	20	-15 to 15	0.2	17	-	0.11	J, N, Z	Ch. ser. control
DG884	90	20	-5 to 8	0.02	-100	-	92.5	M, N	8 x 4 video crosspt
DG894	100	10	0 to 4	0.01	N/A	Yes	136	J, W	RGB video mux

A = Metal Case
P = Sidebrazed

J = Plastic DIP
X = Flatpack

K = CerDIP
Y = SOIC

N = PLCC
Z = LCC