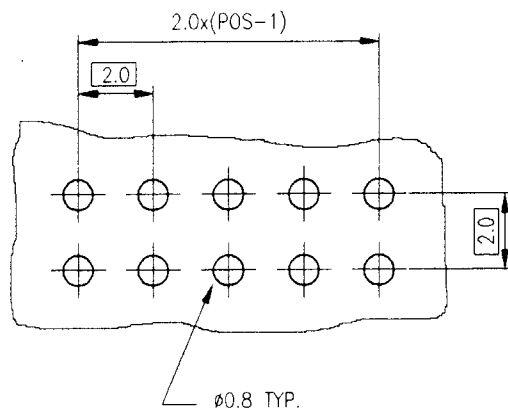


mat'l. code				tolerances unless otherwise specified			CUSTOMER COPY	<div>BERG ELECTRONICS</div>		
ltr	ecn no	dr	date	linear						
A	K50022	JWY	8/14/95		.X ± .3		projection 	title 2.0 MINITEK RECEPTACLE		
B	K60027	JWY	3/20/96		.XX ± .13					
C	K60082	JWY	10/31/96	angles	.XXX ± .051					
D	K80019	BJK	6/24/98	dr	0° ± 2°		mm 	product family		code
				engr	JW YANG	8/14/95		MINITEK		213
				chr	WW LEE	8/14/95		size dwg no		sheet
				appd	SH SON	8/14/95		A4		1 of 2
sheet index	revision sheet	D	D				scale	1:5		
		1	2							



RECOMMENDED PCB LAYOUT

NOTE

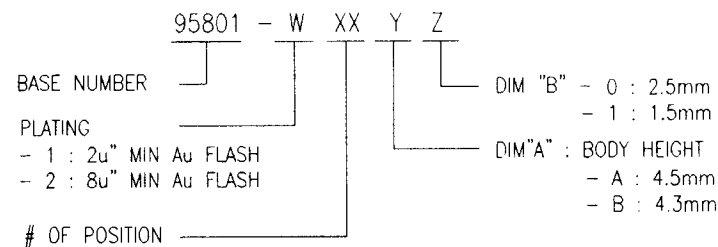
1. MATERIAL

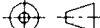
- HOUSING : POLYESTER, UL 94V-0
- TERMINAL : PHOSPHOR BRONZE

2. ANY NUMBER OF POSITION FROM 4(2X2) THRU 40(2X20)

3. GENERAL TOLERANCE : ± 0.25

4. PART NUMBER CONSIST OF :



mat'l. code				tolerances unless otherwise specified			CUSTOMER COPY		BERG ELECTRONICS			
ltr	ecn no	dr	date	linear	.X $\pm .3$			projection 	title 2.0 MINITEK RECEPTACLE			
D					.XX $\pm .13$							
					.XXX $\pm .051$							
				angles	0° $\pm 2'$							
				dr	JW YANG	8/14/95	mm ↔	product family		MINITEK		code
				engr	WW LEE	8/14/95		size		dwg no		213
				chr	WW LEE	8/14/95		scale		A4		sheet
				appd	SH SON	8/14/95	N/S			95801		2 of 2
sheet	revision	D										
index	sheet	2										